APD Semiconductor, Inc.

Super Barrier Rectifier TM

Using state-of-the-art SBR IC process technology, the following features are made possible in a single device:

Major ratings and characteristics

| Characteristics | Values | Units |
|--|------------|--------|
| $I_{F(AV)}$ Rectangular Waveform | 40 | А |
| V _{RRM} | 45 | V |
| V _F @20A, Tj=125 ⁰ C | 0.47 | V, typ |
| Tj (operating/storage) | -65 to 150 | °C |

ELECTRICAL:

- * Low Forward Voltage Drop
- * Reliable High Temperature Operation
- * Super Barrier Design
- * Softest, fast switching capability
- * 150°C Operating Junction Temperature

Maximum Ratings and Electrical Characteristics

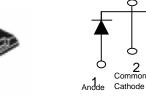
(at 25°C unless otherwise specified) SYMBOL UNITS DC Blocking Voltage V_{RM} Working Peak Reverse Voltage 45 VRWM Volts Peak Repetitive Reverse Voltage V_{RRM} Average Rectified Forward Current (Rated V_R-20Khz Square Wave) - 50% duty I_0 40 Amps cycle Peak Forward Surge Current - 1/2 60hz 280 I_{FSM} Amps Peak Repetitive Reverse Surge Current 2 Amps RRM (2uS-1Khz) Instantaneous Forward Voltage (per leg) Max Тур $I_{\rm F} = 20$ A; $T_{\rm J} = 25^{\circ}$ C V_{F} 0.55 Volts ---I_F = 20A; T_J = 125°C 0.50 ---Maximum Instantaneous Reverse Current at Тур Max Rated V_{RM} I_R^* 0.5 --mΑ $T_{J} = 25^{\circ}C$ ---100 T_J = 125^oC Maximum Rate of Voltage Change dv/dt 10,000 V/uS (at Rated V_R) Maximum Thermal Resistance JC (per leg) 2 °C/W $R\theta_{JC}$ Package = TO-247 ТJ °C Operating and Storage Junction Temperature -65 to +150

* Pulse width < 300 uS, Duty cycle < 2%

Device optimized for low forward voltage drop to maximize efficiency in Power Supply applications

MECHANICAL:

* Molded Plastic TO-247 package



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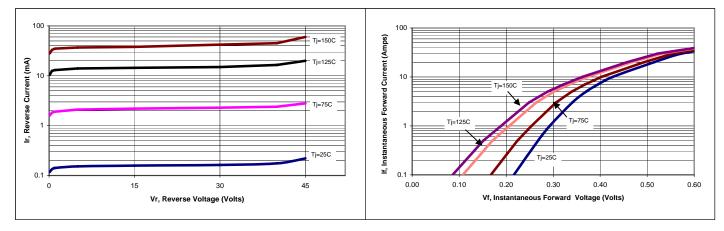


Figure 1: Typical Reverse Current (per leg)

Figure 2: Typical Forward Voltage (per leg)

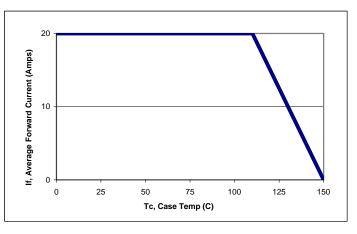


Figure 3: Current Derating, Case (per leg)

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