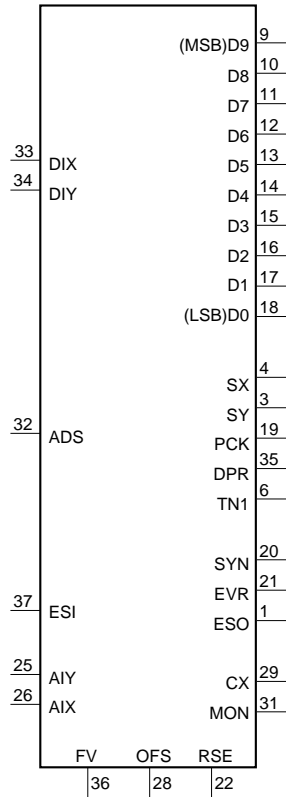
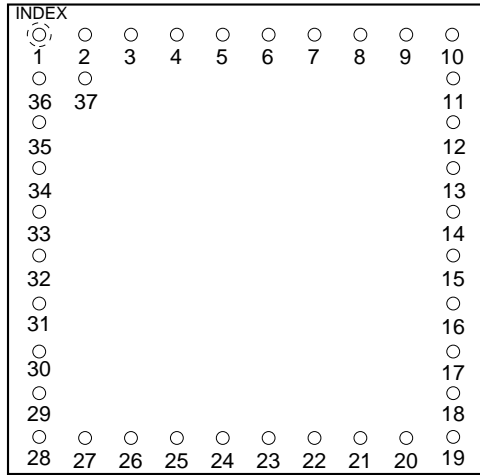


8- OR 10-BIT SERIAL-TO-PARALLEL CONVERTER
—BOTTOM VIEW—



INPUT

- ADS : SERIAL DATA SELECT (H : DIGITAL L : ANALOG)
- AIX, AIY : EQUALIZER
- DIX, DIY : SERIAL DATA
- ESI : PLL SIGNAL
- OFS : AGC OFFSET ADJ.
- FV : VCO FREQ. ADJ.
- RSE : VCO RANGE SELECT (H : HIGH RANGE)

OUTPUT

- CX : EQUALIZER DETECT (L : NO INPUT)
- D9 - D0 : PARALLEL DATA
- DPR : SERIAL DATA DETECT (L : NO INPUT)
- ESO : TEST NODE PLL ERROR SIGNAL
- EVR : REFERENCE VOLTAGE FOR PARALLEL
- MON : EQUALIZER MONITOR
- PCK : PARALLEL CLOCK
- SX, SY : SERIAL DATA
- SYN : TRS DETECT
- TN1 : TEST TERMINAL

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	ESO	11	O	D7	21	O	EVR	31	O	MON
2	—	GND	12	O	D6	22	I	RSE	32	I	ADS
3	O	SY	13	O	D5	23	—	VEE3	33	I	DIX
4	O	SX	14	O	D4	24	—	GND	34	I	DIY
5	—	GND	15	O	D3	25	I	AIY	35	O	DPR
6	O	TN1	16	O	D2	26	I	AIX	36	I	FV
7	—	VEE1	17	O	D1	27	—	GND	37	I	ESI
8	—	VEE2	18	O	D0 (LSB)	28	I	OFS			
9	O	D9 (MSB)	19	O	PCK	29	O	CX			
10	O	D8	20	O	SYN	30	—	GND			