

RoHS Compliant Product  
A suffix of "-C" specifies halogen & lead-free

### FEATURES

- Reliable and Rugged
- Green Device Available
- ESD Protected: 2kV

### APPLICATION

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

### MARKING

G5

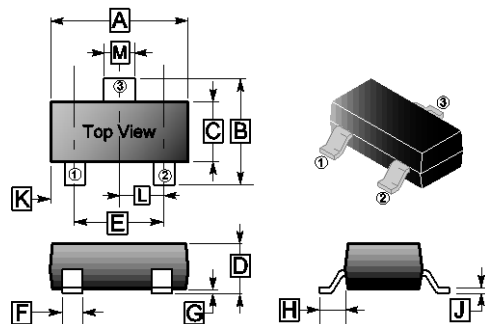
### PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-523	3K	7 inch

### ORDER INFORMATION

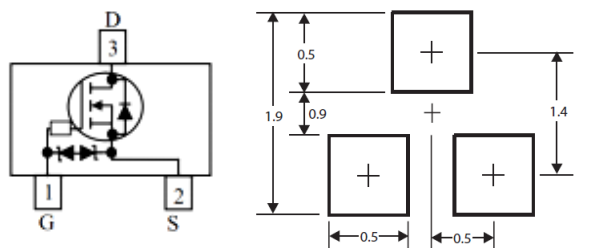
Part Number	Type
SCG2035-C	Lead (Pb)-free and Halogen-free

### SOT-523



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	1.50	1.70	G	-	0.10
B	1.45	1.75	H	0.55 REF.	
C	0.70	0.90	J	0.08	0.20
D	0.60	0.90	K	-	
E	0.90	1.10	L	0.50 TYP.	
F	0.15	0.35	M	0.25	0.40

### Mounting Pad Layout



\*Dimensions in millimeters

### ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{DS}$	20	V	
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V	
Continuous Drain Current <sup>1</sup> @ $V_{GS}=4.5V$	$T_A=25^\circ C$	0.65	A	
	$T_A=70^\circ C$	0.52		
Pulsed Drain Current <sup>3</sup>	$I_{DM}$	1.6	A	
Power Dissipation	$T_A=25^\circ C$	$P_D$	0.3	W
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ C$	
Thermal Resistance Rating				
Thermal Resistance from Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	417	$^\circ C/W$	
Thermal Resistance from Junction-Ambient <sup>2</sup>		833		

**ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C unless otherwise specified)

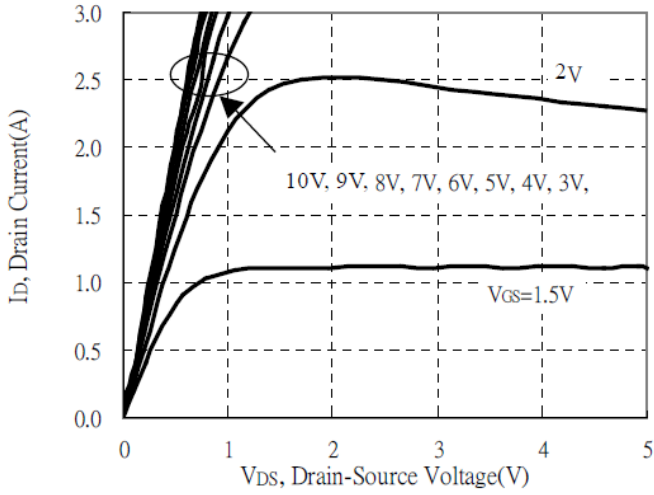
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =250μA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.45	-	1	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	
Forward Transfer conductance	g <sub>fs</sub>	-	2	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =0.6A	
Gate-Body Leakage Current	I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> = ±12V	
Drain-Source Leakage Current	I <sub>DSS</sub>	T <sub>J</sub> =25°C	-	-	1	μA	V <sub>GS</sub> =0, V <sub>DS</sub> =16V
		T <sub>J</sub> =70°C	-	-	25		
Static Drain-Source On-Resistance <sup>4</sup>	R <sub>DS(ON)</sub>	-	-	350	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.55A	
		-	-	700		V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.45A	
		-	-	950		V <sub>GS</sub> =1.8V, I <sub>D</sub> =0.35A	
Total Gate Charge	Q <sub>g</sub>	-	1.3	-	nC	I <sub>D</sub> =0.5A V <sub>DS</sub> =15V V <sub>GS</sub> =4.5V	
Gate-Source Charge	Q <sub>gs</sub>	-	0.5	-			
Gate-Drain ("Miller") Change	Q <sub>gd</sub>	-	0.1	-			
Turn-on Delay Time	T <sub>d(on)</sub>	-	2.6	-	nS	V <sub>DS</sub> =10V I <sub>D</sub> =0.5A V <sub>GS</sub> =10V R <sub>G</sub> =1Ω	
Rise Time	T <sub>r</sub>	-	16	-			
Turn-off Delay Time	T <sub>d(off)</sub>	-	29.8	-			
Fall Time	T <sub>f</sub>	-	11	-			
Input Capacitance	C <sub>iss</sub>	-	64	-	pF	V <sub>DS</sub> =10V V <sub>GS</sub> =0 f=1MHz	
Output Capacitance	C <sub>oss</sub>	-	17	-			
Reverse Transfer Capacitance	C <sub>rss</sub>	-	20	-			
<b>Source-Drain Diode</b>							
Continuous Source Current <sup>1</sup>	I <sub>S</sub>	-	-	0.65	A		
Pulsed Source Current <sup>3</sup>	I <sub>SM</sub>	-	-	1.6	A		
Forward On Voltage <sup>4</sup>	V <sub>SD</sub>	-	-	1.2	V	V <sub>GS</sub> =0, I <sub>S</sub> =0.15A	
Reverse Recovery Time	t <sub>rr</sub>	-	4.9	-	nS	I <sub>F</sub> =0.5A, dI/dt=100A/μs, T <sub>J</sub> =25°C	
Reverse Recovery Charge	Q <sub>rr</sub>	-	1	-	nC		

Notes:

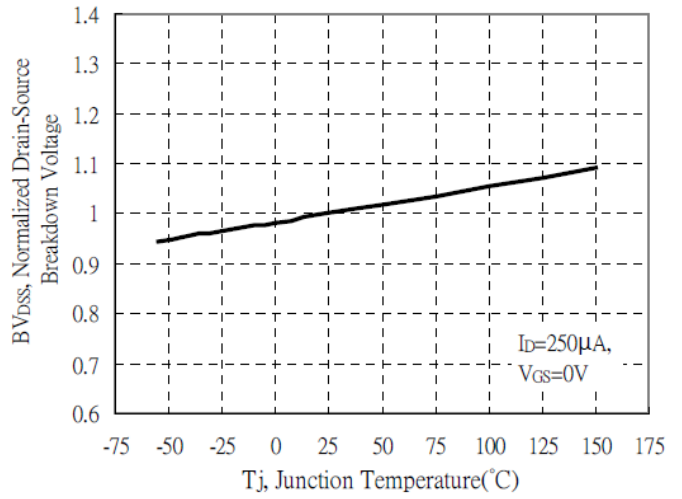
1. Surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2oz copper.
2. When mounted on Min. copper pad.
3. The power dissipation is limited by 150°C junction temperature.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

**TYPICAL CHARACTERISTIC CURVES**

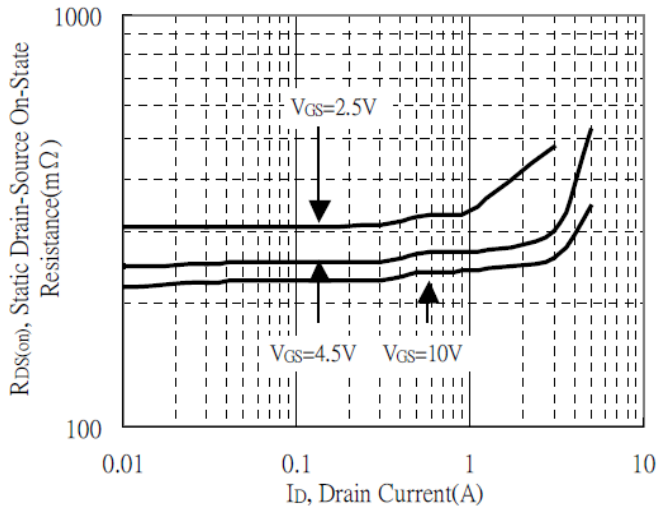
Typical Output Characteristics



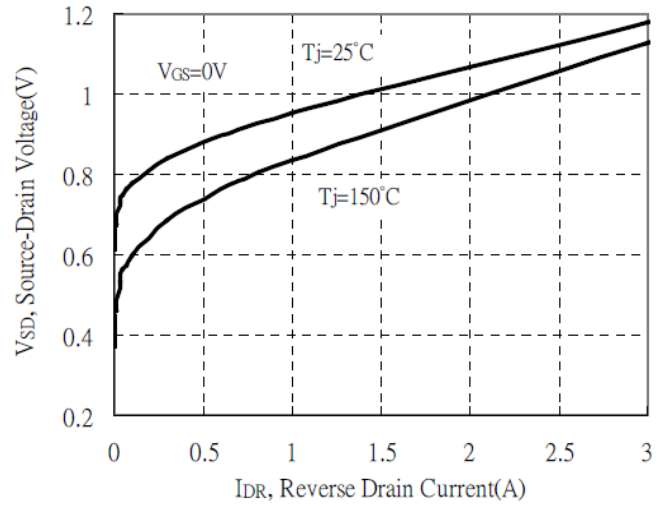
Brekdown Voltage vs Ambient Temperature



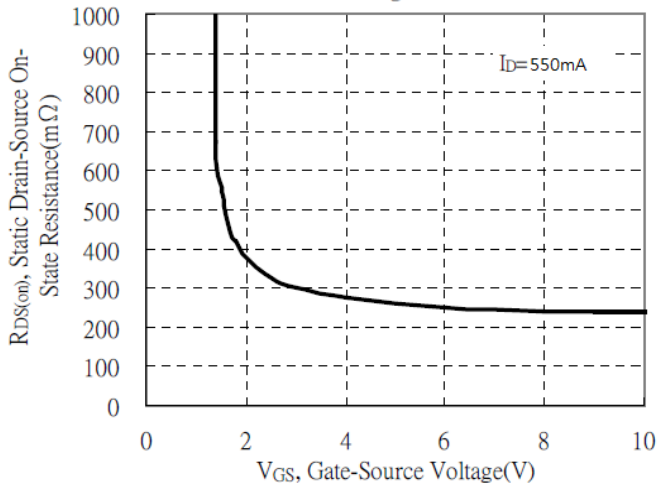
Static Drain-Source On-State resistance vs Drain Current



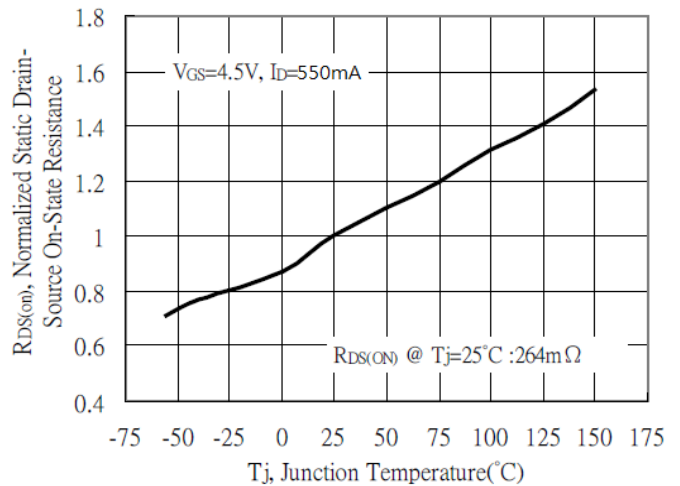
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

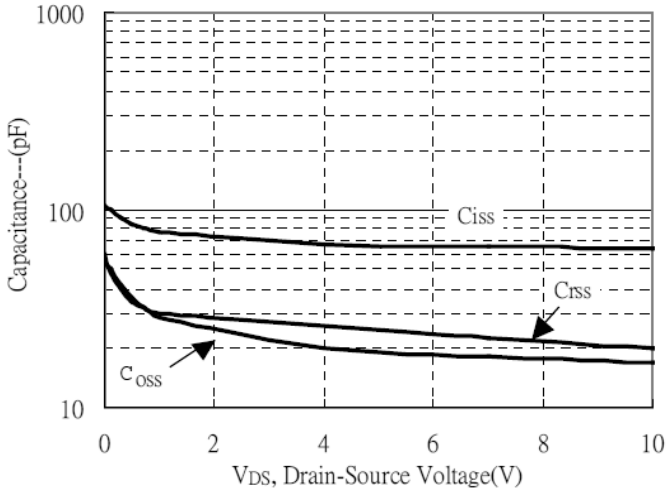


Drain-Source On-State Resistance vs Junction Temperature

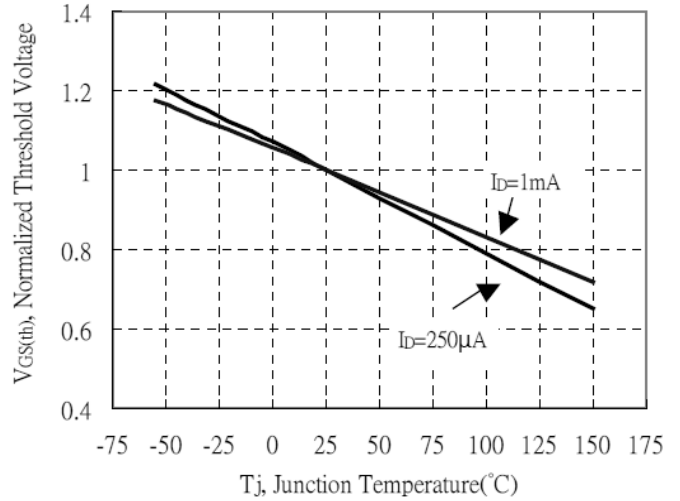


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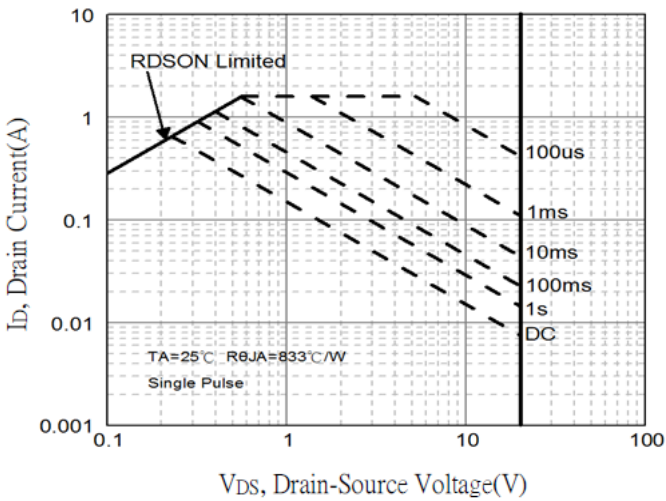
Capacitance vs Drain-to-Source Voltage



Threshold Voltage vs Junction Temperature



Maximum Safe Operating Area



Gate Charge Characteristics

