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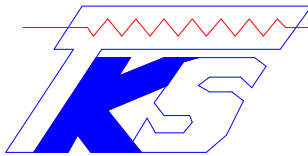
SPECIFICATION FOR APPROVAL

CUSTOMER	C-CUBEE
MODEL NO.	SCK-102
PART NO.	SCK08102MIA
APPLICATION	
CUSTOMER	
MODEL NO.	6322AQ9189F
DATE	Apr. 26, 2004

FOR CUSTOMER APPROVAL	CHECKED BY
	Ouyang
	APPROVED BY
	Daviewei



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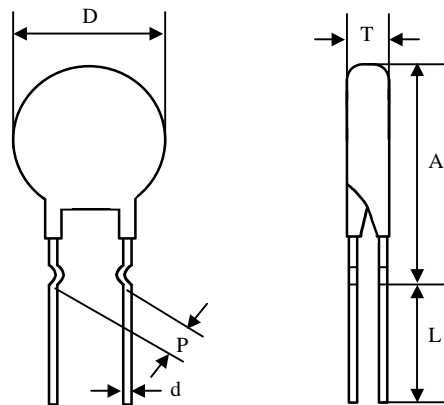
Specification of NTC Thermistor for Surge Current Suppression

PART NO. SCK08102MIACUSTOMER P/N. 6322AQ9189F

1. Part number code

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
Product Code		Body Size			Zero Power Resistance at 25 (R ₂₅)		Max Steady State Current at 25		Tolerance of R ₂₅		Appearance					
SCK	Thinking NTC Thermistor SCK Type		05	5mm	0R5	0.5	X3	0.3A	L	±15%	S	Straight Lead				
			08	8mm	2R5	2.5	2X	2.5A	M	±20%	I	Inner Kink Lead				
			10	10mm	08	8	8	8A	N	±25%	F	Y Kink Lead				
			13	13mm	20	20	10	10A			SC	Straight Cut Lead				
			15	15mm	120	120					:	:				

2. Dimensions



(unit:mm)

Disc size	D max.	P nor.	d nor.	A max.	T max.
φ 08	9.5	5.0±0.8	0.8±0.02	16	5

2-1 Material of coating : Silicone resin

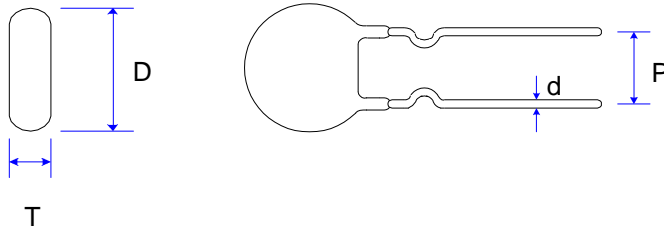
2-2 Material of Leads : Tinned copper wires

2-3 Color of coating : Green

2-4 Print of Marking : SCK 102

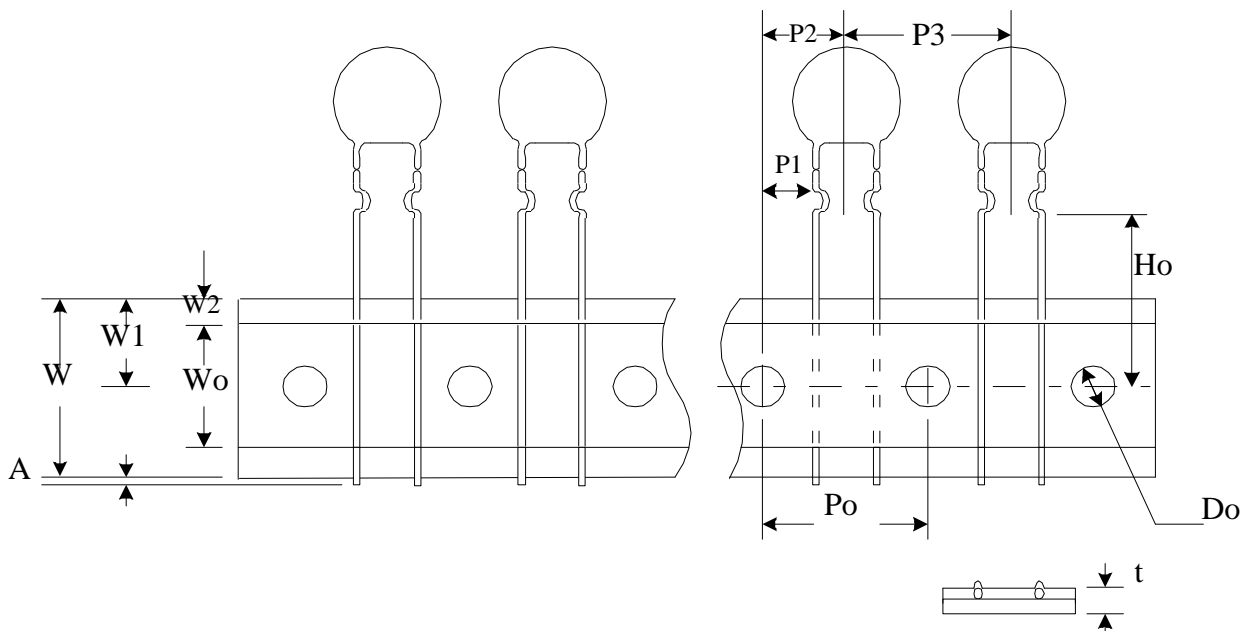
TAPED TYPE

1. Dimensions



ITEM	D	L	d	P	T
MAX.	9.5		0.82	5.8	5
MIN.			0.78	4.2	

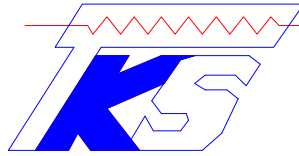
(Unit : mm)



ITEM	Po	P3	P1	P2	Ho	Wo	W1	W2	W	A	Do	t
Nor.	12.7	12.7	3.45	6.35	16	12	9	3	18	1	4	0.6
Tol.	±0.5	±0.5	±0.7	±1.3	±0.5	±1	±0.5	±1	±0.5	Max.	±0.2	±0.2

(Unit : mm)

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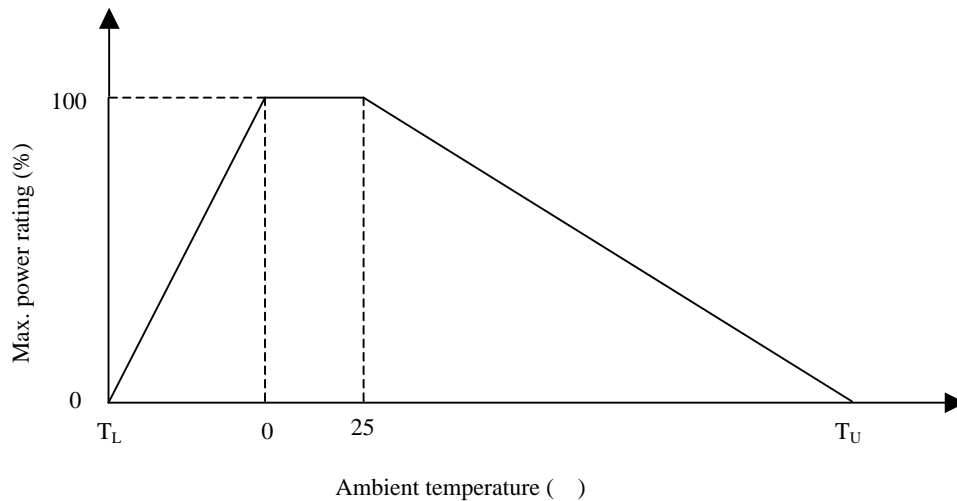


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3. Characteristics

Part no.	Zero power resistance at 25 (Ω)	Max. Steady State current at 25 (A)	Max. power rating at 25 (W)	Thermal dissipation constant (mW/)	Thermal time Constant (sec.)	Capacitance at 240Vac (μF)	Operating temperature range ()
SCK08102MIA	10±20%	2	2.3	16	38	470	-40 ~+170

4. Maximum power rating (Pmax)



Note: T_L = Minimum Temp. of Operating Temp. Range ()

T_U = Maximum Temp. of Operating Temp. Range ()

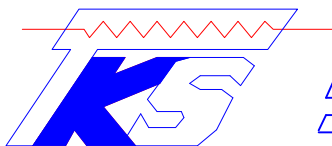
5. Approvals



* UL 1434 recognized (File # E138827)



* CSA recognized (File # 97495)

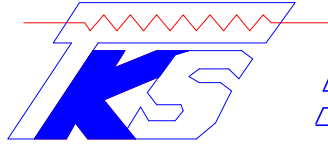


6. Reliability Test

Item	Standard	Test conditions / Methods	Specifications															
Tensile Strength of Terminals	IEC68-2-21	Gradually applying the force specified below to each terminal and keeping the unit fixed for 10±1 sec. <table border="1"> <thead> <tr> <th>Terminal diameter (mm)</th> <th>Force (kg)</th> </tr> </thead> <tbody> <tr> <td>0.5<d 0.8</td> <td>1.0</td> </tr> <tr> <td>0.8<d 1.25</td> <td>2.0</td> </tr> </tbody> </table>	Terminal diameter (mm)	Force (kg)	0.5<d 0.8	1.0	0.8<d 1.25	2.0	No visible damage									
Terminal diameter (mm)	Force (kg)																	
0.5<d 0.8	1.0																	
0.8<d 1.25	2.0																	
Bending Strength of Terminals	IEC68-2-21	Hanging the force specified below to each terminal and gradually bending each terminal by 90° in one direction, then 90° in the opposite direction, and again back to the origin. <table border="1"> <thead> <tr> <th>Terminal diameter (mm)</th> <th>Force (kg)</th> </tr> </thead> <tbody> <tr> <td>0.5<d 0.8</td> <td>0.5</td> </tr> <tr> <td>0.8<d 1.25</td> <td>1.0</td> </tr> </tbody> </table>	Terminal diameter (mm)	Force (kg)	0.5<d 0.8	0.5	0.8<d 1.25	1.0	No visible damage									
Terminal diameter (mm)	Force (kg)																	
0.5<d 0.8	0.5																	
0.8<d 1.25	1.0																	
Solderability	IEC68-2-20	235 ±5 , 2 ± 0.5 sec	At least 95% of terminal electrode is covered by new solder															
Resistance to Soldering Heat	IEC68-2-20	350 ±5 , 3.5 ±0.5 sec	No visible damage R/R 10 %															
High Temperature Storage	IEC68-2-2 UL1434	Tmax ±5 x 1000HRS	No visible damage R/R 20 %															
Damp Heat	IEC68-2-3 UL1434	40 ± 2 , 9 0 ~ 95 % RH , 1000 ±24 HRS	No visible damage R/R 20 %															
Thermal Shock	IEC68-2-14 UL1434	The thermal shock conditions shown below shall be repeated 5 cycles <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature ()</th> <th>Period (minutes)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Tmin±5</td> <td>30±3</td> </tr> <tr> <td>2</td> <td>Room temperature</td> <td>5±3</td> </tr> <tr> <td>3</td> <td>Tmax±5</td> <td>30±3</td> </tr> <tr> <td>4</td> <td>Room temperature</td> <td>5±3</td> </tr> </tbody> </table>	Step	Temperature ()	Period (minutes)	1	Tmin±5	30±3	2	Room temperature	5±3	3	Tmax±5	30±3	4	Room temperature	5±3	No visible damage R/R 20 %
Step	Temperature ()	Period (minutes)																
1	Tmin±5	30±3																
2	Room temperature	5±3																
3	Tmax±5	30±3																
4	Room temperature	5±3																
Life Test	CNS5550	25 ±5 , Imax. x 1000 HRS	No visible damage R/R 20 %															
Endurance	UL1434	25 ±5 , Imax. , CT , 1min ON / 5 min OFF x1000 cycles CT=Capacitance at 240Vac	No visible damage R/R 20 %															
Insulation test	MIL-STD-202F-Method 302	1000 VDC 1 min	No visible damage 500 MΩ															

Products have been tested at Thinking Electronic Industrial Co., Ltd. Laboratory recognized by UL (Underwriters Laboratories Inc.) under CTDTP (Client Test Data Program).

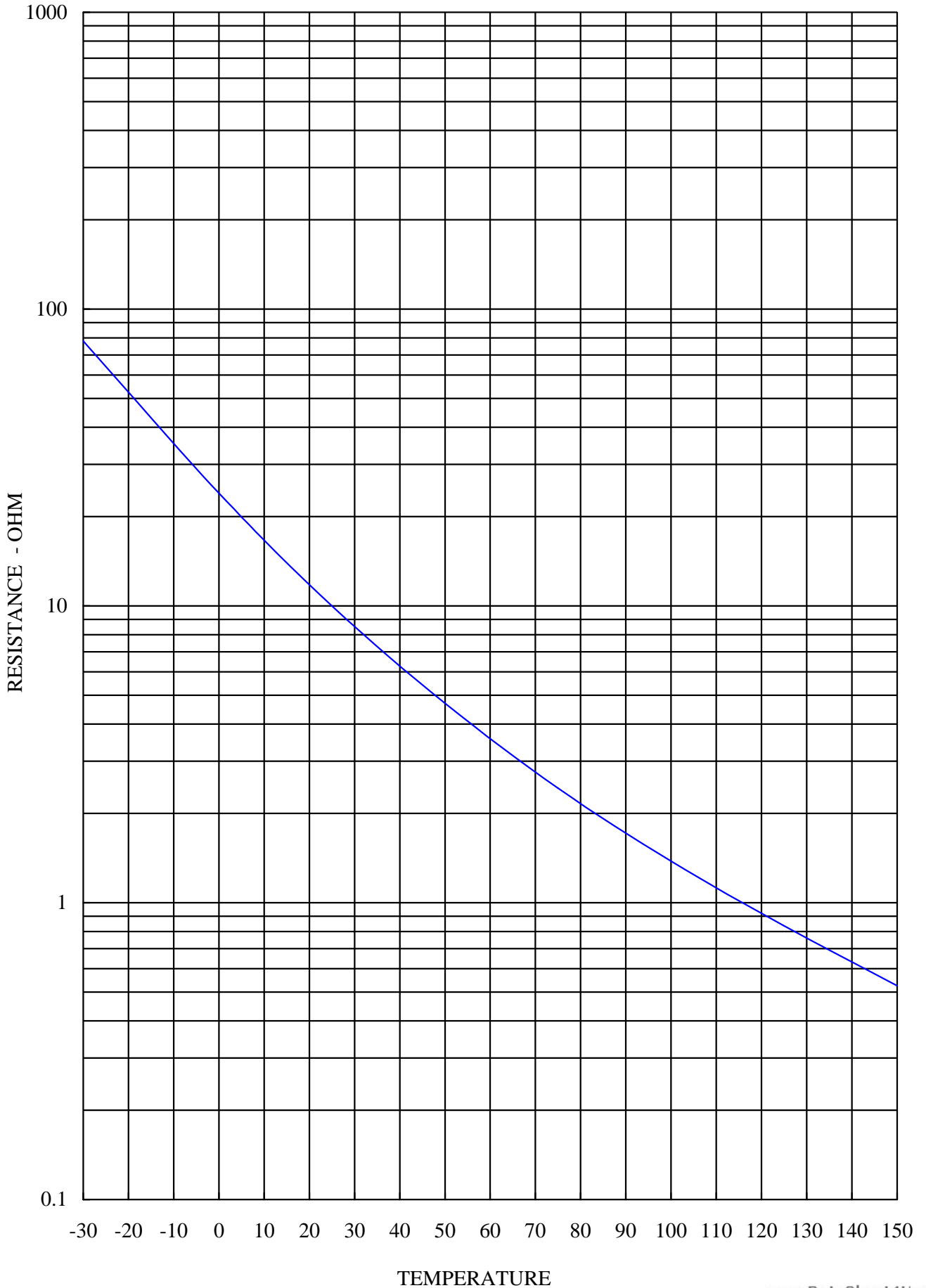
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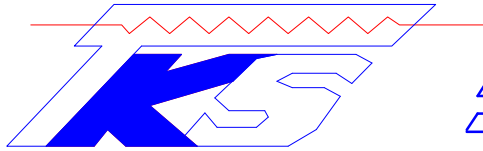
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7. R-T characteristic curve

SCK08102MIA



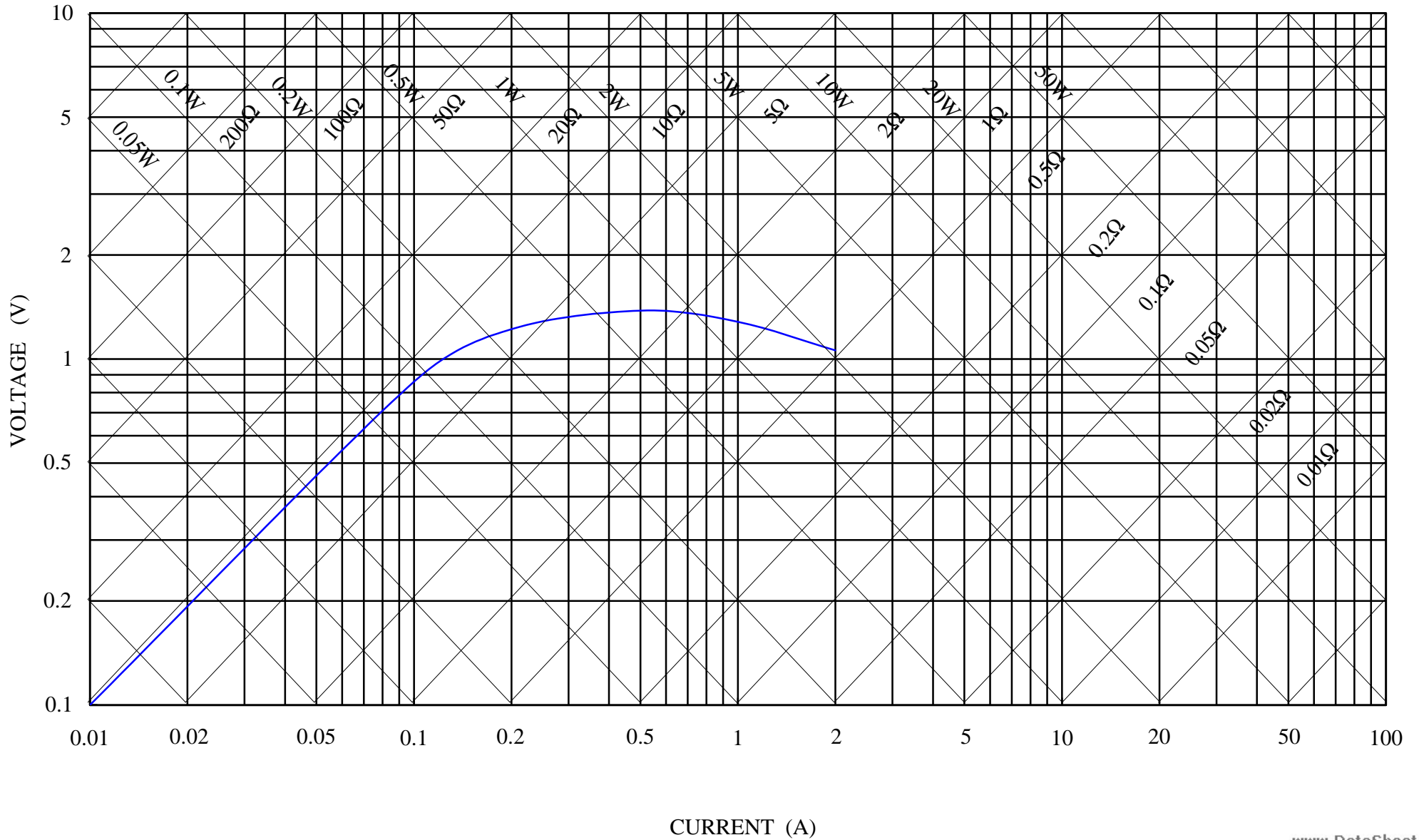
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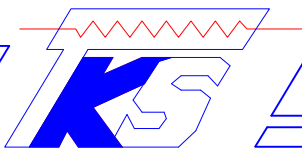
8. V-I characteristic curve (Ambient Ta=25)

SCK08102MIA



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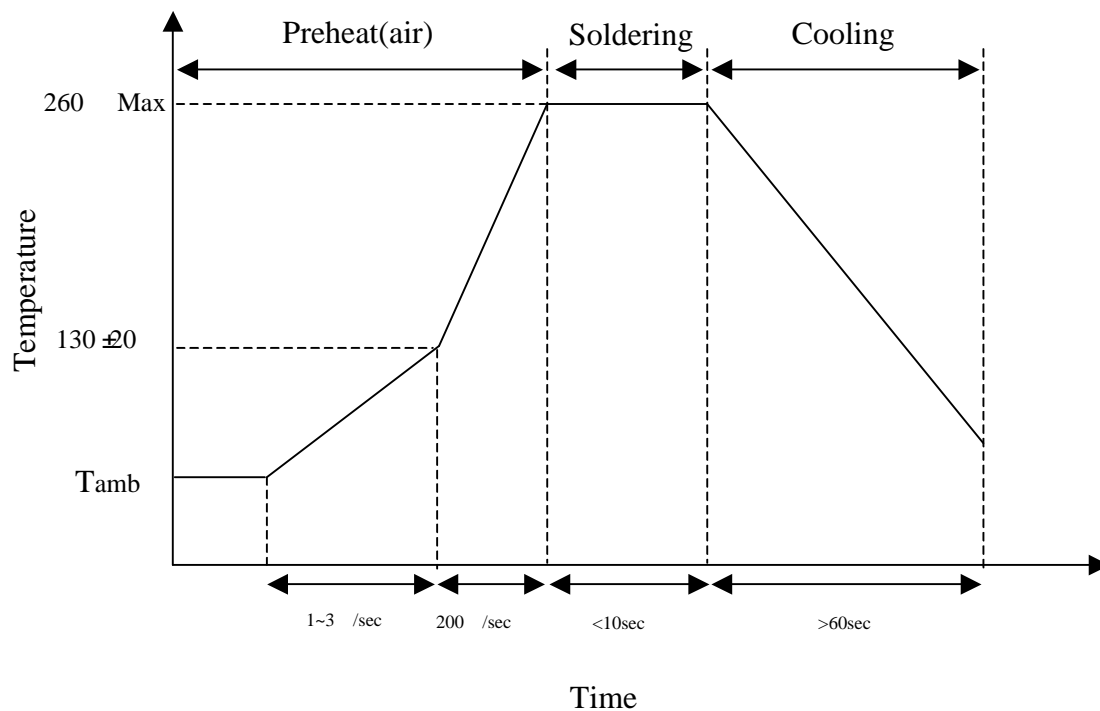


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9. Wave Flow

Recommended Wave Soldering Profile



Recommended Reworking Conditions With Soldering Iron

Item	Conditions
Temperature of Soldering Iron-tip	360 (max.)
Soldering Time	2 sec (max.)
Distance from coating	6 mm (min.)