

- @ C.X.O (Clock Crystal Oscillator) : [SCO-02](#), [SCO-06](#), [SCO-105](#), [SCO-103](#), [SCO-200 Series](#)
 @ TCXO (Temperature Compensated Crystal Oscillator) : [STA](#), [STB](#), [STC](#), [STD](#), [STE Series](#)
 @ VCXO (Voltage Controlled Crystal Oscillator) : [SVA](#), [SVB](#), [SVC](#), [SVD](#), [SVE](#), [SVF](#), [SVG](#), [SVH Series](#)
 @ VCTCXO(Voltage Controlled TCXO) : [STA](#), [STB](#), [STC](#), [STD](#), [STE Series](#)

■ **C.X.O (Clock Crystal Oscillator)**
 SCO-02 (PECL OSC Specification)



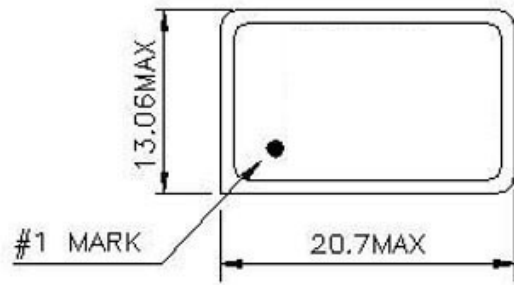
SCO-02

ELECTRICAL SPECIFICATIONS	
MODEL	SCO-02 (PECL OSC Specification)
Frequency Range	7.000000MHz to 155.520000MHz
Operating Temperature Range	0 to +70°C or -40 to +85°C
Storage Temperature Range	-55 to +125°C
Supply Voltage(VDD)	3.3Vdc □5%, 5.0Vdc □5%
Aging (at 25°C)	□5 ppm/year Max
Load drive Capability	50 Ω
Start up Time	10msec Max
Input Current	60mA Max
Frequency Tolerance/Stability	□25, □50, □100 ppm Max
Output Voltage Logic High(VOH)	2.275 Min. (3.3V), 3.96 Min (5.0V)
Output Voltage Logic Low(VOL)	1.68 Max. (3.3V), 3.40 Max (5.0V)
Duty Cycle	40/60% or 45/55 (at 50% of waveform)
Rise / Fall Time	2nsec Max. (20% to 80% of waveform)

[SCO-02 \(PECL OSC Specification \) Series\[Drawing\]](#)

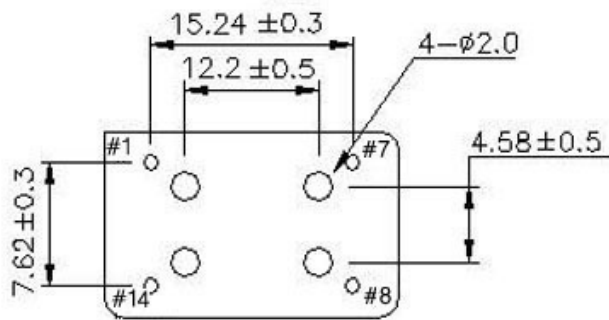
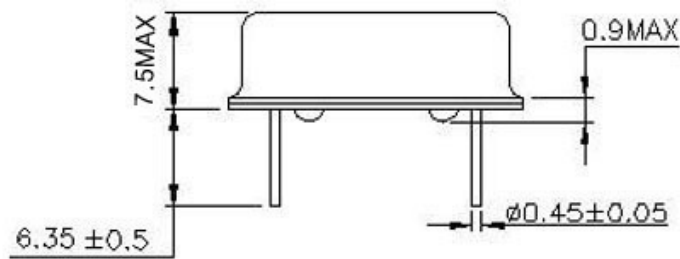
MECHANICAL DIMENSIONS

SCO-020 (PECL OSC Specification)

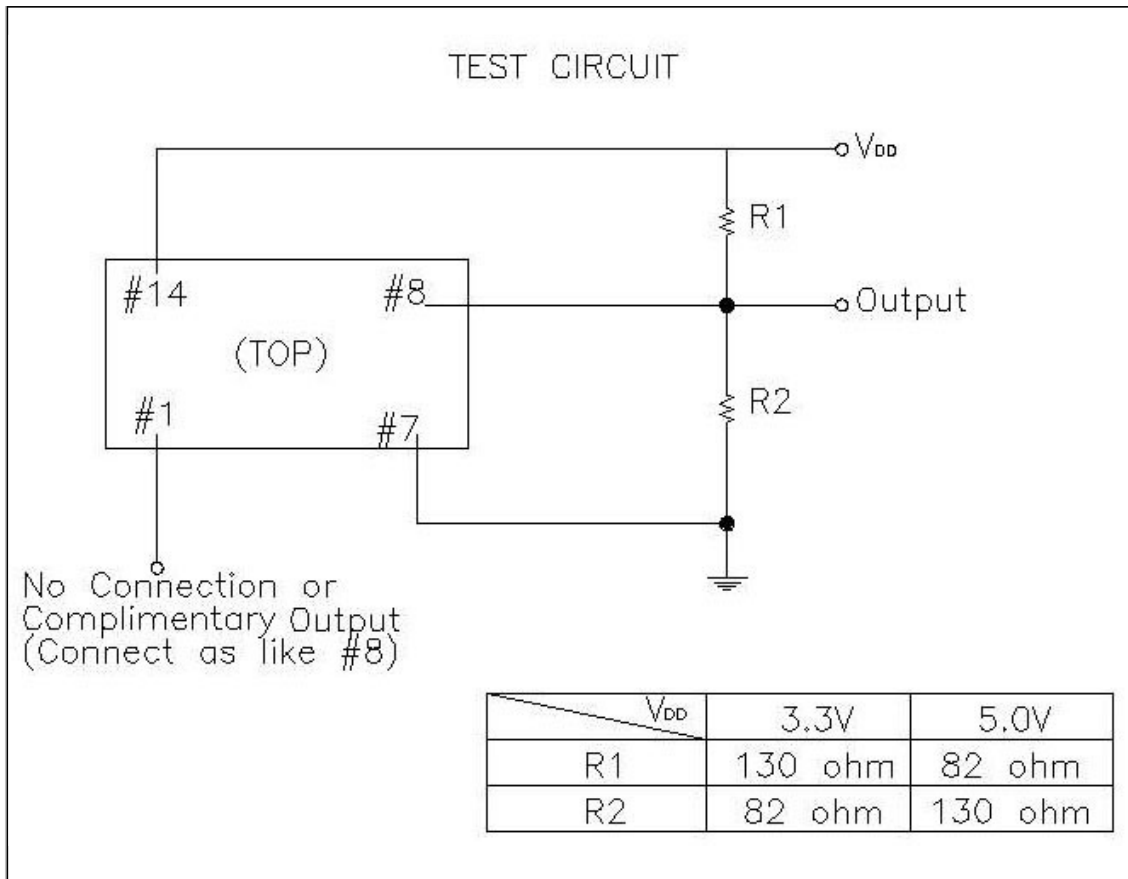


PIN CONNECTION

#1 N.C or COMPLIMENTARY OUTPUT
 #7 GND
 #8 OUTPUT
 #14 V_{DD}



TEST CIRCUIT



■ C.X.O (Clock Crystal Oscillator)
SCO-02



SCO-02

ELECTRICAL SPECIFICATIONS			
MODEL	SCO-02 Series		
Frequency Range	1.000 to 125.000 MHz		
Operating Temperature Range	0 to +70°C or -40 to +85≅ °C		
Storage Temperature Range	-55 to +125≅ °C		
Supply Voltage(V_{DD})	5.0Vdc ±10%	3.3Vdc ±10%	
Frequency Tolerance/Stability	±100, ±50, ±25 ppm Max		
Input Current	1.000 to 20.000MHz 20.001 to 40.000MHz 40.001 to 80.000MHz 80.001 to 125.000MHz	20mA Max 30mA Max 40mA Max 50mA Max	15mA Max 25mA Max 35mA Max 45mA Max
Load drive Capability	10TTL Load or 15pf HCMOS Load		
Output Voltage Logic High(V_{OH})	w/TTL Load w/HCMOS Load	2.4Vdc Min 90% of V_{DD} Min	

Output Voltage Logic Low(V _{OL})	w/TTL Load w/HCMOS Load	0.4V _{DC} Max 10% of V _{DD} Max
Duty Cycle	at 50% of WaveForm w/HCMOS Load or 1.4V _{DC} w/TTL Load	50 ±10% (STD) 50 ±5% (Optional)
Rise / Fall Time	1.000 to 20.000MHz 20.001 to 70.000MHz 70.001 to 125.000MHz	10nsec Max 6nsec Max 4nsec Max
Aging (at 25°C)		±5 ppm/year Max
Start up Time		10msec Max
Pin 1 Tri-State Input Voltage	No Connection V _{IH} : ≥ 2.0V _{DC} V _{IL} : ≤ 0.8V _{DC}	Enables Output Enables Output Disables Output: High Impedance
Period Jitter: Absolute		±100ps Max
Period Jitter: One Sigma		±25ps Max

- HCMOS / TTL output
- 5.0V / 3.3V supply voltage
- 14 pin / 8 pin DIP package
- Custom lead length options available
 - Wide frequency
- All-metal welded package

[SCO-02 Series\[Drawing\]](#)

MECHANICAL DIMENSIONS

