

N-channel SiC power MOSFET

V _{DSS}	1200V
R _{DS(on)} (Typ.)	36mΩ
I _D *1	43A
P_D	176W

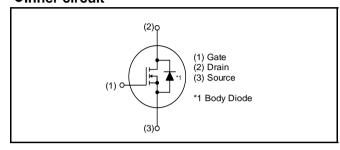
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

•Inner circuit



Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- · Induction heating
- Motor drives

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT4036KE

● **Absolute maximum ratings** (T_{vi} = 25°C unless otherwise specified.)

Parameter		Symbol	Value	Unit	
Drain - source voltage		$V_{\rm DSS}$	1200	V	
Continuous drain	\/ _\/	$T_c = 25^{\circ}C$, , *1	43	Α
and source current	$V_{GS} = V_{GS_on}$	T _c = 100°C	I _D , I _S *1	30	Α
Pulsed drain current	$V_{GS} = V_{GS_on}$	$T_c = 25^{\circ}C$	l _{D,pulse} *2	84	Α
Body diode pulsed forward	ard current	T _c = 25°C	I _{S,pulse} *1,*3	43	Α
Body diode surge forward current		$V_{GS} = 0 V$	I _{S,pulse} *1,*4	84	Α
Gate - source voltage (DC)		V_{GSS_DC}	-4 to +21	V	
Gate - source surge voltage (t _{surge} < 300ns)		$V_{\rm GSS_surge}^{*5}$	-4 to +23	V	
Recommended turn-on gate - source drive voltage		${\sf V_{GS_on}}^{*6}$	+15 to +18	V	
Recommended turn-off gate - source drive voltage		V_{GS_off}	0	V	
Virtual junction temperature		T_{vj}	175	°C	
Range of storage temperature		T_{stg}	-40 to +175	°C	

ullet Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Doromotor	Symbol	Conditions		Values		
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	W	$V_{GS} = 0 \text{ V}, I_D = 9.2 \text{mA}$				\ <u>'</u>
	V (BR)DSS	$T_{vj} = 25^{\circ}C$	1200	-	-	V
		$V_{GS} = 0 \text{ V}, V_{DS} = 1200 \text{V}$				
Zero Gate voltage Drain current	I _{DSS}	$T_{vj} = 25^{\circ}C$	-	1	80	μA
Drain ourion		T _{vj} = 150°C	-	10	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V , V_{DS} = 0V$	1	-	100	nA
Gate - Source leakage current		$V_{GS} = -4V$, $V_{DS} = 0V$	ı	ı	-100	nA
Gate threshold voltage	$V_{GS(th)}^{*7}$	$V_{DS} = 10V, I_D = 11.1mA$	2.8	ı	4.8	V
		$V_{GS} = 18V, I_{D} = 21A$				
Static Drain - Source on - state resistance	R _{DS(on)} *8	$T_{vj} = 25^{\circ}C$	-	36	47	mΩ
c clate resistance		T _{vj} = 150°C	-	72	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	ı	1	-	Ω

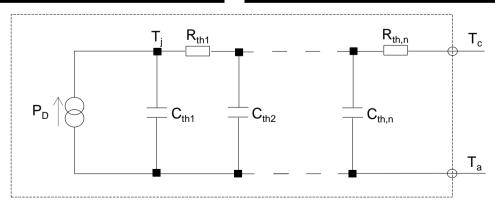
●Thermal resistance

Parameter	Symbol	Values			Unit
Falametei		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R _{thJC} *9	-	0.65	0.85	K/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	4.9 × 10 ⁻²	
R _{th2}	3.0 ×10 ⁻¹	K/W
R _{th3}	3.0 ×10 ⁻¹	

Symbol	Value	Unit
C _{th1}	8.7 ×10 ⁻⁴	
C_{th2}	4.0 × 10 ⁻³	Ws/K
C _{th3}	5.2 ×10 ⁻²	



ullet Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

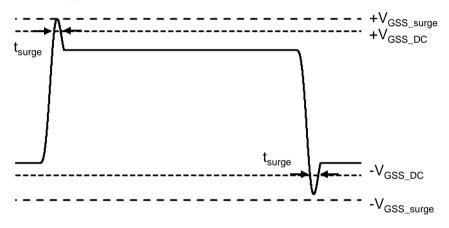
Doromotor	Symbol Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Transconductance	g _{fs} *8	$V_{DS} = 10V, I_{D} = 21A$	-	11	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	2335	-	
Output capacitance	C _{oss}	V _{DS} = 800V	-	70	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	5	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 800V$	1	84	1	pF
Total Gate charge	Qg *8	$V_{DS} = 800V$ $I_{D} = 21A$	-	91	-	
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	-	20	-	nC
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	24	-	
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 800V$ $I_{D} = 21A$	-	10	-	
Rise time	t _r *8	$V_{GS} = +18V / 0V$	-	28	-	20
Turn - off delay time	t _{d(off)} *8	$R_G = 3.3\Omega$, L = 250µH E_{on} includes diode	-	31	-	ns
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50 \text{nH}, C_{\sigma} = 10 \text{pF}$	-	12	-	
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	480	-	., 1
Turn - off switching loss	E _{off} *8		-	57	-	μJ

●Body diode electrical characteristics (Source-Drain) (T_{vi} = 25°C unless otherwise specified)

Darameter	Cumbal	nbol Conditions -	Values			1 10:4
Parameter	Symbol		Min.	Тур.	Max.	Unit
Forward voltage	V _{SD} *8	$V_{GS} = 0V, I_{S} = 21A$	ı	3.3	ı	V
Reverse recovery time	t _{rr} *8	$I_F = 21A$ $V_R = 800V$	ı	20	ı	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 2400A/µs	ı	130	ı	nC
Peak reverse recovery current	I _{rrm} *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	12	-	А

^{*1} Limited by maximum T_{vj} and for Max. R_{thJC}.

*5 Example of acceptable V_{GS} waveform



- *6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying $V_{GS} = 21V$ for 100ms.
- *8 Pulsed
- *9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf

^{*2} Pulse width and duty cycle are limited by T_{vi,max}.

^{*3} Only for body-diode, Repititive pulse, PW ≤ 1.5µs, Duty cycle ≤ 5%

^{*4} When used as a protective function, PW ≤ 10µs

Fig.1 Power Dissipation Derating Curve

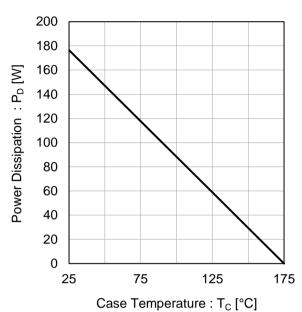


Fig.2 Maximum Safe Operating Area

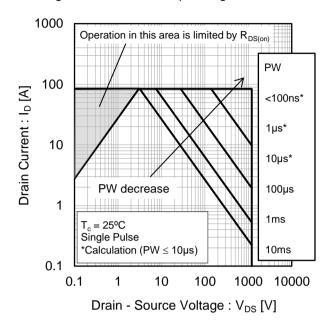
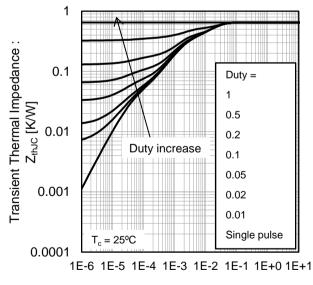
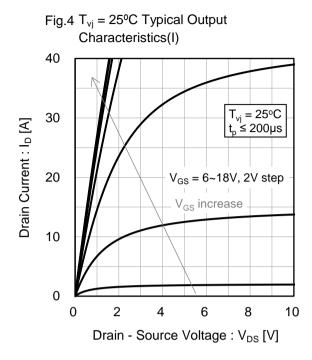


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width



Pulse Width: PW [s]



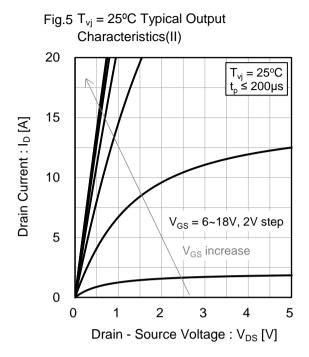
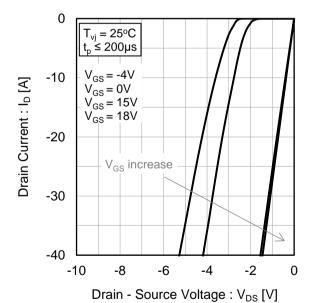
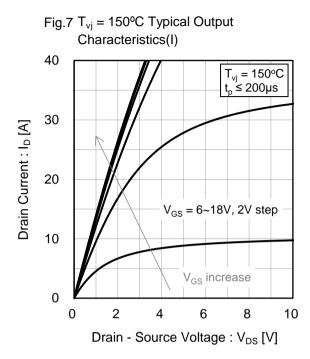


Fig.6 T_{vj} = 25°C 3rd Quadrant Characteristics





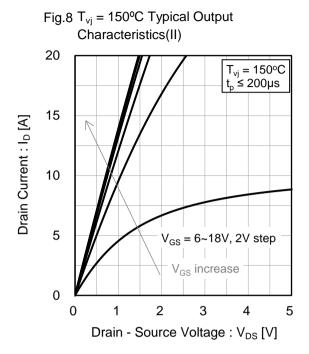


Fig.9 $\frac{T_{vj}}{Characteristics}$ = 150°C 3rd Quadrant 0 T_{vi} = 150°C [,] ≤ 200µs -10 $V_{GS} = -4V$ $V_{GS} = 0V$ $V_{GS} = 15V$ $V_{GS} = 18V$ Drain Current: Ip [A] -20 V_{GS} increase -30 -40 -10 -8 -6 -2 0 Drain - Source Voltage : V_{DS} [V]

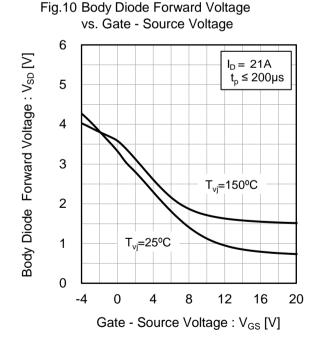


Fig.11 Typical Transfer Characteristics (I)

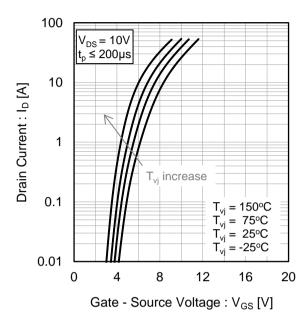


Fig.12 Typical Transfer Characteristics (II)

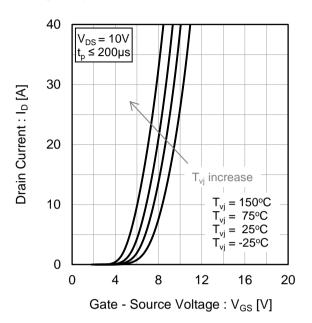


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

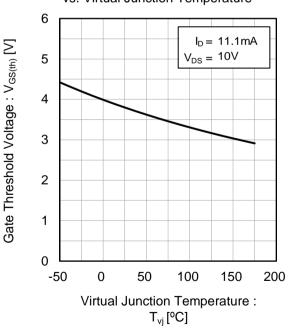
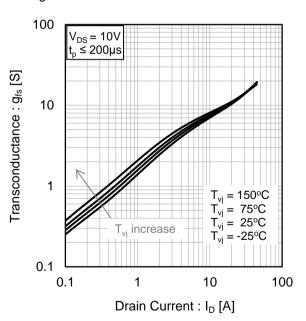
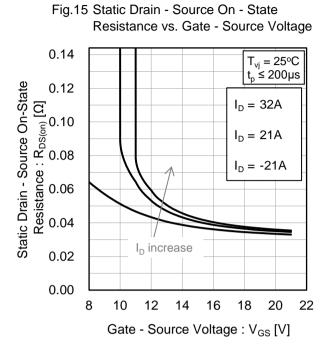


Fig.14 Transconductance vs. Drain Current

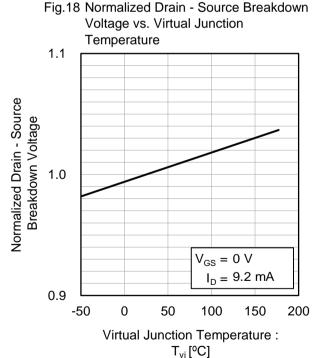




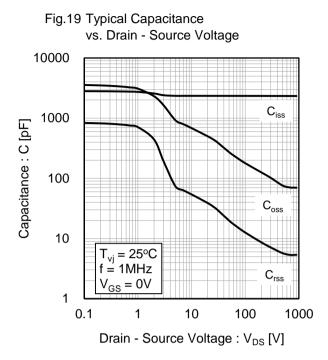
Resistance vs. Virtual Junction Temperature 0.14 $V_{GS} = 18V$ $t_p \le 200 \mu s$ Static Drain - Source On-State 0.12 = 32A= 21A $I_{D} = -21A$ I_D increase 0.02 0.00 0 -50 50 100 150 200 Virtual Junction Temperature: T_{vi} [°C]

Fig.16 Static Drain - Source On - State

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current = 150°C = 125°C Static Drain - Source On-State $T_{vj} = 75^{\circ}C$ = 25°C Resistance: R_{DS(on)} [Ω] = -25°C 0.1 T_{vi} increase 0.01 $V_{GS} = 18V$ $t_p \le 200 \mu s$ 0.001 10 100 Drain Current: I_D [A]



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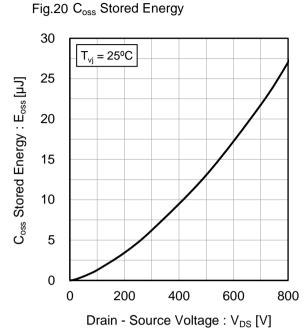


Fig.21 Dynamic Input Characteristics

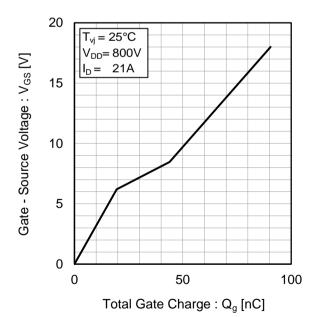


Fig.22 Typical Switching Time vs. External Gate Resistance

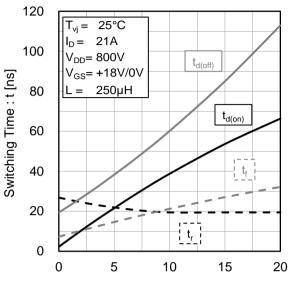
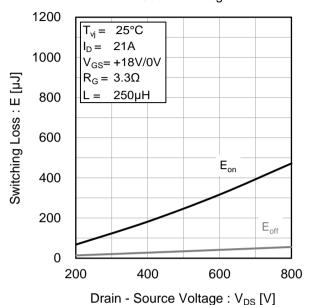


Fig.23 Typical Switching Loss vs. Drain - Source Voltage



External Gate Resistance : $R_{\text{G}}\left[\Omega\right]$

Fig.24 Typical Switching Loss vs. Drain Current

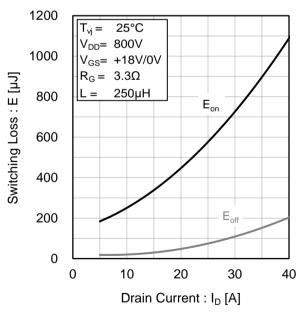
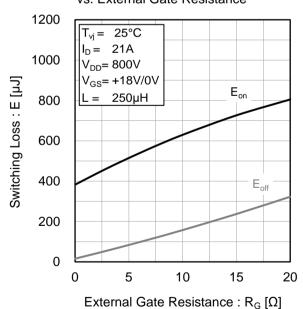


Fig.25 Typical Switching Loss vs. External Gate Resistance



Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

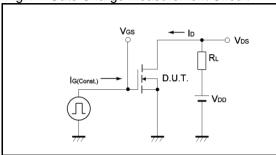


Fig.2-1 Switching Characteristics Measurement Circuit

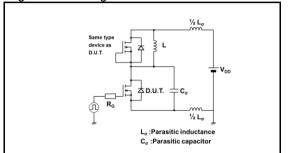


Fig.2-3 Waveforms for Switching Energy Loss

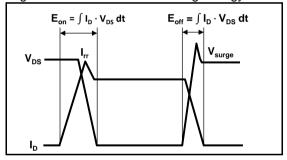


Fig.3-1 Reverse Recovery Time Measurement Circuit

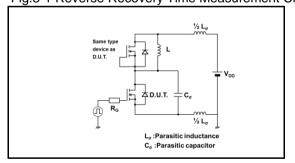


Fig.1-2 Gate Charge Waveform

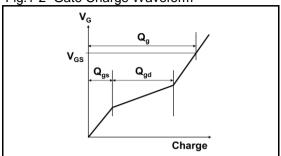


Fig.2-2 Waveforms for Switching Time

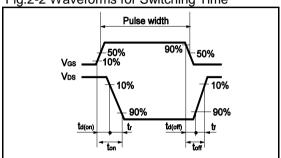
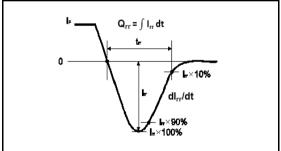
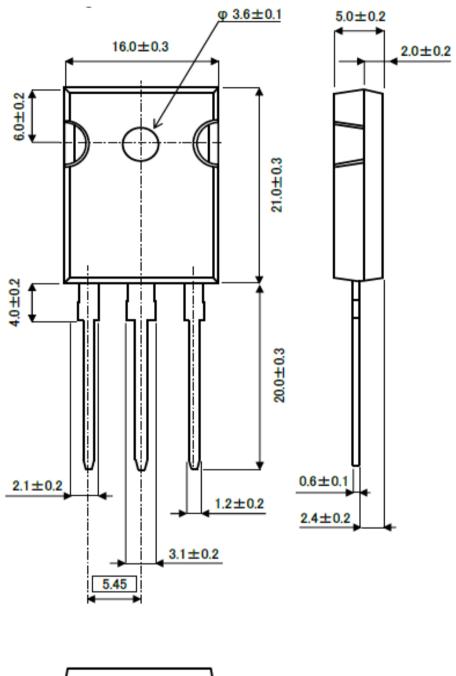


Fig.3-2 Reverse Recovery Waveform

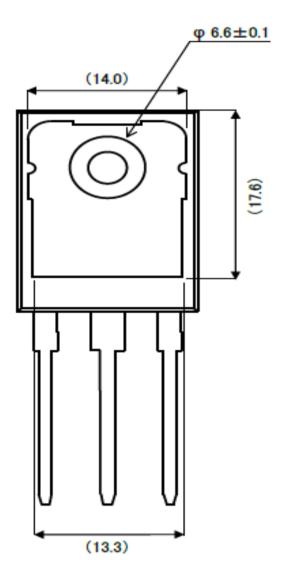


Package Dimensions



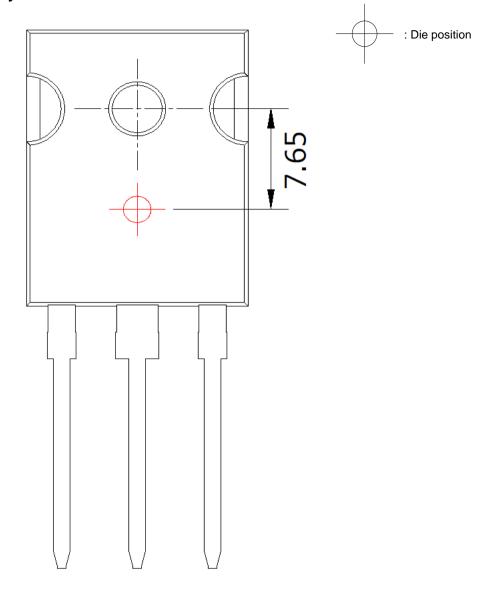


Unit: mm



Unit: mm

●Die Bonding Layout



- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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