



## FAST RECOVER EPITAXIAL DIODE (FRED)

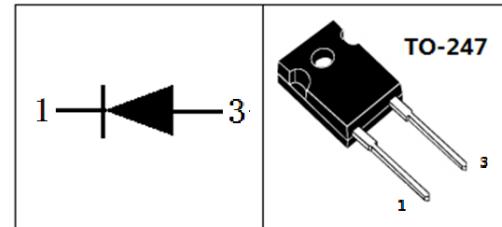
### Features

- Planar passivated chips
- Very short recovery time
- Extremely low switching losses
- Low IRM values
- Soft recovery behaviour
- 100% avalanche tested

|  |                                 |
|--|---------------------------------|
| <b>V<sub>RRM</sub> = 1200 V</b>                                      | <b>I<sub>F(AV)</sub> = 75 A</b> |
| <b>V<sub>F</sub> (typ) = 2.0V</b> ( $I_F=75A, T_{vj}=25^\circ C$ )   |                                 |
| <b>t<sub>rr</sub> &lt; 90 ns</b> ( $I_F = 1 A$ ; $di/dt = 200 A/s$ ) |                                 |
| <b>Package</b>   | <b>TO247-2L</b>                 |

### Applications

- Antiparallel diode for high frequency switching devices
- Anti saturation diode
- Snubber diode
- Free wheeling diode in converters and motor control circuits
- Rectifiers in switch mode power supplies (SMPS)
- Inductive heating and melting
- Uninterruptible power supplies (UPS)
- Ultrasonic cleaners and welders



### Absolute Maximum Ratings

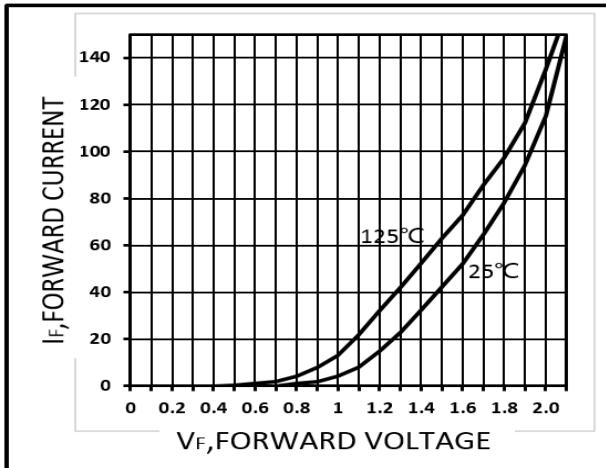
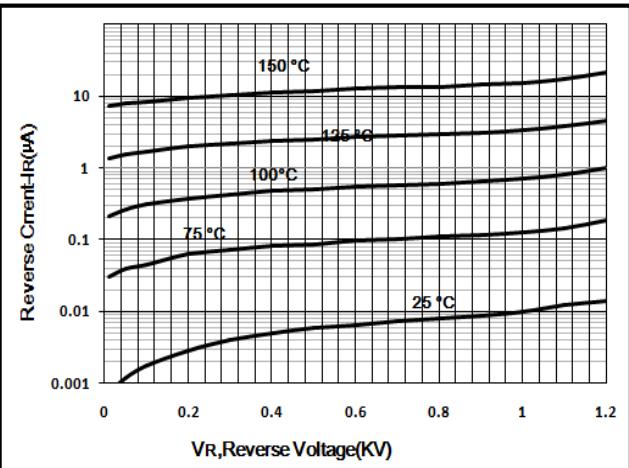
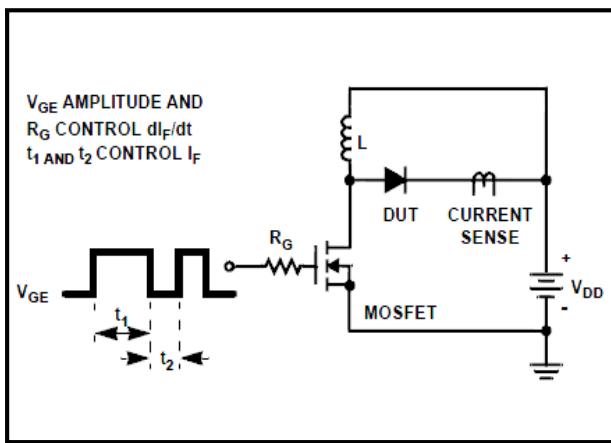
| Symbol                   | Parameter   | Value       | Units |
|--------------------------|---|-------------|-------|
| <b>V<sub>RRM</sub></b>   | Peak Repetitive Reverse Voltage                           | 1200        | V     |
| <b>I<sub>F(AV)</sub></b> | Diode Continuous Forward Current (T <sub>c</sub> =100 °C) | 75          | A     |
| <b>I<sub>FRM</sub></b>   | Repetitive Peak Surge Current (20kHz Square Wave)         | 150         | A     |
| <b>I<sub>FSM</sub></b>   | Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)  | 750         | A     |
| <b>T<sub>J</sub></b>     | Operating Junction Temperature Range                      | -55 to +150 | °C    |
| <b>T<sub>STG</sub></b>   | Storage Temperature Range                                 | -55 to +150 | °C    |

### ELECTRICAL SPECIFICATIONS (T<sub>j</sub> = 25 °C unless otherwise specified)

| Symbol               | Parameter                          | Test Conditions                           | Min. | Typ. | Max. | Units |
|----------------------|------------------------------------|---|------|------|------|-------|
| <b>V<sub>R</sub></b> | Cathode to Anode Breakdown Voltage | I <sub>R</sub> = 100μA                    | 1200 |      |      |       |
| <b>V<sub>F</sub></b> | Diode Forward Voltage              | I <sub>F</sub> =75A T <sub>c</sub> =25°C  |      | 2.0  | 2.7  | V     |
|                      | Diode Forward Voltage              | I <sub>F</sub> =75A T <sub>c</sub> =125°C |      | 1.8  | 2.5  | V     |
| <b>IRM</b>           | Maximum Reverse Leakage Current    | VR=1200V T <sub>c</sub> =25°C             |      |      | 10   | μA    |
|                      |                                    | VR=1200V T <sub>c</sub> =125°C            |      |      | 100  | μA    |

**DYNAMIC RECOVERY CHARACTERISTICS**( $T_J = 25^\circ\text{C}$  unless otherwise specified)

| Symbol    | Parameter   | Test Conditions   | Min. | Typ. | Max. | Units |
|-----------|---|---|------|------|------|-------|
| $I_{RRM}$ | Diode Peak Reverse Recovery Current   | $V_{DD}=100\text{V}; I_F=1\text{A};$<br>$dif/dt=200\text{A}/\mu\text{s};$<br>See Fig.4  |      | 5.1  |      | A     |
| $Q_{rr}$  | Reverse recovery charge<br>(Area Under the Curve Defined by $I_{RRM}$ and trr). |   |      | 250  |      | nc    |
| $trr$     | Diode Reverse Recovery Time   |   |      | 75   | 90   | ns    |
| S         | $S = tb/ta$   |   |      | 0.6  |      |       |
| $I_{RRM}$ | Diode Peak Reverse Recovery Current   | $V_{DD}=800\text{V}; I_F=30\text{A};$<br>$dif/dt=850\text{A}/\mu\text{s};$<br>See Fig.4 |      | 27   |      | A     |
| $Q_{rr}$  | Reverse recovery charge<br>(Area Under the Curve Defined by $I_{RRM}$ and trr). |   |      | 910  |      | nc    |
| $trr$     | Diode Reverse Recovery Time   |   |      | 112  |      | ns    |
| S         | $S = tb/ta$   |   |      | 1.26 |      |       |

**Fig.1 Forward Current vs Forward Voltage**

**Fig.2 Reverse Current vs Reverse Voltage**

**Fig.3 trr Test Circuit**

**Fig.4 trr Waveforms and Definitions**
