

SDB20D45D2

Schottky Barrier Rectifier

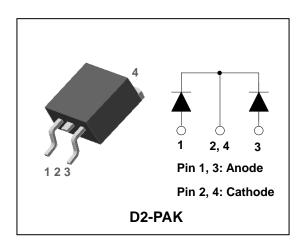
LOW VOLTAGE SCHOTTKY RECTIFIER

Features

- Low forward voltage drop and leakage current
- Low power loss and High efficiency
- · Guard-ring for overvoltage protection
- · Dual common cathode rectifier
- Full lead (Pb)-free and RoHS compliant device

Applications

- Power supply Output rectification
- High efficiency SMPS
- Free-wheeling diode
- Reverse battery protection
- · DC to DC systems



Product Characteristics

I _{F(AV)}	2 X 10A		
V_{RRM}	45V		
V _{FM} at 125℃	0.5V		
I _{FSM}	120A		

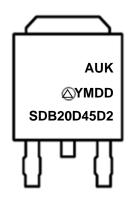
Description

Schottky barrier rectifier designed for high frequency miniature Switched Mode Power Supplies such as adaptors and on board DC to DC converters.

Ordering Information

Device	Marking Code	Package	Packaging
SDB20D45D2	SDB20D45D2	D2-PAK	Tape & Reel

Marking Information



AUK = Manufacture Logo

 Δ = Control Code of Manufacture

YMDD = Date Code Marking

-. Y = Year Code

-. M = Monthly Code

-. DD = Daily Code

SDB20D45D2 = Specific Device Code

Absolute Maximum Ratings (Limiting Values)

Characteristic		Symbol	Value	Unit	
Maximum repetitive reverse voltage Maximum working peak reverse voltage Maximum DC blocking voltage		$egin{array}{c} V_{RRM} \ V_{RWM} \ V_{R} \end{array}$	45	٧	
Maximum average forward rectified current	per diode		10	А	
	total device	I _{F(AV)}	20		
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load per diode		I _{FSM}	120	А	
Storage temperature range		T _{stg}	-45℃ to +150℃	$^{\circ}$ C	
Maximum operating junction temperature		TJ	150	$^{\circ}$	

Thermal Characteristics

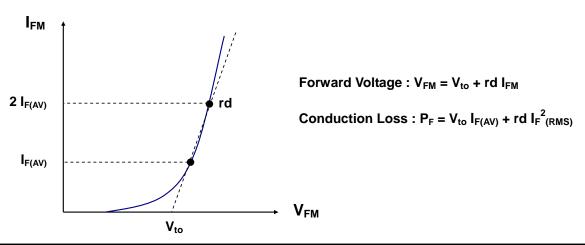
Characteristic		Symbol	Value	Unit
Maximum the areal maximum in institute to	per diode	D	3.0	°C/W
Maximum thermal resistance junction to case	total device	$R_{th(j-c)}$	2.8	

Electrical Characteristics (Per Diode)

Characteristic	Symbol	Test Condition		Min.	Тур.	Max.	Unit
Peak forward voltage drop	V _{FM} ⁽¹⁾	I _{FM} = 10A	T _j =25℃	-	-	0.54	V
			T _j =125℃	-	-	0.50	V
Deverage leaders assument	I _{RM} ⁽¹⁾	$V_R = V_{RRM}$	T _j =25℃	-	-	1.5	mA
Reverse leakage current			T _j =125℃	-	-	150	mA
Junction capacitance	C _j	$V_R = 5V_{DC}$, $f=1MHz$		-	550	-	pF

Note : (1) Pulse test : $t_P \le 380~\mu s$, Duty cycle $\le 2\%$

To evaluate the conduction losses use the following equation (Fig 4.) : P_F = 0.35 x $I_{F(AV)}$ + 0.015 $I_{F(RMS)}^2$



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Rating and Characteristic Curves

Fig. 1) Typical Forward Characteristics (Per diode)

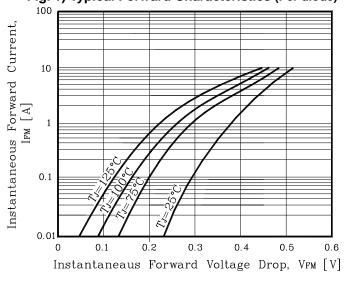


Fig. 2) Typical Reverse Characteristics (Per diode)

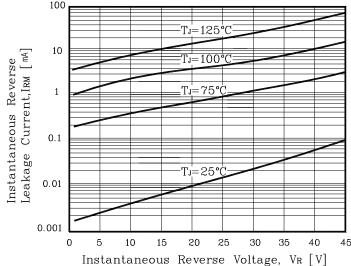


Fig. 3) Maximum Forward Derative Curve

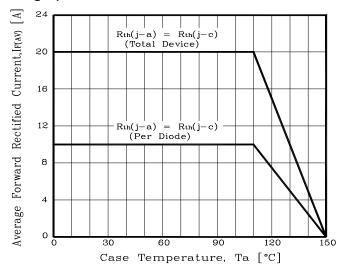


Fig. 4) Forward Power Dissipation (Per diode)

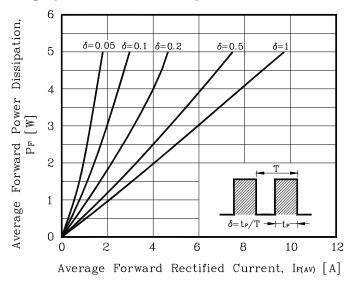


Fig. 5) Maximum Non-Repetitive Peak Forward Surge Current (Per diode)

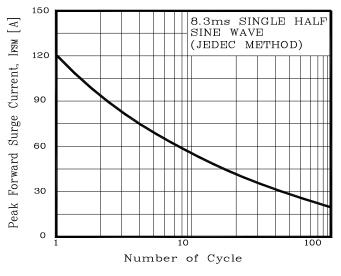
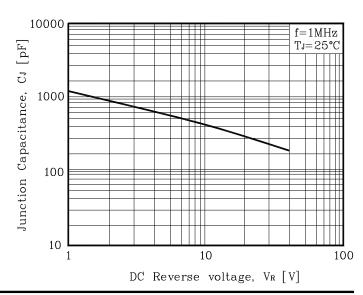
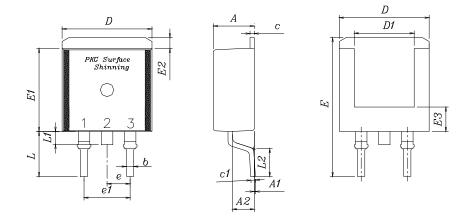


Fig. 6) Typical Junction Capacitance (Per diode)

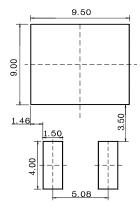


Package Outline Dimension



SYMBOL		ERS	NOTE	
PIMPOL	MINIMUM	NOIE		
Α	4.35	4.50	4.65	
A1	_	ı	0.15	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.90	
С	0.40	0.50	0.60	
c1	0.40	0.50	0.60	
D	9.80	10.00	10.20	
D1	6.40	6.60	6.80	
Ε	15.00	15.40	15.80	
E1	9.05	9.20	9.35	
E2	1.00	1.20	1.40	
E3	2.50	2.70	2.90	
е	2.34	2.54	2.74	
e1	4.88	5.08	5.28	
L	4.60	5.00	5.40	
L1	1.40	1.45	1.50	
L2	2.50	_	_	

**** Recommend PCB solder land (Unit: mm)**



KSD-D6S006-001

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