Current Mode PWM Controller SDC4566

General Description

SDC4566 is a highly integrated current mode PWM control IC optimized for high performance, low standby power and cost effective offline flyback converter applications in sub 60W range.

The internal slope compensation improves system large signal stability and reduces the possible subharmonic oscillation at high PWM duty cycle output. Leading-edge blanking on current sense(CS) input removes the signal glitch due to snubber circuit diode reverse recovery and thus greatly reduces the external component count and system cost in the design.

SDC4566 offers complete protection coverage with automatic self-recovery feature including cycle-by-cycle current limiting (OCP), over load protection (OLP), VCC over voltage clamp and under voltage lockout (UVLO). The gate drive output is clamped to maximum 14V to protect the power MOSFET.

Features

- Frequency shuffling technology for improved emc performance
- Audio noise free operation
- Extended burst mode control for improved efficiency and minimum standby power design
- Internal synchronized slope compensation
- Low VCC startup current and low operating current
- Leading edge blanking on current sense input
- Good protection coverage with auto self-recovery （UVLO/OVP/ OCP/OLP/OTP）
- FB/CS open loop protection
- Secondary Rectifier Short Protection
- Package: SOT-23-6

Applications

- Battery charger
- Power adapter
- Set-top box power supplies

Figure 1. Package Type

Pin Configuration

Package: SOT-23-6

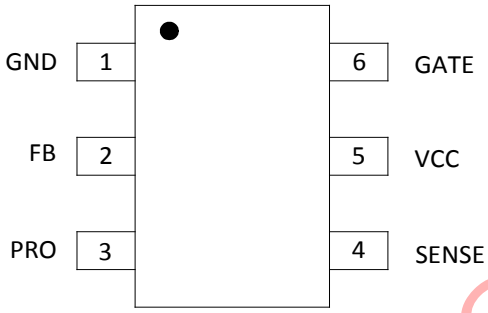


Figure 2. Pin Configuration

Pin Number	Pin Name	Function
1	GND	Ground
2	FB	Feedback input pin. The PWM duty cycle is determined by voltage level into this pin and SENSE pin input.
3	PRO	For external arbitrary OVP or OTP.
4	SENSE	Current sense input pin. Connected to MOSFET current sensing resistor node.
5	VCC	Chip DC power supply pin.
6	GATE	Totem-pole gate drive output for the power MOSFET.

Table 1. Pin Description

Functional Block Diagram

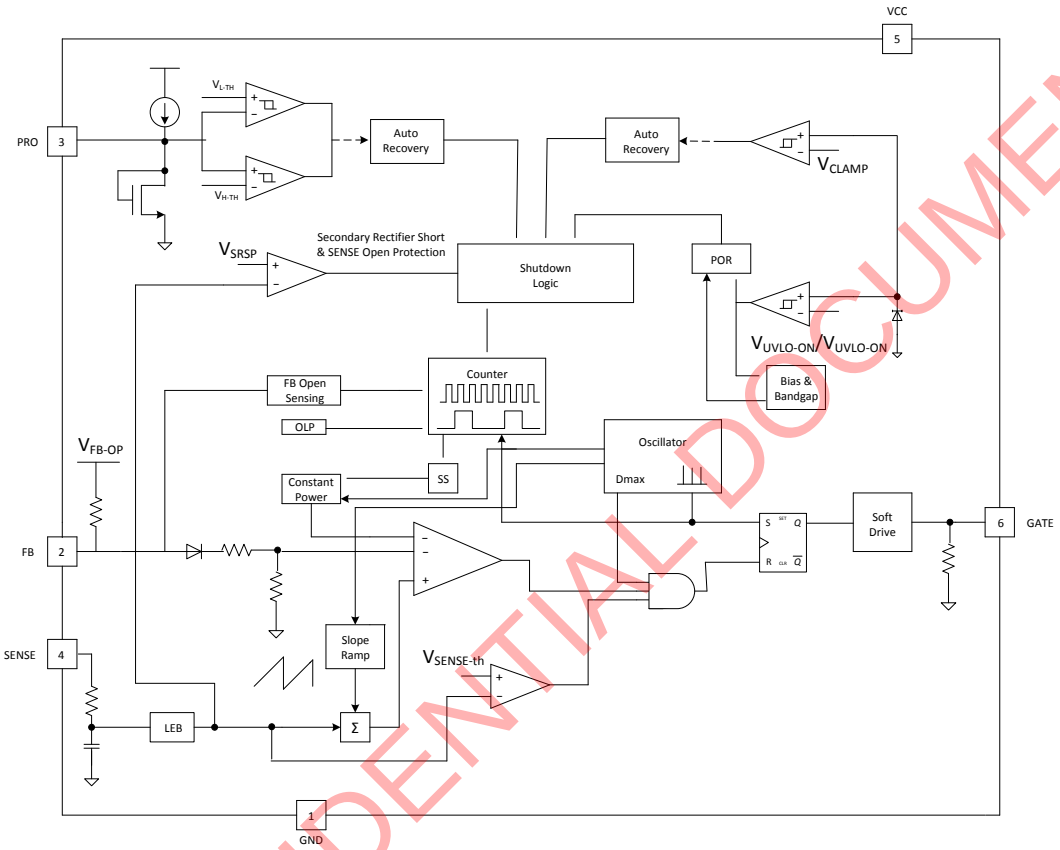
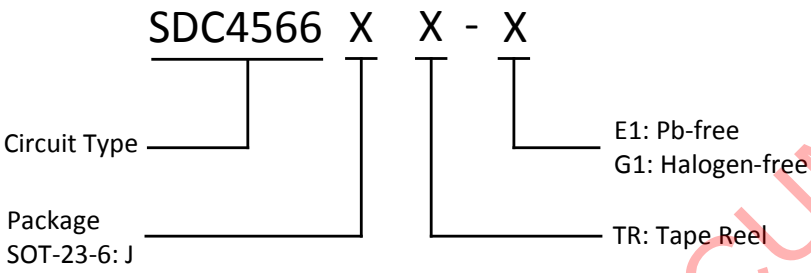


Figure 3. Functional Block Diagram

Ordering Information



Package	Temperature Range	Part Number		Marking ID		Packing Type
		Pb-free	Halogen-free	Pb-free	Halogen-free	
SOT-23-6	-40℃-85℃	SDC4566JTR-E1	SDC4566JTR-G1	4566	4566G	Tape Reel

Absolute Maximum Ratings (NOTE: Stresses greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device.)

Parameter	Symbol	Value	Unit
VCC DC supply voltage	V_{CC}	-0.3~30	V
VCC clamp voltage	V_{CC_CLAMP}	32	V
VCC DC clamp current	I_{CLAMP}	10	mA
V_{FB} input voltage	V_{FB}	-0.3~7	V
SENSE input voltage	V_{SENSE}	-0.3~7	V
V_{PRO} input voltage	V_{PRO}	-0.3~7	V
Operating junction temperature	T_J	150	°C
Storage temperature	T_{STG}	-55~150	°C
Latch-up test per JEDEC 78	-	200	mA
ESD,HBM model per Mil-Std-883H,Method 3015	HBM	2000	V
ESD,MM model per JEDEC EIA/JESD22-A115	MM	200	V

Table 2. Absolute Maximum Ratings

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
VCC DC supply voltage	V_{CC}	10	30	V
Oscillation frequency	f_{OSC}	60	70	kHz
Operating Temperature Range	T_{OP}	-40	85	°C

Table 3. Recommended Operating Conditions

Electrical Characteristics (Ta=25°C, unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage (VCC)						
VCC start up current	$I_{STARTUP}$	V_{CC} =12.5V, RI=100k,Measure Leakage current into VCC	-	3	20	uA
Operation current	I_{CC}	V_{CC}=16V, RI=100k, V_{FB}=3V	-	1.6	-	mA
VCC under voltage lockout enter	$V_{UVLO(ON)}$	-	6.5	7.5	8.5	V
VCC under voltage lockout exit (recovery)	$V_{UVLO(OFF)}$	-	13.5	14.5	15.5	V
VCC zener clamp voltage	V_{CC_CLAMP}	I_{VCC}=10mA	30	32	34	V
Feedback Input Section(FB Pin)						
FB open loop voltage	V_{FB_OPEN}	-	-	4.2	-	V
FB pin short circuit current	I_{FB_SHORT}	Short FB pin to GND and Measure Current	-	0.4	-	mA
Zero duty cycle fb threshold voltage zero	V_{TH_0D}	VCC=16V	-	-	0.85	V
Power limiting FB threshold voltage	V_{TH_PL}	-	3.3	3.7	4.1	V
Power limiting debounce time	t_{D_PL}	-	-	90	-	ms
The threshold enter burst mode	V-Burst-L	-	-	1.1	-	V
The threshold exit burst mode	V-Burst-H	-	-	1.2	-	V
Current Sense Input(Sense Pin)						
Over current threshold voltage at zero duty cycle	$V_{SENSE-th}$	FB=3.3V	0.70	0.75	0.80	V
SENSE clamp voltage	$V_{SENSE--CLAMP}$	-	-	0.95	-	V
Leading edge blanking time	t_{LEB}	-	-	300	-	ns
Over current detection and control delay	t_{PD}	-	-	75	-	ns
OTP/OVP Protection (PRO PIN)						
Normal oscillation frequency	f_{OSC}	-	60	65	70	kHz
Shuffling frequency frequency	$f_{Shuffling}$	-	-3		+3	%
Burst mode base frequency	f_{Green}	V_{CC}= 16V		22		kHz

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Maximum duty cycle	D_{MAX}	V_{CC}=16V, FB=3V, CS=0	70	80	90	%
Frequency temperature stability	$f_{DV-TEMP}$	V_{CC}=16V, Ta= -20°C to 100 °C	-	-	5	%
Frequency voltage stability	f_{DV-VCC}	V_{CC}=12V~25V,	-	-	5	%
PRO open load voltage	V_{PRO}	-	-	2.5	-	V
PRO low protection voltage	V_{L-TH}	-	0.95	1.0	1.05	V
PRO high voltage protection	V_{H-TH}	-	3.7	4.0	4.3	V
Soft start time	t_{soft}	-	3	4	5	ms
Gate Drive Output						
Output low level	V_{OL}	V_{CC}=16V, I_O=-20mA	-	-	0.8	V
Output high level	V_{OH}	V_{CC}=16V, I_O=20mA	10	-	-	V
Output clamp voltage level	V_{CLAMP}	-	-	14	-	V
Output rising time	t_r	V_{CC} =16V, CL =1nf	-	125	-	ns
Output falling time	t_f	V_{CC}=16V, CL=1nf	-	40	-	ns

Table 4. Electrical Characteristics

Function Description

The SDC4566 is a highly integrated PWM controller IC optimized for offline flyback converter applications in sub 60W power range. The extended burst mode control greatly reduces the standby power consumption and helps the design easily meet the international power conservation requirements.

Startup Current and Start up Control

Startup current of SDC4566 is designed to be very low so that VCC could be charged up above UVLO threshold level and device starts up quickly. A large value startup resistor can therefore be used to minimize the power loss yet provides reliable startup in application.

Operating Current

The Operating current of SDC4566 is low at 1.8mA. Good efficiency is achieved with SDC4566 low operating current together with extended burst mode control features.

Frequency shuffling for EMI improvement

The frequency Shuffling/jittering (switching frequency modulation) is implemented in SDC4566. The oscillation frequency is modulated with a random source so that the tone energy is spread out. The spread spectrum minimizes the conduction band EMI and therefore reduces system design challenge.

Extended Burst Mode Operation

Under zero load or light load condition, ment of the power dissipation in a switching mode power supply is from switching loss on the MOSFET transistor, the core loss of the transformer and the loss on the snubber circuit. The magnitude of power loss is in proportion to the number of switching events within a fixed period of time, Reducing switching freque leads to the reduction on power loss and thus conserves the energy.

SDC4566 self adjusts the switching mode according to the loading condition. Under no load to light/medium load condition, the FB input drops below burst mode threshold level, Device enters Burst Mode control. The gate drive output switches only when VDD voltage drops below a preset level and FB input is active to output an on state, otherwise the gate drive remains at off state to minimize the switching loss and reduces the standby power consumption to the greatest extend. The frequency control also eliminates the audio noise at any loading conditions.

Oscillator Operation

It can typically operate at built-in 65kHz center frequency and features frequency jittering function. Its jittering depth is ±3% with about 4ms envelope frequency at 65kHz.

Current Sensing and Leading Edge Blanking

Cycle-by-cycle current limiting is offered in SDC4566 current mode PWM control. The switch current is detected by a sense resistor into the sense pin. An internal leading edge blanking circuit chops off the sense voltage spike at initial MOSFET on state due to Snubber diode reverse recovery so that the external RC filtering on sense input is no longer required. The current limit comparator is disabled and thus cannot turn off the external MOSFET during the blanking period. PWM duty cycle is determined by the current sense input voltage and the FB input voltage.

Internal Synchronized Slope Compensation

Built-in slope compensation circuit adds voltage ramp onto the current sense input voltage for PWM generation. This greatly improves the close loop stability at CCM and prevents the sub-harmonic oscillation and thus reduces the output ripple voltage.

Gate Drive

SDC4566 gate is connected to an external MOSFET gate for power switch control. Too weak the gate drive strength results in higher conduction and switch loss of MOSFET while too strong gate drive output compromises the EMI. A good tradeoff is achieved through the built-in totem pole gate design with right output strength and dead time control. The low idle loss and good EMI system design is easier to achieve with this dedicated control scheme. An internal 14V clamp is added for MOSFET gate protection at higher than expected VCC input.

Protection Controls

Good power supply system reliability is achieved with its rich protection features in- cluding cycle-by-cycle current limiting (OCP), Over Load Protection (OLP) and over voltage clamp, Under Voltage Lockout on VCC (UVLO).

Typical Performance Characteristics

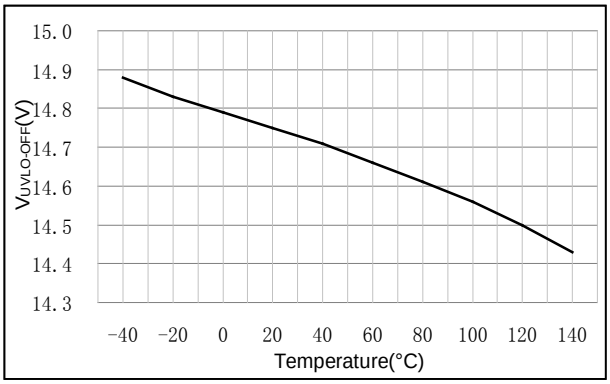


Figure 4. $V_{UVLO-OFF}$(V) vs. Temperature（℃）

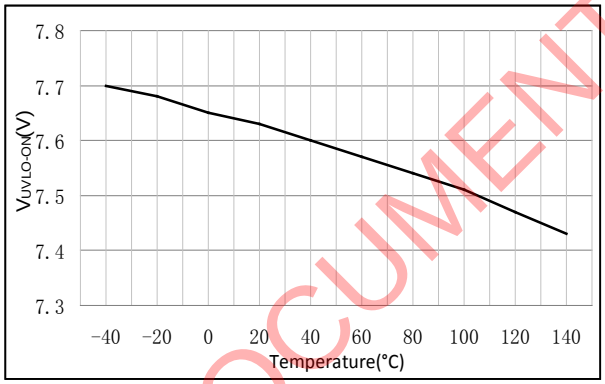


Figure 5. $V_{UVLO-ON}$(V) vs. Temperature（℃）

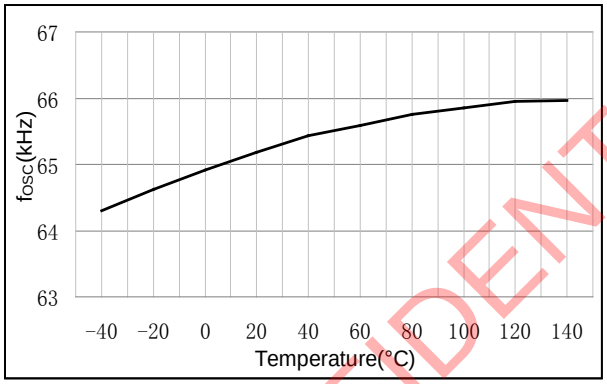


Figure 6. f_{OSC}(kHz) vs. Temperature(℃)

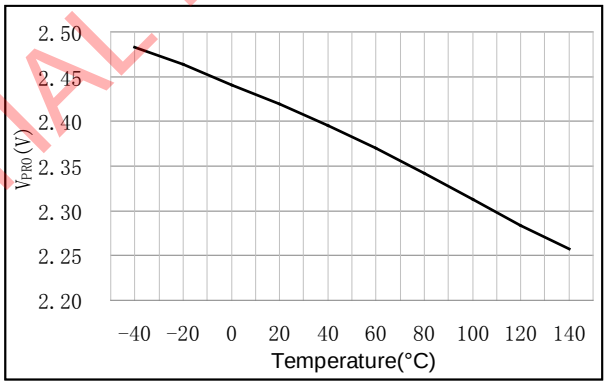


Figure 7. V_{PRO}(V) vs. Temperature(℃)

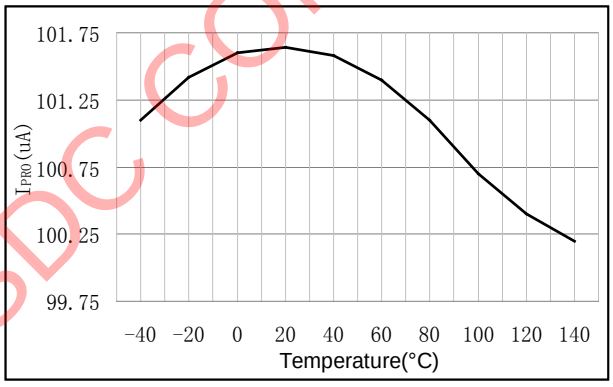


Figure 8. I_{PRO}(mA) vs. Temperature(℃)

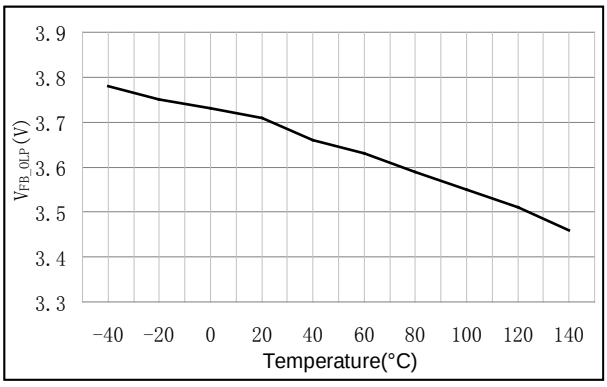


Figure 9. V_{FB_OLP}(V) vs. Temperature(℃)

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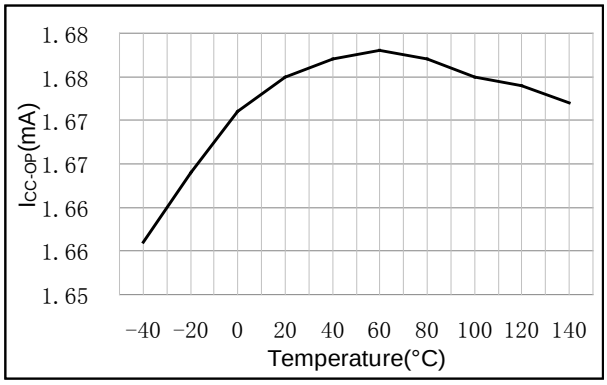


Figure 10. I_{CC-OP}(mA) vs. Temperature(℃)

Typical Application

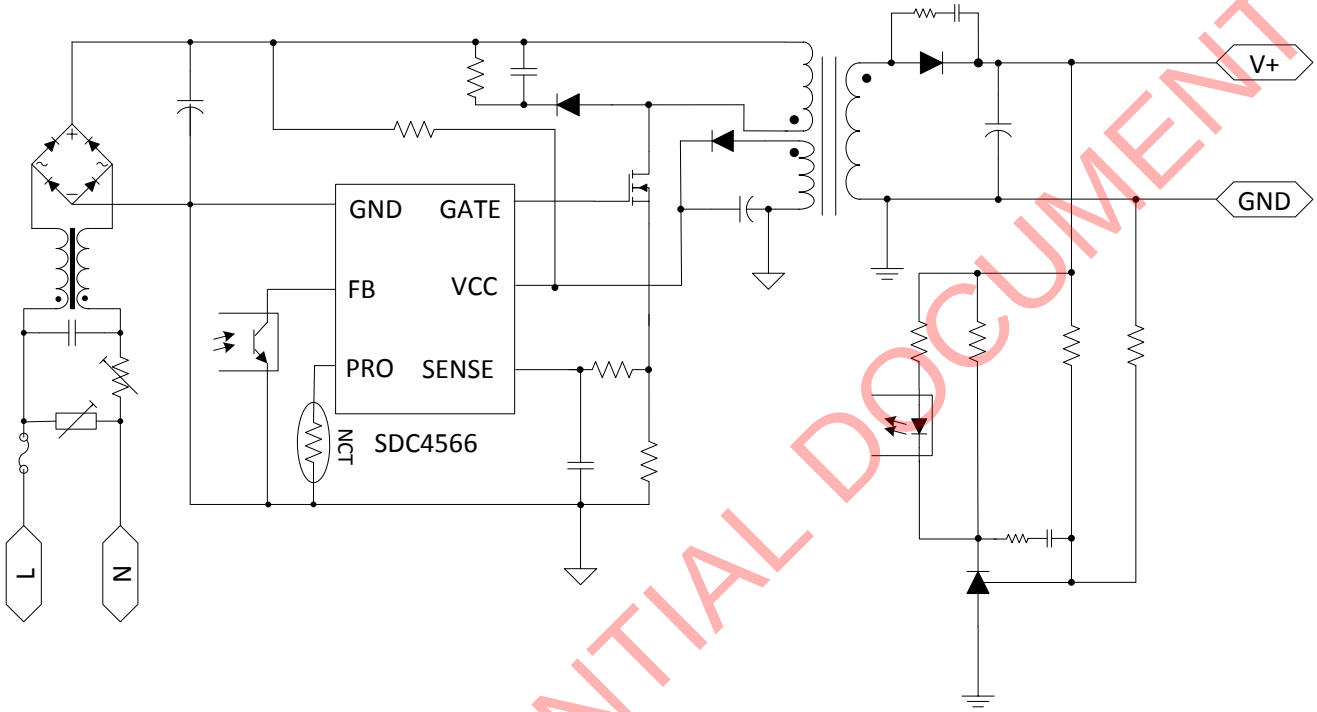
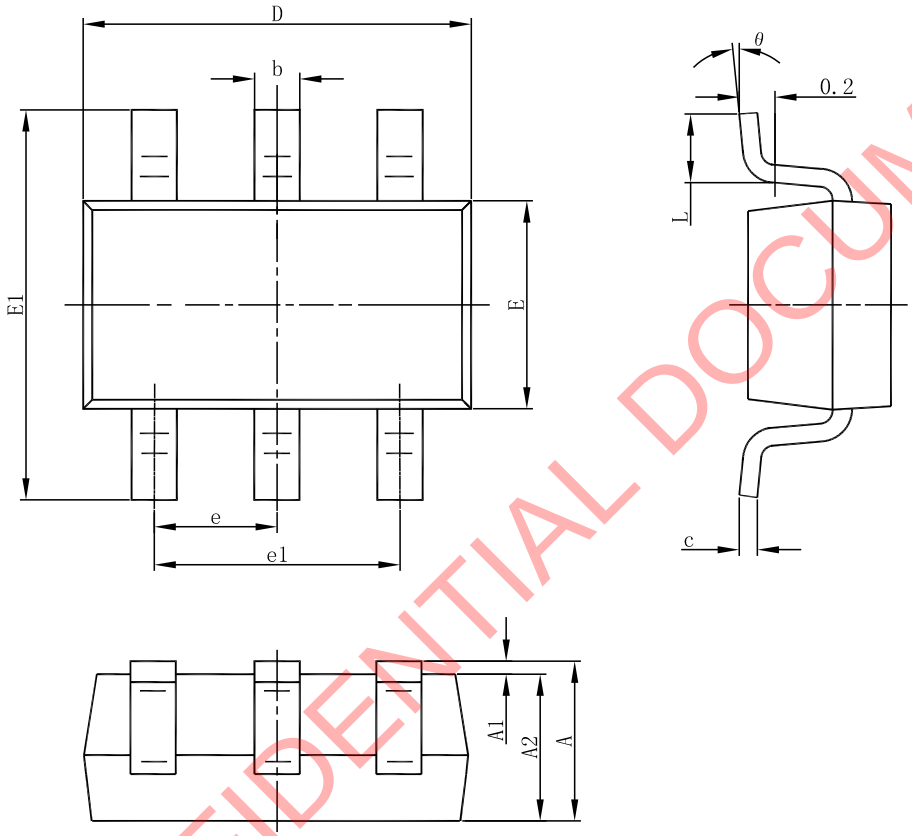


Figure 11. Typical Application

Package Dimension

SOT-23-6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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