SDC608

General Description

SDC608 is a high-performance current mode control IC designed for AC/DC convertor, which supplies about continuous 3.8W output power at the universal AC input range from 85V to 265V.

Features

- Built-in oscillator
- Built-in high voltage power transistor of 700V
- Cycle-by-cycle current limit
- Very low power consumption under no load
- Internal slope compensation and feedback compensation
- Over load protection
- Built-in high precise current limit with temperature compensation
- Over temperature protection
- Low startup and operating current
- VCC over-voltage clamp
- 3.8W output power at the universal AC input range,4.5W output power at AC input 220V
- Package: SOP-7

Applications

- Offline AC/DC flyback converter
- Adaptor/Chargers for cell and other portable apparatus

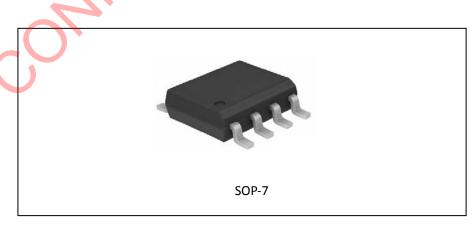


Figure 1. Package Type

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Pin Configuration

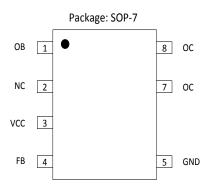


Figure 2. Pin Configuration

Pin Number	Pin Name	Function				
1	ОВ	Start up current input, connecting to start-up resistor				
2	NC	NC				
3	VCC	Supply voltage pin				
4	FB	Feedback pin				
5	GND	Ground				
7,8	ОС	Output of HV transistor, connecting to primary wind of transformer				

Table 1. Pin Configuration

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Functional Block Diagram

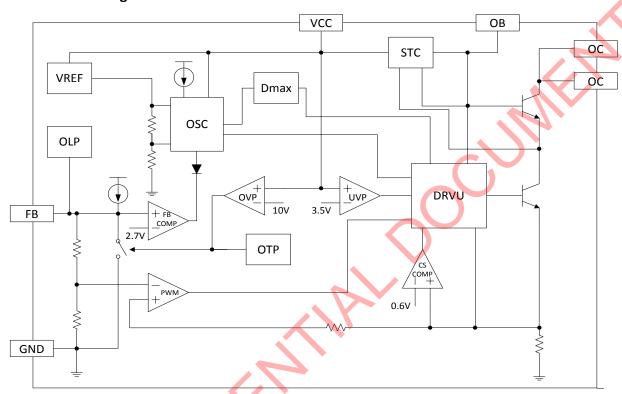
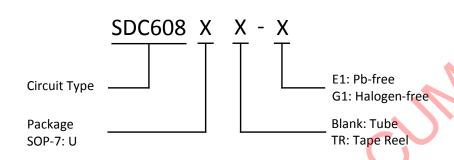


Figure 3. Functional Block Diagram

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Ordering Information



Dealers		Part N	umber	M	arking ID	Da alsina Toma	
Package	Temperature	Pb-free	Halogen-free	Pb-free	Halogen-free	Packing Type	
SOP-7 -40°C ~	7 40°C 95°C	SDC608U-E1	SDC608U-G1	SDC608	SDC608G	Tube	
	-40 C~85 C	SDC608UTR-E1	SDC608UTR-G1	SDC608	SDC608G	Tape Reel	



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Absolute Maximum Ratings (NOTE: Stresses greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device.)

Parameter	Parameter Symbol V		Unit	
Power supply voltage VCC	V_{CC}	18	V	
Collector current	I _C	1.5	A	
Endurance voltage of OC collector	V_{CB}	-0.3~700	V	
Peak value of switching current	I_p	330	mA	
Total dissipation power	P_{D}	460	mW	
Operating Junction Temperature	TJ	-40~150	°C	
Storage temperature range	T_{STG}	-55~150	°C	
Lead temperature (soldering, 10sec)	T_{LEAD}	260,10s	°C	
Latch-up test per JEDEC 78	-	200	mA	
ESD, HBM model per Mil-Std-883, Method 3015	НВМ	2000	V	
ESD, MM model per JEDEC EIA/JESD22-A115	EDEC EIA/JESD22-A115 MM 200		V	

Table 2. Absolute Maximum Ratings

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Power supply voltage, VCC	V _{cc}	4.8	9.0	V
Operating temperature	Ta	-40	85	°C

Table 3. Recommended Operating Conditions



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Electrical Characteristics (Ta=25°C, V_{CC} =7.0V, unless otherwise specified)

Pa	Symbol	Conditions	Min	Тур	Max	Unit	
	Output Section						
On-state saturation vo	V_{SAT}	I _{oc} =290mA	-	-	1	V	
Output rise time		$T_{\rm r}$	C _L =1nF	-	- /	75	ns
Output fall time		T_{f}	C _L =1nF	-	13	75	ns
HV start-up current		${ m I}_{ ext{STC}}$	- 1)- `	2.6	mA
	Os	scillator Sec	tion				
Oscillation frequency		$\mathbf{f}_{ ext{osc}}$	-	55	61	68	kHz
Temperature Stability		$\triangle F_v$	V _{cc} =4.8V~9V	-	-	1	%
Temperature Stability		$\triangle F_{\scriptscriptstyle T}$	Ta=0°C~85°C	-	-	1	%
	Fe	edback Sec	tion				
Input impedance	Pull-up current	${ m I}_{ m FB}$	V _{FB} =2.5V	0.40	0.55	0.70	mA
Input impedance	Pull-down resistance	$R_{\scriptscriptstyle FB}$	-	10	15	20	kΩ
PSRR	PSRR			-	60	70	dB
	Currer	nt Sampling	Section				
Over current threshold	Over current threshold voltage			0.55	0.60	0.65	V
Current limit		$I_{ ext{TH_OC}}$	-	0.27	0.29	0.31	Α
PSRR	PSRR			-	60	70	dB
Over current detection	Over current detection and control delay			-	150	250	ns
		PWM Section	on				
Maximum duty cycle	Maximum duty cycle			52	57	62	%
Minimum duty cycle		$D_{\scriptscriptstyle m MIN}$	-	-	1.5	-	%
	Powe	er Current S	ection				
Startup leakage curren	$I_{ ext{ST}}$	-	-	15	50	uA	
Static operation current		$I_{ ext{OP}}$	V _{FB} =0V, V _{CC} =8V	2.0	2.8	4.0	mA
Startup threshold voltage		V_{ST}	-	8.6	9.0	9.4	V
Minimal operating voltage		$V_{\scriptscriptstyle UV}$	-	3.2	3.5	3.8	V
Restart voltage	V_{RST}	-	1.7	2.0	2.4	V	
Over voltage protection	n	V_{ov}	-	9.6	10	10.6	V



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Parameter	Symbol	Conditions	Min	Тур	Max	Unit		
OTP Section								
Thermal shutdown temperature	Тотр	-	-	150	1	$^{\circ}$		
	BJT Section	1						
Collector cutoff current	$I_{ ext{CBO}}$	V _{CB} =700V, I _E =0	-	1-	0.1	mA		
Collector-emitter cutoff current	I_{CEO}	V _{CE} =400V, I _B =0	- 1	1	0.1	mA		
Collector-base cutoff current	${ m I}_{ m EBO}$	V _{EB} =9V, I _C =0) -	0.1	mA		
Collector-base breakdown voltage	V_{CBO}	I _C =0.1mA	700	-	-	V		
Collector-emitter sustain voltage	V_{CEO}	I _C =1mA	400	-	-	V		
Collector-base sustain voltage	V_{EBO}	I _E =0.1mA	9	-	-	V		
DC current gain	$h_{ ext{FE}}$	V _{CE} =5V, I _C =0.5A	15	-	30	-		
Collector-emitter saturation voltage	V _{CE_STA}	I _C =1A, I _B =0.25A		0.3	0.9	V		
Base-emitter saturation voltage	$V_{\text{BE_STA}}$	I _C =1A, I _B =0.25A		0.9	1.2	V		

Table 4. Electrical Characteristics

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Function Description

Startup control

During Startup phase, reference voltage, the oscillator and all protection circuits are OFF. Startup current of SDC608 is designed to be very low so that VCC could be charged up above UVLO threshold level and device starts up quickly. A large startup resistor can therefore be used to minimize the power loss yet achieve a reliable startup in application.

PWM control

The peak current (sensed on the IS pin) is set by the voltage on FB pin. By comparing the voltage on FB pin and the IS ramp voltage, the duty-cycle of the PWM modulator is thus adjusted to provide the necessary load current at the desired output voltage. FB can be controlled by internal control circuit and external feedback circuit.

VCC over voltage protection

VCC over voltage protection circuit is integrated into IC. When VCC voltage reaches 9.8V(TYP), FB voltage is pulled down via internal control circuit, then the PWM switching is shut off. When VCC voltage goes down below 9.8V(TYP), the switching is reactivated. The VCC over voltage protection ensures IC to operate reliably.

Current limit

The output is shut off to limit the power when voltage of IS Pin exceeds Current sense threshold voltage.

Green mode control

Under no-load and light-load condition, the switching frequency internally decreases to lower the switching power loss and improve the conversion efficiency. If FB is less than 2.7V(Typ), the cycle of the oscillator will increase with it, the less FB is, the wider the cycle of the oscillator is, until the oscillation stop.

Power transistor drive

During the ON cycle, OB pin supplies base current for the power transistor, OE pulls down the emitter of the power transistor to IS, and OB is adaptive to the IS current. If the current of IS exceeds the specified current of FB, SDC608 will turn into the OFF cycle. During the OFF cycle, OB is pulled down, the power transistor will shut off.

Over temperature protection

When IC's internal temperature reaches 150°C, FB voltage will be pulled down by internal control circuit, the switching frequency decreases or shut off. This protection protects the IC from over temperature.

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Typical Application

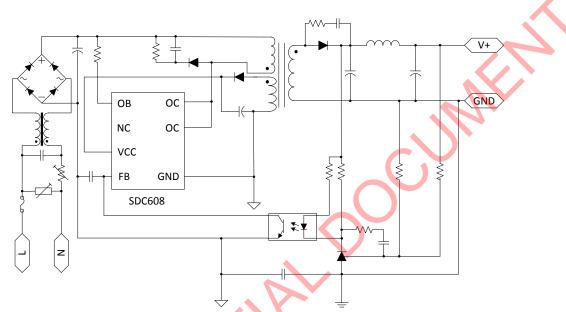
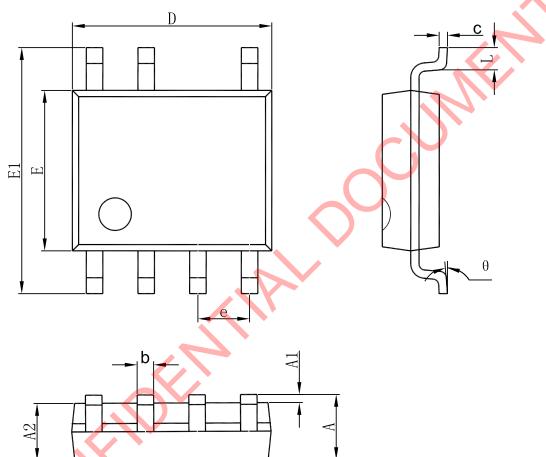


Figure 4. Typical Application

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Package Dimension SOP-7



Symbol	Dimensions In	Dimensions In Millimeters		In Inches	
	Min	Max	Min	Max	
A	1. 350	1. 750	0.053	0.069	
A1	0.100	0. 250	0.004	0.010	
A2	1. 350	1. 550	0.053	0.061	
b	0.330	0. 510	0.013	0.020	
С	0. 170	0. 250	0. 007	0.010	
D	4. 700	5. 100	0. 185	0. 201	
е	1. 270	(BSC)	0. 050 (BSC)		
Е	5.800	6. 200	0. 228	0. 244	
E1	3.800	4. 000	0. 150	0. 157	
L	0.400	1. 270	0.016	0.050	
θ	0°	8°	0°	8°	

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