



#### 2A SCHOTTKY BARRIER RECTIFIER CHIP SCALE PACKAGE

Moisture Sensitivity: Level 1 per J-STD-020

Weight: 0.001 grams (Approximate)

Low forward voltage (V<sub>F</sub>) minimizes conduction losses and

Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation. Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3)

Terminals: NiAu Bump. Solderable per MIL-STD-202, Method

**Features and Benefits** 

improves efficiency.

**Mechanical Data** 

208 @4)

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Case: X3-WLB1608-2

Polarity: Cathode Dot

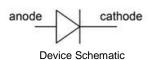
### **Product Summary**

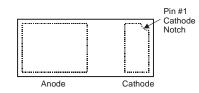
V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F max</sub> (V)	I <sub>R max</sub> (μΑ)
30	2.0	0.48	150

## **Description and Applications**

The SDM2U30CSP is a 30-volt 2A Schottky Barrier Rectifier that is optimized for low forward voltage drop and low leakage current, housed in a compact chip scale package (CSP) that occupies only 1.28mm<sup>2</sup> board space with low profile. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space. It is ideally suited for use in portable applications as a:

- Blocking Diode
- Boost Diode
- Switching Diode
- Reverse Protection Diode





### Ordering Information (Note 4)

Part Number	Case	Packaging
SDM2U30CSP-7B	X3-WLB1608-2	10,000/Tape & Reel
SDM2U30CSP-7	X3-WLB1608-2	5,000/Tape & Reel

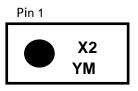
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html. SDM2U30CSP-7B uses carrier tapes with 2mm pocket-to-pocket pitch; SDM2U30CSP-7 uses carrier tapes with 4mm pocket-to-pocket pitch.

# **Marking Information**



X2= Product Type Marking Code YM=Date Code Marking Y= Year (ex: C= 2015) M=Month (ex: 9= September) Dot Denotes Cathode Pin

Date Code Key

Year	201	4	2015		2016	20	17	2018		2019		2020
Code	В		С		D	E		F		G		Н
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	30	V
Average Rectified Output Current	lo	2.0	A
Repetitive Peak Forward Current (Pulse Wave = 1 Sec, Duty Cycle = 66%)	I <sub>FRM</sub>	4.2	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	20	A

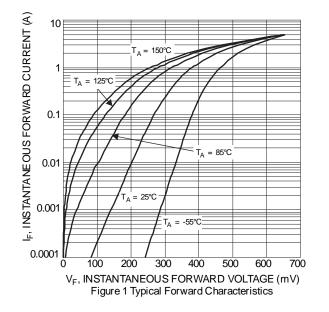
# **Thermal Characteristics**

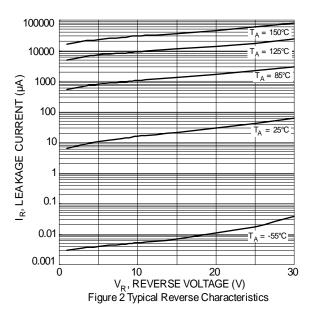
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	155	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

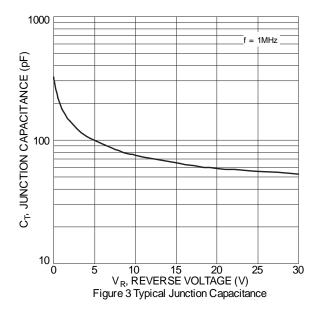
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Valtage Drep	N/	—	0.38	0.42	V	I <sub>F</sub> = 1.0A
Forward Voltage Drop	VF	—	0.45	0.48		I <sub>F</sub> = 2.0A
Reverse Current (Note 6)	I <sub>R</sub>	—	—	150	μA	V <sub>R</sub> = 30V
Junction Capacitance	CJ	—	110	_	pF	$V_{R} = 4V, f = 1.0MHz$

Notes: 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf. 6. Short duration pulse test used to minimize self-heating effect.



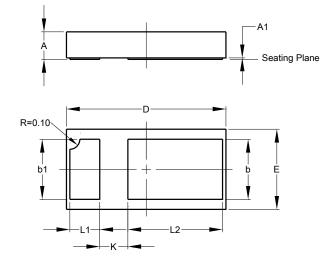






# **Package Outline Dimensions**

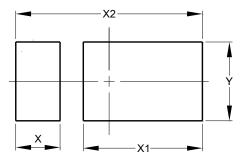
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



X3-WLB1608-2						
Dim	Min	Max	Тур			
Α	0.250	0.300	0.275			
A1	-	0.015	-			
b	-	-	0.600			
b1	-	-	0.600			
D	1.57	1.63	1.60			
Е	0.77	0.83	0.80			
Κ	-	-	0.282			
L1	0.25	0.35	0.30			
L2	0.90	1.00	0.95			
All Dimensions in mm						

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
Х	0.385		
X1	1.035		
X2	1.622		
Y	0.690		



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