



SDM40E20L /S /C /A

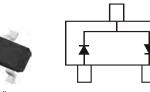
DUAL SURFACE MOUNT SCHOTTKY BARRIER DIODE

Features

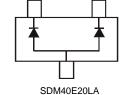
- Very Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- High Conductance
- Lead, Halogen and Antimony Free, RoHS Compliant "Green" Device (Notes 4, 5 and 6)

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Polarity: See Diagram
- Leads: Solderable per MIL-STD-202, Method 208
- Terminals: SDM40E20L/S/A Finish --- Matte Tin Finish annealed over Alloy 42 leadframe. SDM40E20LC Finish — Matte Tin Finish annealed over CDA194 leadframe.
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.008 grams (approximate)







Top View

SDM40E20LS

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.			
Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	20	V
RMS Reverse Voltage	V _{R(RMS)}	14	V
Forward Continuous Current (Note 1)	I _{FM}	0.4	A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	2	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	Π_	225	mW	
	(Note 2)	PD	300	11100
Typical Thermal Resistance Junction to Ambient (Note 1)		Р	444	°C/W
	(Note 2)	$R_{ extsf{ heta}JA}$	333	C/VV
Operating and Storage Temperature Range		T _J , T _{STG}	-65 to +125	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 3)	V _{(BR)R}	20	_	_	V	$I_{R} = 0.5 mA$
Forward Voltage Drop	\/-	_	_	0.310	V	I _F = 0.1A
Polward voltage Drop	VF			0.430	v	I _F = 0.5A
Leakage Current (Note 3)	I _R			100	۸	V _R = 10V
Leakage Current (Note 3)				250	μA	V _R = 20V
Total Capacitance	CT		120	_	pF	$f = 1MHz, V_R = 0VDC$

1. Device mounted on FR-5 1.0 x 0.75 x 0.062 inch PCB pad layout. Notes:

2. Device mounted on Alumina PCB, 0.4 inch x 0.3 inch x 0.024 inch pad layout.

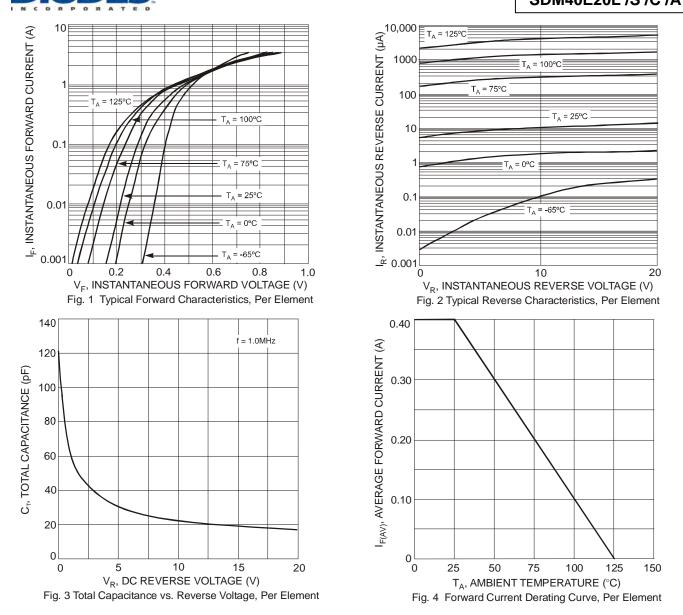
3. Short duration pulse test used to minimize self-heating effect.

No purposefully added lead. Halogen and Antimony Free.
Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

6. Product manufactured with Green Molding Compound and does not contain Halogens or Sb₂O₃ Fire Retardants.



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Ordering Information (Note 7)

Part Number	Case	Packaging
SDM40E20LS-7-F	SOT-23	3000/Tape & Reel
SDM40E20LC-7	SOT-23	3000/Tape & Reel
SDM40E20LA-7	SOT-23	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

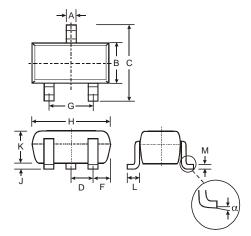
Marking Information

		_		_									
KSW	ΥM	Ty YM = Date	DM40E20LS pe Marking (e Code Marki ex: T = 2006	Code	KWS	×	WS = SDM4 Type M = Date Co = Year ex: T	Marking Co ode Marking	de	KWA	×	KWA = SDM40E2 Type Mar YM = Date Code Y =Year ex: T = 2	king Code Marking
			n ex: 9 = Sep	tember			M = Month ex		mber			M = Month ex: 9 =	
Date Code	Key												
Year		2005	2006	2007	2008	200	09 20	10 2	2011	2012	2013	2014	2015
Code	•	S	Т	U	V	W	/ >	<	Y	Z	А	В	С
Month	h	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oc	t Nov	Dec
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SDM40E20L /S /C /A Document number: DS30298 Rev. 11 - 2

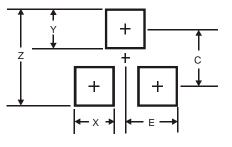


Package Outline Dimensions



SOT-23					
Dim	Min	Max			
Α	0.37	0.51			
В	1.20	1.40			
С	2.30	2.50			
D	0.89	1.03			
F	0.45	0.60			
G	1.78	2.05			
н	2.80	3.00			
J	0.013	0.10			
κ	0.903	1.10			
L	0.45	0.61			
М	0.085	0.180			
α	0°	8°			
All Dir	nensions	in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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