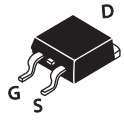
# SDP/B85N03L

## N-Channel Logic Level Enhancement Mode Field Effect Transistor

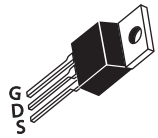
| PRODUCT SUMMARY | | |
|---|---|---|
| $V_{DSS}$ | $I_D$ | $R_{DS(on)}$ (mΩ) Max |
| 30V | 83A | 5 @ $V_{GS}$ = 10V |
| | | 7.5 @ $V_{GS}$ = 4.5V |

### FEATURES

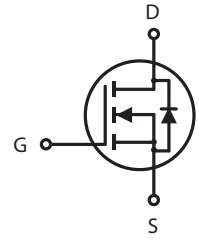
- Super high dense cell design for extremely low RDS(ON).
- High power and current handling capability.
- TO-220 & TO-263 package.




SDB SERIES
TO-263(DD-PAK)




SDP SERIES
TO-220




### ABSOLUTE MAXIMUM RATINGS (TC=25°C unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|---|---|---|
| Drain-Source Voltage | $V_{DS}$ | 30 | V |
| Gate-Source Voltage | $V_{GS}$ | ±20 | V |
| Drain Current-Continuous @TJ=125°C | $I_D$ | 83 | A |
| -Pulsed [a] | $I_{DM}$ | 249 | A |
| Drain-Source Diode Forward Current | $I_S$ | 75 | A |
| Maximum Power Dissipation @Tc=25°C | $P_D$ | 75 | W |
| Operating and Storage Temperature Range | $T_J$, $T_{STG}$ | -65 to 175 | °C |
| **THERMAL CHARACTERISTICS** | | | |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 2 | °C/W |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 62.5 | °C/W |

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## ELECTRICAL CHARACTERISTICS (Tc=25 °C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| OFF CHARACTERISTICS | | | | | | |
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | $V_{GS}$=0V, $I_D$=250uA | 30 | | | V |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$=24V, $V_{GS}$ =0V | | | 10 | uA |
| Gate-Body Leakage | $I_{GSS}$ | $V_{GS}$ = ±16V, $V_{DS}$=0V | | | ±100 | nA |
| ON CHARACTERISTICS [a] | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$=$V_{GS}$, $I_D$ = 250uA | 1 | 1.5 | 3 | V |
| Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS}$ = 10V, $I_D$ = 37.5A | | 4 | 5 | m ohm |
| | | $V_{GS}$ = 4.5V, $I_D$ = 30A | | 5.5 | 7.5 | m ohm |
| On-State Drain Current | $I_{D(ON)}$ | $V_{GS}$ = 10V, $V_{DS}$ = 10V | 75 | | | A |
| Forward Transconductance | $g_{FS}$ | $V_{DS}$ = 10V, $I_D$ = 37A | | 56 | | S |
| DYNAMIC CHARACTERISTICS [b] | | | | | | |
| Input Capacitance | $C_{ISS}$ | $V_{DS}$ =15V, $V_{GS}$ = 0V<br>f =1.0MHz | | 3800 | | pF |
| Output Capacitance | $C_{OSS}$ | | | 1750 | | pF |
| Reverse Transfer Capacitance | $C_{RSS}$ | | | 390 | | pF |
| SWITCHING CHARACTERISTICS [b] | | | | | | |
| Turn-On Delay Time | $t_{D(ON)}$ | $V_{DD}$ = 15V,<br>$I_D$ = 1A,<br>$V_{GS}$= 10V<br>$V_{GEN}$ = 60 ohm | | 55 | | ns |
| Rise Time | $t_r$ | | | 268 | | ns |
| Turn-Off Delay Time | $t_{D(OFF)}$ | | | 363 | | ns |
| Fall Time | $t_f$ | | | 233 | | ns |
| Total Gate Charge | $Q_g$ | $V_{DS}$ =15V, $I_D$ =85A,$V_{GS}$ =10V | | 95.2 | | nC |
| | | $V_{DS}$ =15V, $I_D$ =85A,$V_{GS}$ =4.5V | | 45 | | nC |
| Gate-Source Charge | $Q_{gs}$ | $V_{DS}$ =15V, $I_D$ = 85A,<br>$V_{GS}$ =10V | | 7.2 | | nC |
| Gate-Drain Charge | $Q_{gd}$ | | | 5.6 | | nC |

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## ELECTRICAL CHARACTERISTICS ($T_C$=25°C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| DRAIN-SOURCE DIODE CHARACTERISTICS [a] | | | | | | |
| Diode Forward Voltage | $V_{SD}$ | $V_{GS}$ = 0V, Is =37.5A | | 0.9 | 1.3 | V |

Notes

a.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.
b.Guaranteed by design, not subject to production testing.

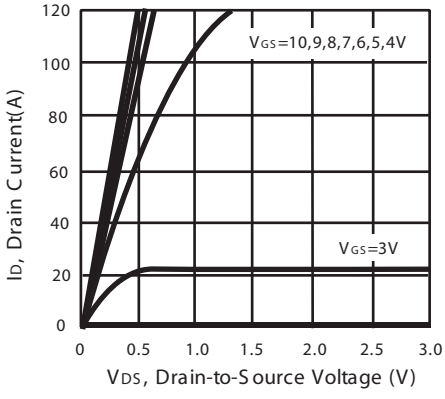


Figure 1. Output Characteristics

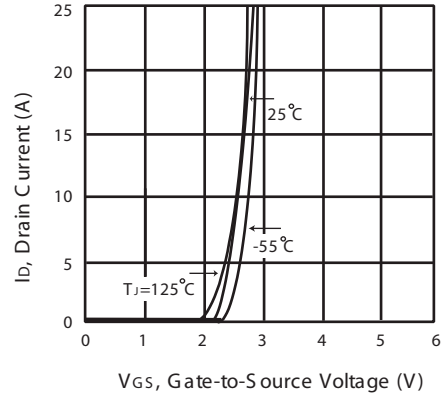


Figure 2. Transfer Characteristics

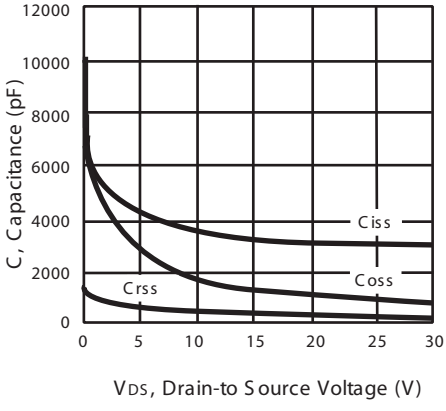


Figure 3. Capacitance

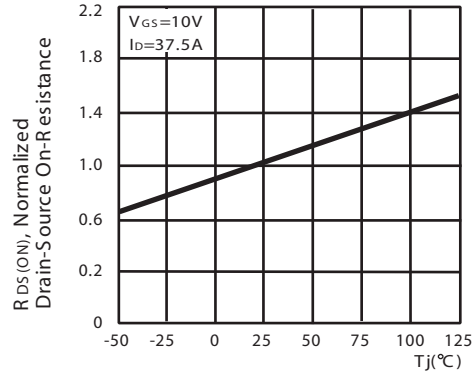


Figure 4. On-Resistance Variation with Temperature

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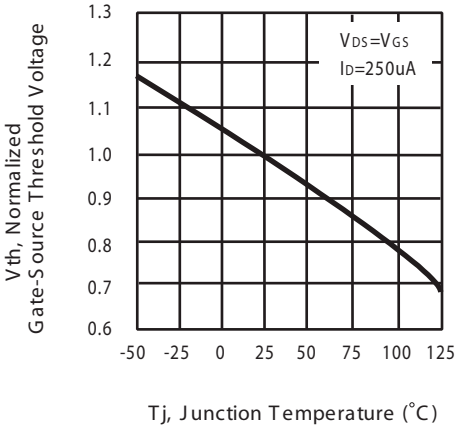


Figure 5. Gate Threshold Variation with Temperature

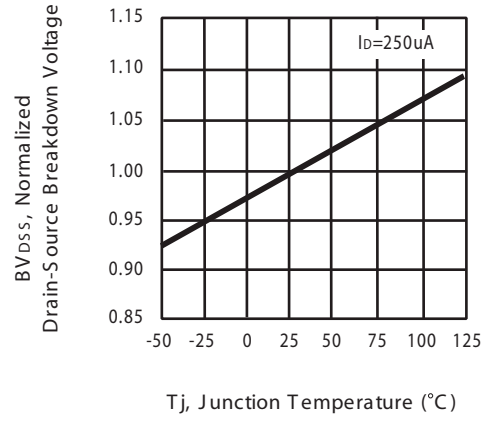


Figure 6. Breakdown Voltage Variation with Temperature

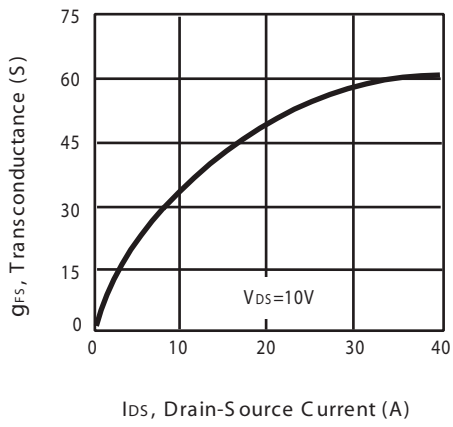


Figure 7. Transconductance Variation with Drain Current

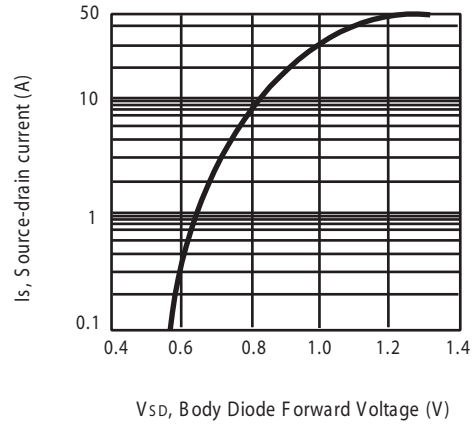


Figure 8. Body Diode Forward Voltage Variation with Source Current

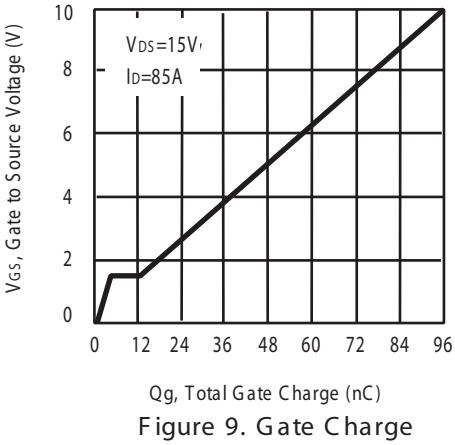


Figure 9. Gate Charge

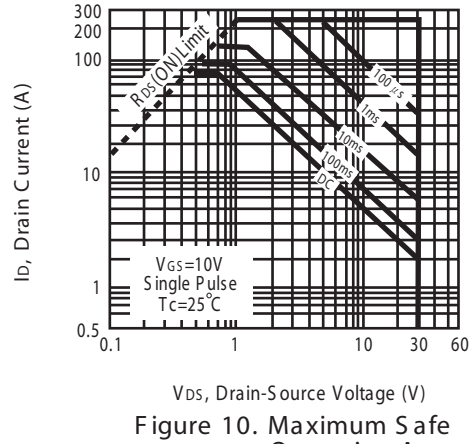


Figure 10. Maximum Safe Operating Area

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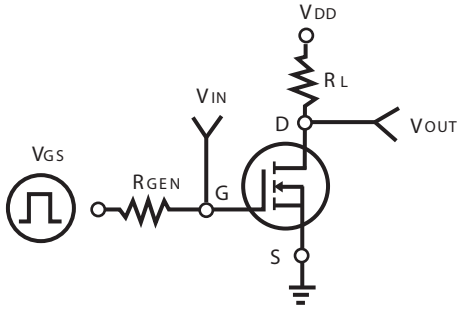


Figure 11. Switching Test Circuit

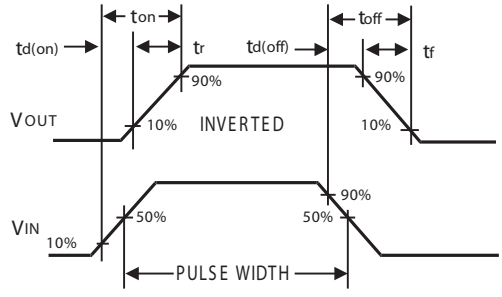


Figure 12. Switching Waveforms

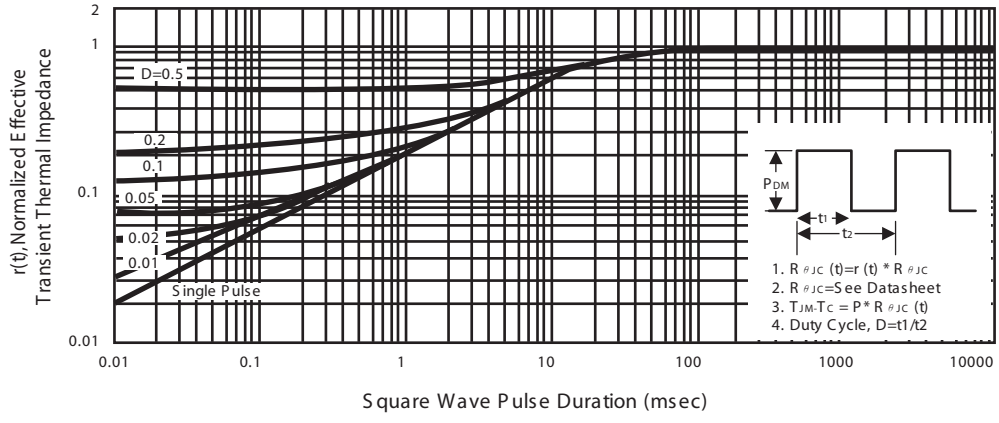


Figure 13. Normalized Thermal Transient Impedance Curve

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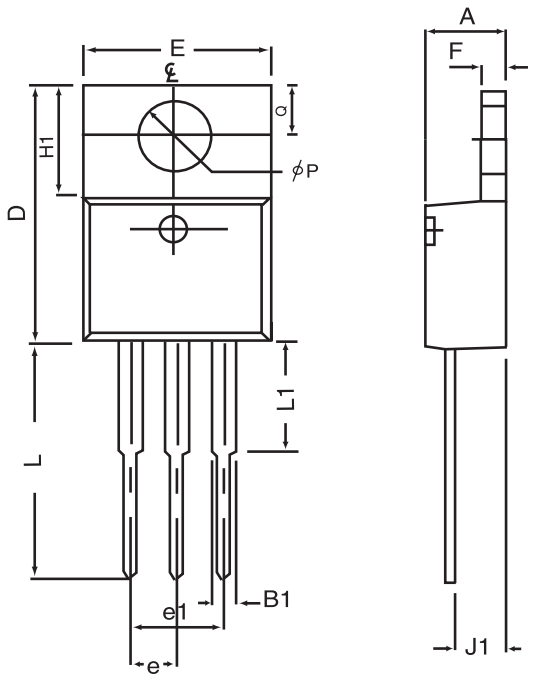
## PACKAGE OUTLINE DIMENSIONS

### TO-220




| SYMBOLS | MILLIMETERS | | INCHES | |
|---|---|---|---|---|
| | MIN | MAX | MIN | MAX |
| A | 4.32 | 4.80 | 0.170 | 0.189 |
| B1 | 1.27 | 1.65 | 0.050 | 0.630 |
| D | 14.6 | 16.00 | 0.575 | 0.610 |
| E | 9.70 | 10.41 | 0.382 | 0.410 |
| e | 2.34 | 2.74 | 0.092 | 0.108 |
| e1 | 4.68 | 5.48 | 0.184 | 0.216 |
| F | 1.14 | 1.40 | 0.045 | 0.055 |
| H1 | 5.97 | 6.73 | 0.235 | 0.265 |
| J1 | 2.20 | 2.79 | 0.087 | 0.110 |
| L | 12.88 | 14.22 | 0.507 | 0.560 |
| L1 | 3.00 | 6.35 | 0.120 | 0.250 |
| φP | 3.50 | 3.94 | 0.138 | 0.155 |
| Q | 2.54 | 3.05 | 0.100 | 0.120 |