

# Small Signal Zener Diode

#### **General Description**

These diodes small signal Zener diodes, fabricated in planar technology. Miniature surface mount package is excellent for hand-held and portable applications where is space is limited.

#### **Features and Benefits**

- Silicon epitaxial planar diode
- · Low Zener impedance and low leakage current
- Standard Zener voltage tolerance is 5%.
- "Green" device and RoHS compliant device

### **Applications**

· Voltage regulator

#### **Ordering Information**

Part Number	Marking Code	Package	Packaging
SDZ36V	Y6 🗆	SOT-23	Tape & Reel

#### **Marking Information**

Y6 □

Y 6 = Specific Device Code

□ = Year & Week Code Marking

#### **Pinning Information**

Pin	Description	Simplified Outline	Graphic Symbol
1	Anode	3	3
2	Not Connected		*
3	Cathode	1 2	



**SOT-23** 





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### Absolute Maximum Ratings (Tamb=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Power dissipation <sup>1)</sup>	P <sub>D</sub>	200	mW
Operating junction temperature	TJ	150	°C
Storage temperature range	T <sub>stg</sub>	-55℃ to +150℃	°C

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

#### **Thermal Characteristics** (T<sub>amb</sub>=25°C, Unless otherwise specified)

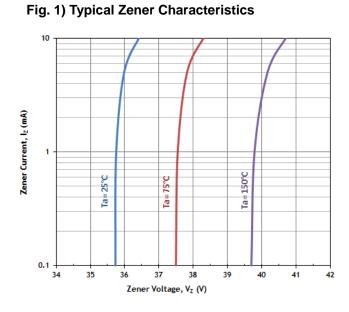
Characteristic	Symbol	Ratings	Unit	
Thermal resistance, junction to ambient <sup>1)</sup>	$R_{th(j-a)}$	625	°C/W	

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

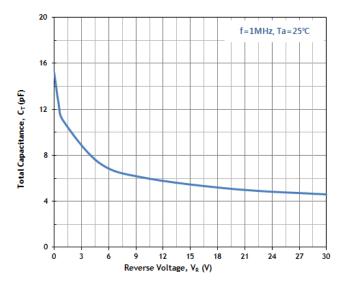
#### **Electrical Characteristics** (T<sub>amb</sub>=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Zener voltage	Vz	I <sub>Z</sub> =5mA	34.2	-	37.8	V
Dynamic impedance	Z <sub>ZT</sub>	I <sub>Z</sub> =5mA	-	-	95	Ω
KNEE dynamic impedance	Z <sub>ZK</sub>	I <sub>Z</sub> =0.25mA	-	-	700	Ω
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =27V	-	-	0.1	μA

### **Rating and Characteristic Curves**



#### Fig. 3) Typical Capacitance Characteristics



#### Fig. 2) Typical Reverse Characteristics

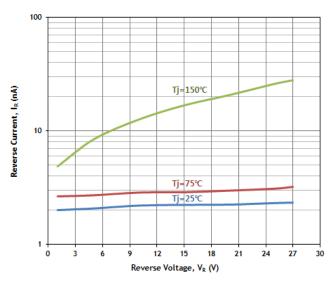
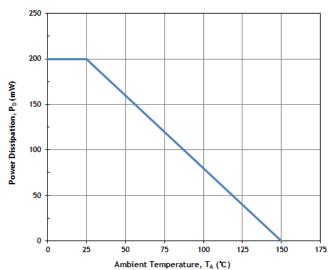
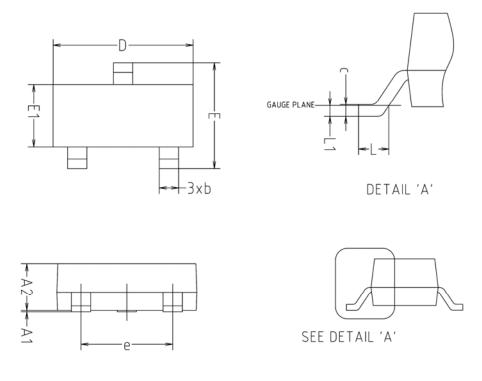


Fig. 4) Power Dissipation vs. Ambient Temperature

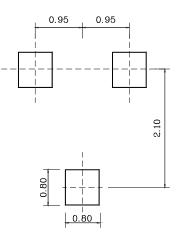


### Package Outline Dimensions



SYMBOL		NOTE		
STIDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
с	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

#### **※** Recommend PCB solder land (Unit : mm)



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