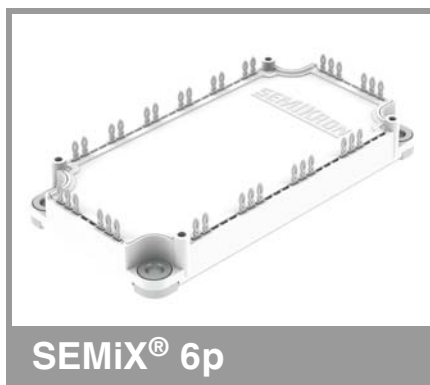


# SEMiX156GD12T4p



## Trench IGBT Modules

### SEMiX156GD12T4p

#### Features\*

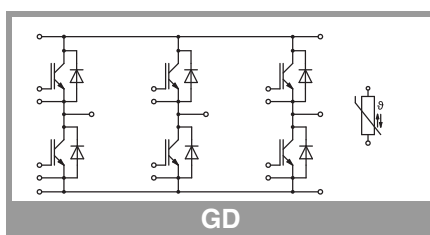
- Press Fit
- Homogeneous Si
- Trench = Trenchgate technology
- $V_{CE(sat)}$  with positive temperature coefficient
- High short circuit capability
- UL recognised file no. E63532

#### Typical Applications

- AC inverter drives
- UPS
- Electronic Welding

#### Remarks

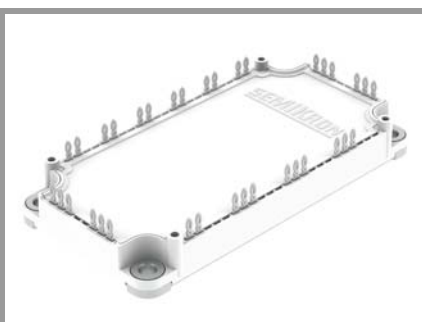
- Case temperature limited to  $T_C = 125^\circ\text{C}$  max.
- $V_{isol}$  between temperature sensor and power section is only 2500V
- Product reliability results valid for  $T_j \leq 150^\circ\text{C}$  (recommended  $T_{jop} = -40 \dots 150^\circ\text{C}$ )



Absolute Maximum Ratings				
Symbol	Conditions		Values	Unit
IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V
I <sub>C</sub>	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	239	A
		T <sub>c</sub> = 80 °C	184	A
I <sub>Cnom</sub>			150	A
I <sub>CRM</sub>	I <sub>CRM</sub> = 3 x I <sub>Cnom</sub>		450	A
V <sub>GES</sub>			-20 ... 20	V
t <sub>psc</sub>	V <sub>CC</sub> = 800 V V <sub>GE</sub> ≤ 20 V V <sub>CES</sub> ≤ 1200 V	T <sub>j</sub> = 150 °C	10	μs
T <sub>j</sub>			-40 ... 175	°C
Inverse diode				
V <sub>RRM</sub>	T <sub>j</sub> = 25 °C		1200	V
I <sub>F</sub>	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	181	A
		T <sub>c</sub> = 80 °C	136	A
I <sub>Fnom</sub>			150	A
I <sub>FRM</sub>	I <sub>FRM</sub> = 2xI <sub>Fnom</sub>		300	A
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms, sin 180°, T <sub>j</sub> = 25 °C		900	A
T <sub>j</sub>			-40 ... 175	°C
Module				
I <sub>t(RMS)</sub>	per connector pin		50	A
T <sub>stg</sub>			-40 ... 125	°C
V <sub>isol</sub>	AC sinus 50Hz, t = 1 min		4000	V

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V <sub>CE(sat)</sub>	I <sub>C</sub> = 150 A	T <sub>j</sub> = 25 °C		1.80	2.05	V
	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		2.10	2.40	V
V <sub>CE0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.8	0.9	V
		T <sub>j</sub> = 150 °C		0.7	0.8	V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		6.7	7.7	mΩ
	chiplevel	T <sub>j</sub> = 150 °C		9.3	10.7	mΩ
V <sub>GE(th)</sub>	V <sub>GE</sub> =V <sub>CE</sub> , I <sub>C</sub> = 6 mA		5	5.8	6.5	V
I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>j</sub> = 25 °C				2.0	mA
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		9.3		nF
C <sub>oes</sub>		f = 1 MHz		0.58		nF
C <sub>res</sub>		f = 1 MHz		0.51		nF
Q <sub>G</sub>	V <sub>GE</sub> = - 8 V...+ 15 V			850		nC
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			5.0		Ω
t <sub>d(on)</sub>	V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C		151		ns
t <sub>r</sub>	I <sub>C</sub> = 150 A	T <sub>j</sub> = 150 °C		32		ns
E <sub>on</sub>	V <sub>GE</sub> = +15/-15 V	T <sub>j</sub> = 150 °C		11		mJ
	R <sub>G on</sub> = 1.1 Ω	T <sub>j</sub> = 150 °C				
t <sub>d(off)</sub>	R <sub>G off</sub> = 1.1 Ω	T <sub>j</sub> = 150 °C		408		ns
t <sub>f</sub>	di/dt <sub>on</sub> = 4950 A/μs	T <sub>j</sub> = 150 °C		76		ns
	di/dt <sub>off</sub> = 1600 A/μs	T <sub>j</sub> = 150 °C				
E <sub>off</sub>	dv/dt = 3500 V/μs					
	L <sub>s</sub> = 25 nH				17	mJ
R <sub>th(j-c)</sub>	per IGBT				0.18	K/W
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0.81 W/(m*K))			0.04		K/W

# SEMiX156GD12T4p



SEMiX® 6p

## Trench IGBT Modules

### SEMiX156GD12T4p

#### Features\*

- Press Fit
- Homogeneous Si
- Trench = Trenchgate technology
- $V_{CE(sat)}$  with positive temperature coefficient
- High short circuit capability
- UL recognised file no. E63532

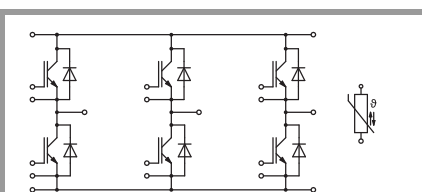
#### Typical Applications

- AC inverter drives
- UPS
- Electronic Welding

#### Remarks

- Case temperature limited to  $T_C=125^{\circ}\text{C}$  max.
- $V_{isol}$  between temperature sensor and power section is only 2500V
- Product reliability results valid for  $T_j \leq 150^{\circ}\text{C}$  (recommended  $T_{jop} = -40 \dots 150^{\circ}\text{C}$ )

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 150 A	T <sub>j</sub> = 25 °C		2.14	2.46	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		2.07	2.38	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.3	1.50	V
		T <sub>j</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		5.6	6.4	mΩ
		T <sub>j</sub> = 150 °C		7.8	8.5	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 150 A	T <sub>j</sub> = 150 °C		235		A
Q <sub>rr</sub>	di/dt <sub>off</sub> = 5000 A/μs	T <sub>j</sub> = 150 °C		26.5		μC
E <sub>rr</sub>	V <sub>GE</sub> = -15 V V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C		11.5		mJ
R <sub>th(j-c)</sub>	per diode				0.33	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0.81 W/(m*K))			0.05		K/W
Module						
L <sub>CE</sub>				18		nH
R <sub>CC'+EE'</sub>	measured per switch	T <sub>C</sub> = 25 °C		1		mΩ
		T <sub>C</sub> = 125 °C		1.4		mΩ
R <sub>th(c-s)1</sub>	calculated without thermal coupling (λ <sub>grease</sub> =0.81 W/(m*K))			0.004		K/W
R <sub>th(c-s)2</sub>	including thermal coupling, T <sub>s</sub> underneath module (λ <sub>grease</sub> =0.81 W/ (m*K))			0.006		K/W
M <sub>s</sub>	to heat sink (M5)		3		6	Nm
M <sub>t</sub>				-		Nm
				-		Nm
w				300		g
Temperature Sensor						
R <sub>100</sub>	T <sub>C</sub> =100°C (R <sub>25</sub> =5 kΩ)			493 ± 5%		Ω
B <sub>100/125</sub>	R(T)=R <sub>100</sub> exp[B <sub>100/125</sub> (1/T-1/T <sub>100</sub> )]; T[K];			3550 ±2%		K



GD

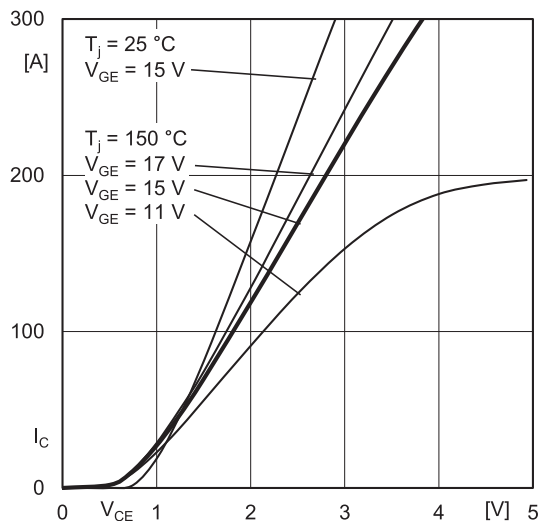


Fig. 1: Typ. output characteristic, inclusive  $R_{CC'+EE'}$

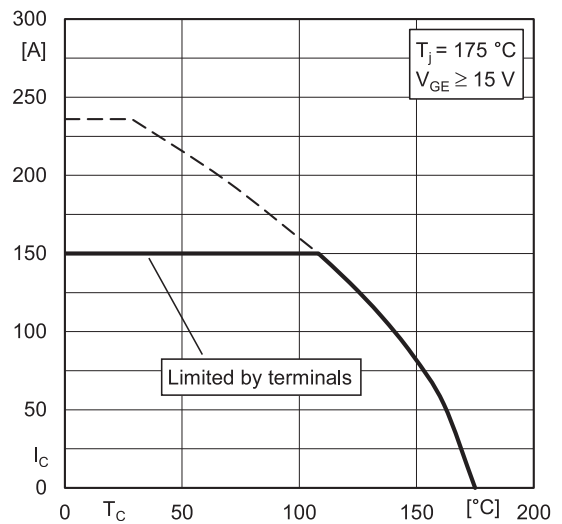


Fig. 2: Rated current vs. temperature  $I_C = f(T_C)$

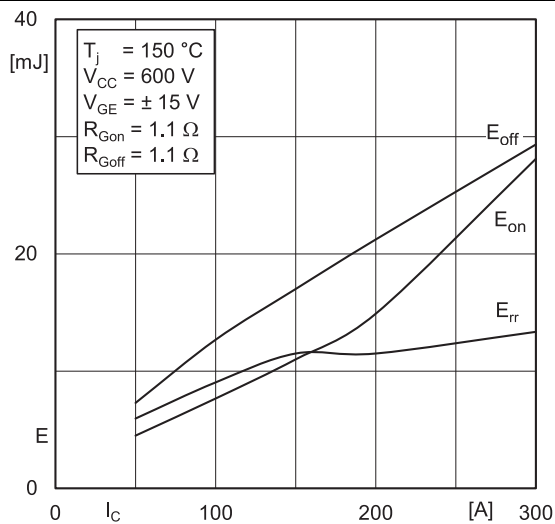


Fig. 3: Typ. turn-on /-off energy =  $f(I_C)$

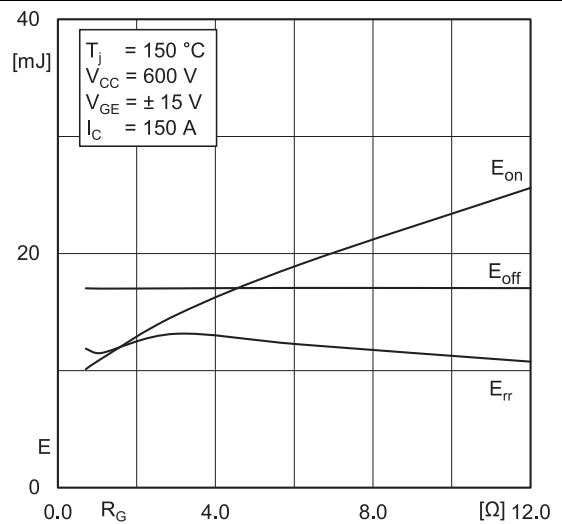


Fig. 4: Typ. turn-on /-off energy =  $f(R_G)$

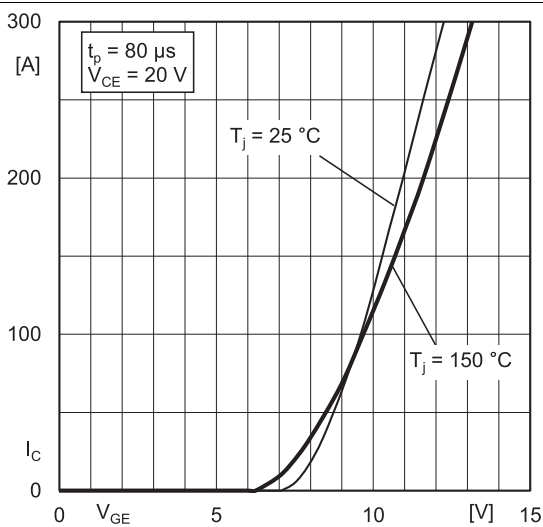


Fig. 5: Typ. transfer characteristic

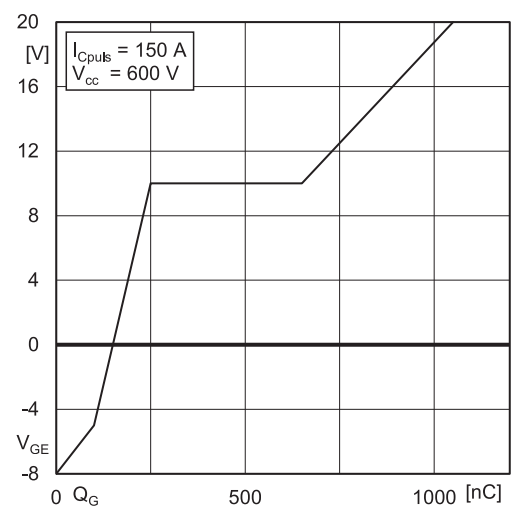


Fig. 6: Typ. gate charge characteristic

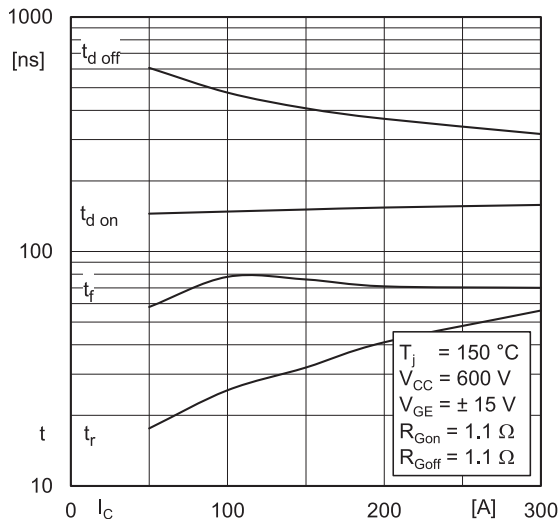


Fig. 7: Typ. switching times vs.  $I_C$

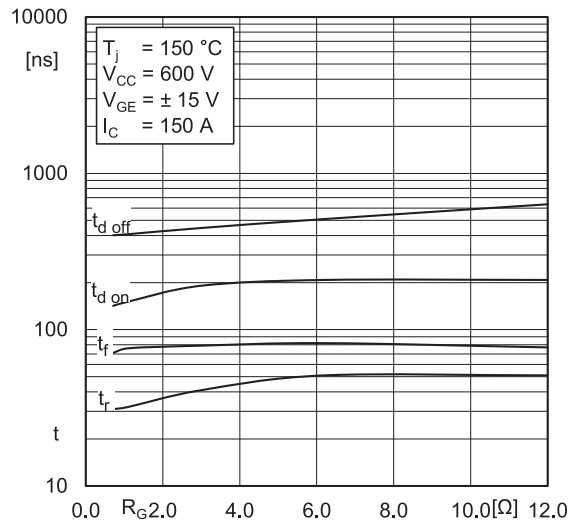


Fig. 8: Typ. switching times vs. gate resistor  $R_G$

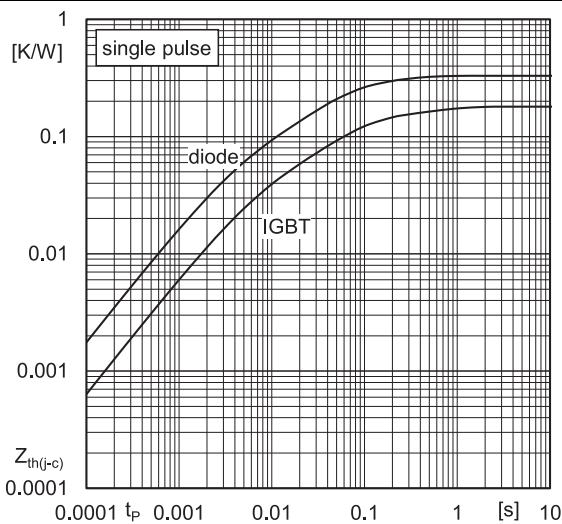


Fig. 9: Transient thermal impedance

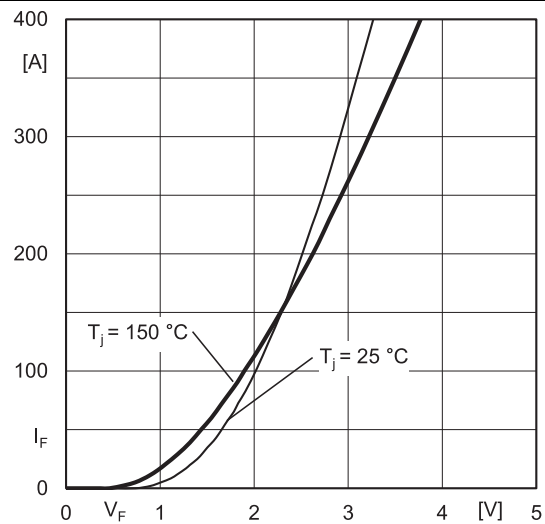


Fig. 10: Typ. CAL diode forward charact., incl.  $R_{CC'+EE'}$

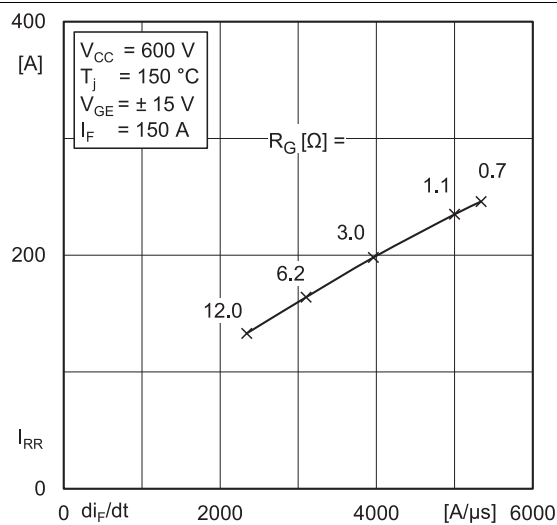


Fig. 11: Typ. CAL diode peak reverse recovery current

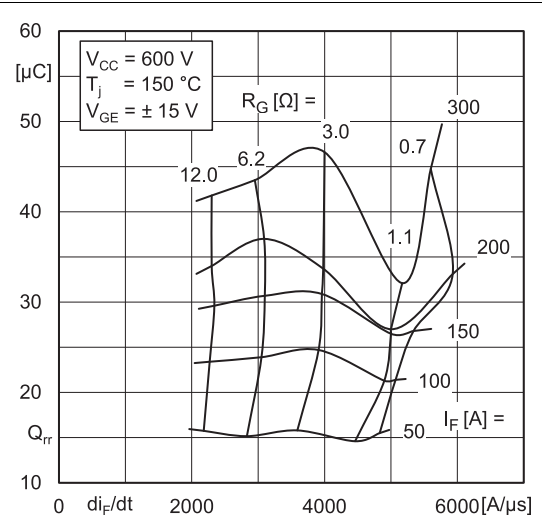
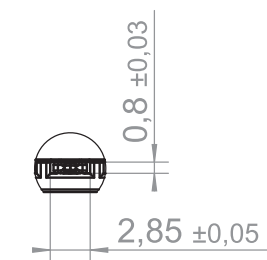
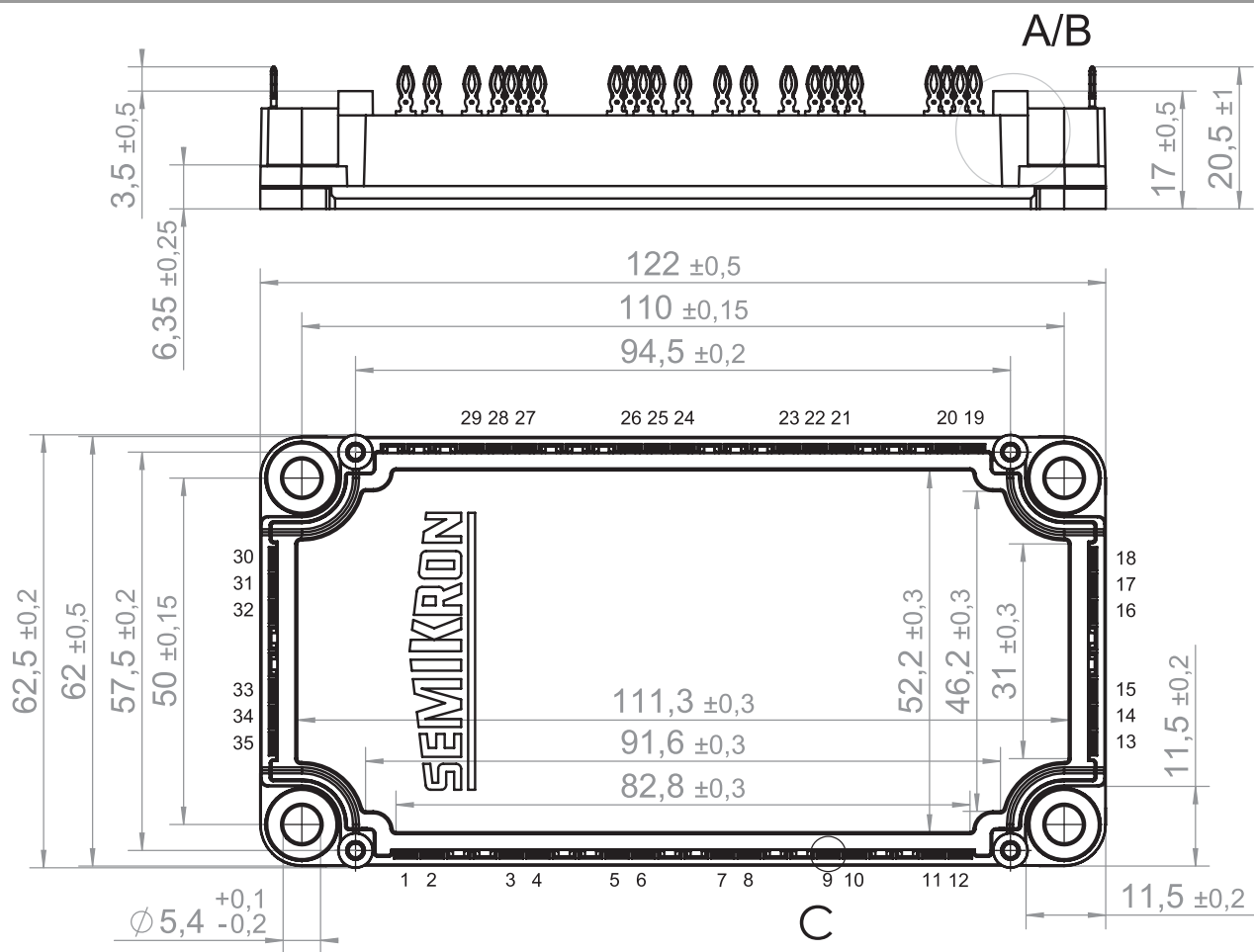
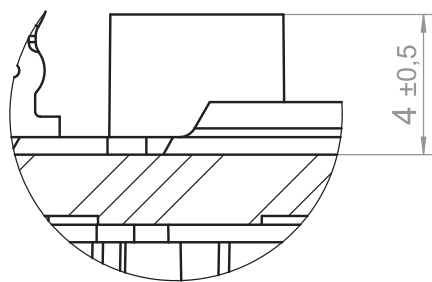


Fig. 12: Typ. CAL diode recovery charge

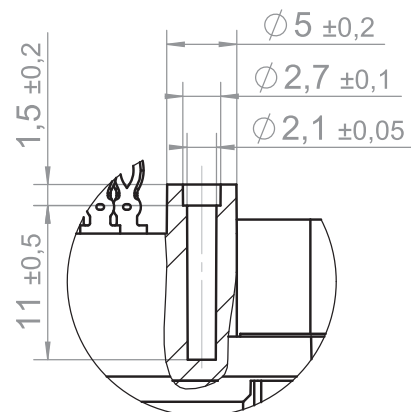


C (2 : 1)



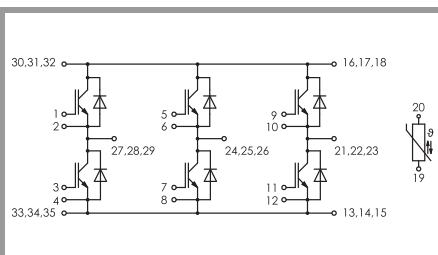
B (5 : 1)

Cross-sectional plane in the middle of the module

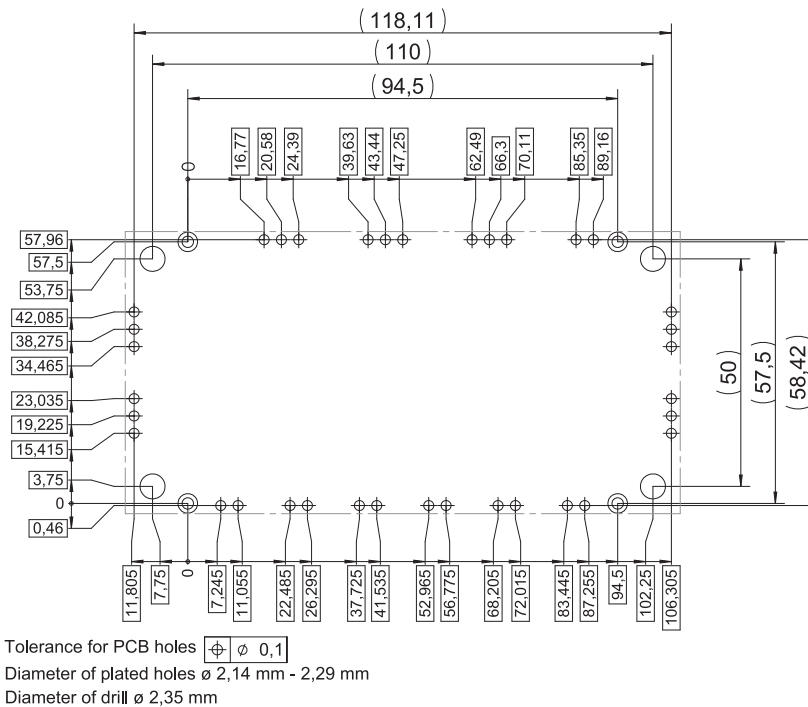


A (2 : 1)

Cut-out shows section through the center of the PCB-dome



pinout



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

## \*IMPORTANT INFORMATION AND WARNINGS

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