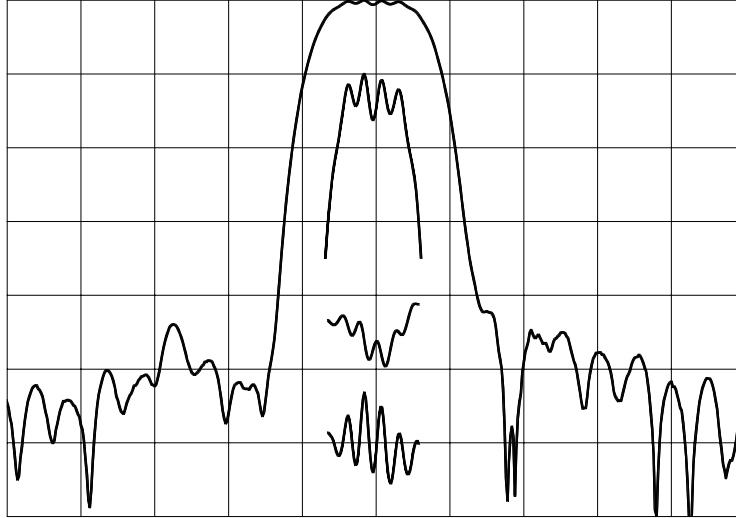




## TYPICAL PERFORMANCE



Horizontal: 1 MHz/div    Vertical (from top): Magnitude    10.1 dB/div  
 Phase Deviation    10 deg/div  
 Group Delay Variation    250 ns/div

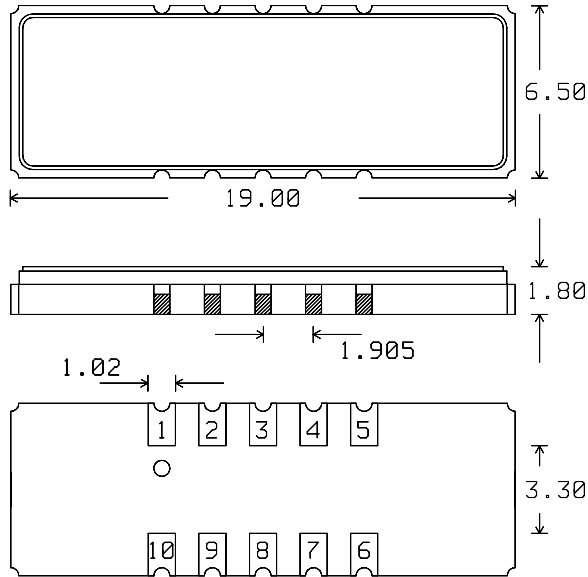
## SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (Fc) <sup>1</sup>	69.92	70.00	70.08	MHz
Insertion Loss		7.5	8.5	dB
1 dB Bandwidth	0.70	1.09		MHz
3 dB Bandwidth	1.00	1.40		MHz
40 dB Bandwidth		2.7	3.0	MHz
Passband Ripple		0.6	1.0	dB
Phase Deviation from Linear <sup>2</sup>		5.5	12	deg
Group Delay Variation <sup>2</sup>		320	500	ns
Absolute Delay		2.1		μs
Substrate		LiTaO <sub>3</sub>		-
Temperature Coefficient of Frequency (Tc) <sup>3</sup>		-23		ppm/°C
Ambient Temperature		25		°C
System Source and Load Impedance		50		Ω

- Notes: 1. Average of lower & upper 3 dB frequencies.  
 2. Evaluated over 60% of the 3 dB bandwidth.  
 3. Typical change of filter frequency response with temperature is  $\Delta f/f_{ref} = (T-T_{ref}) \cdot Tc$  ppm.



**PACKAGE OUTLINE**

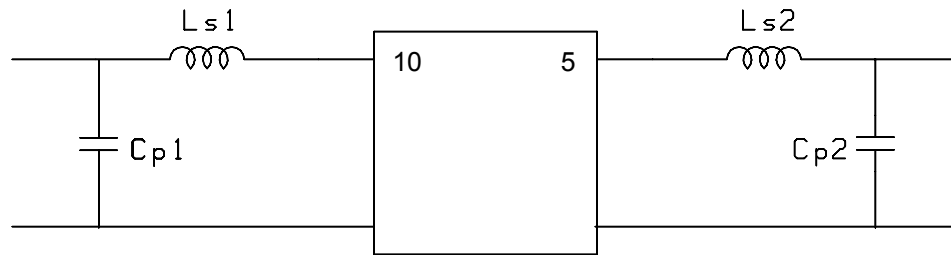


Units: mm

**Pin Configuration:**

Input: 10  
Output: 5  
Ground: 1,2,3,4,6,7,8,9

**MATCHING CIRCUIT**



Component values in 50 Ω:  
(Minimum Q = 40)

Ls1 = 150 nH  
Cp1 = 68 pF

Ls2 = 150 nH  
Cp2 = 68 pF

**Notes**

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.

