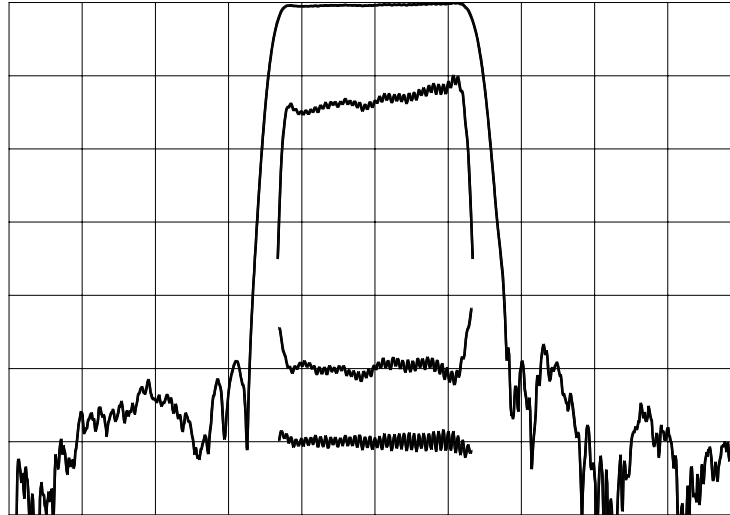




## TYPICAL PERFORMANCE



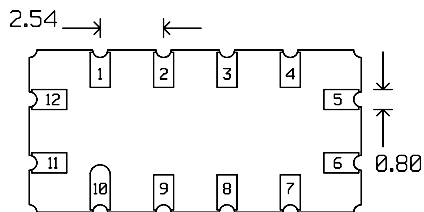
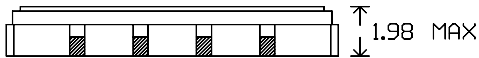
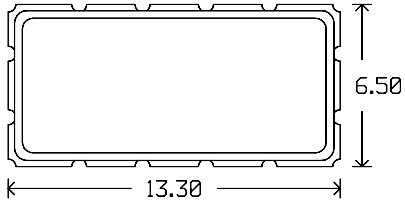
Horizontal: 7 MHz/div      Vertical (from top):      Magnitude      10 dB/div  
 Magnitude      1 dB/div  
 Phase Deviation      5 deg/div  
 Group Delay Variation 100 ns/div

## SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (Fc) <sup>1</sup>	69.8	70	70.2	MHz
Insertion Loss		13.7	14.5	dB
1 dB Bandwidth	17.25	18.0		MHz
3 dB Bandwidth	18	19.0		MHz
40 dB Bandwidth		24.0	24.5	MHz
Passband Ripple		0.6	1	dB
Phase Deviation from Linear <sup>2</sup>		2	12	deg
Group Delay Variation <sup>2</sup>		30	100	ns
Absolute Delay		1.07		μs
Substrate		LiNbO <sub>3</sub>		-
Temperature Coefficient of Frequency (Tc) <sup>3</sup>		-90		ppm/°C
Ambient Temperature		25		°C
System Source and Load Impedance		50		Ω

- Notes: 1. Average of lower & upper 3 dB frequencies.  
 2. Evaluated over 90% of the 3 dB bandwidth.  
 3. Typical change of filter frequency response with temperature is  $\Delta f/f_{ref} = (T-T_{ref}) \cdot Tc$  ppm.

## PACKAGE OUTLINE

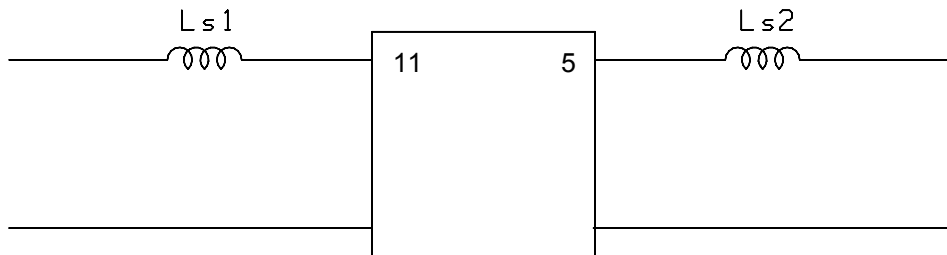


Units: mm

### Pin Configuration:

Input: 11  
Output: 5  
Ground: 1,2,3,4,6,7,8,9,10,12

## MATCHING CIRCUIT



Component values in 50  $\Omega$ : Ls1 = 120 nH  
(Minimum Q = 45)

Ls2 = 100 nH

### Notes

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.