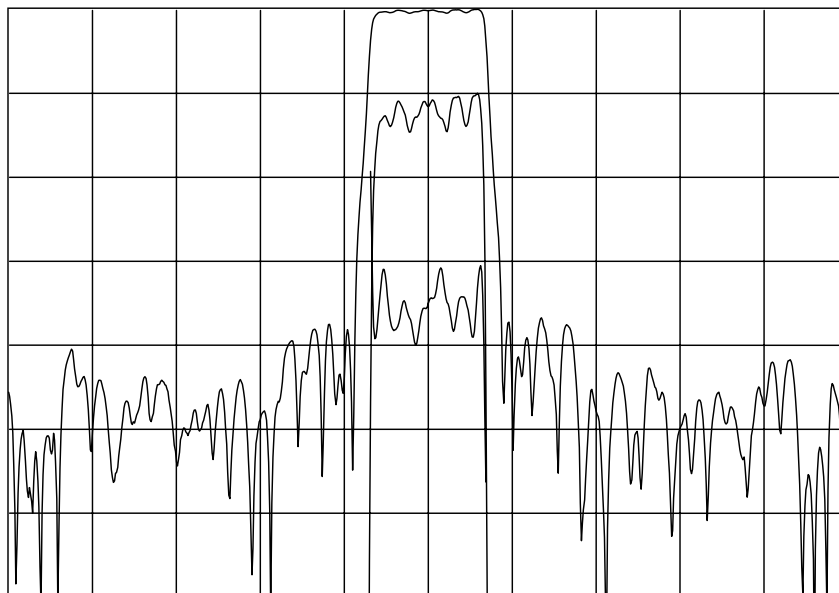




TYPICAL PERFORMANCE



Horizontal: 1 MHz/div

Vertical (from top):

Magnitude

10 dB/div

Magnitude

1 dB/div

Phase Deviation

5 deg/div

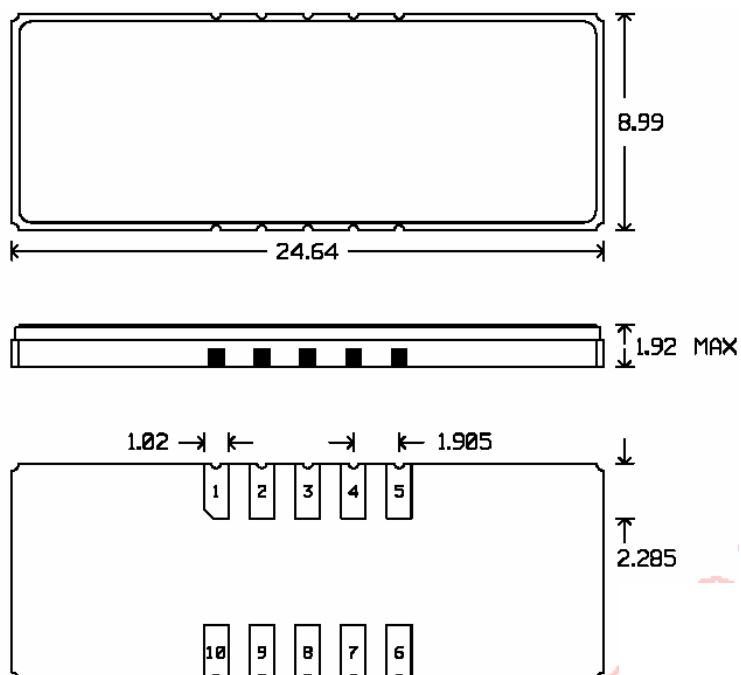
SPECIFICATION

Parameter ¹	Min.	Typ.	Max.	Units.
Center Frequency (Fc) ²		69.99		MHz
Minimum Insertion Loss ³		17	20	dB
Lower 1.5 dB Band Edge		69.33	69.36	MHz
Upper 1.5 dB Band Edge	70.62	70.65		MHz
Lower 14 dB Band Edge	69.21	69.25		MHz
Upper 14 dB Band Edge		70.73	70.77	MHz
Lower 25 dB Band Edge	69.09	69.18		MHz
Upper 25 dB Band Edge		70.80	70.89	MHz
Passband Ripple (Fc +/- 0.615 MHz)		0.5	1.0	dB p-p
Phase Linearity (Fc +/- 0.615 MHz)		4.5	8	deg p-p
Input Return Loss @ Fc	10	16		dB
Output return Loss @ Fc	10	16		dB
Rejection (15 MHz to 61 MHz)	40	60		dB
Rejection (61 MHz to 68 MHz)	30	38		dB
Rejection (72 MHz to 79 MHz)	30	38		dB
Rejection (79 MHz to 140 MHz)	40	50		dB
Source and Load Impedance	50			Ω
Substrate Material	Quartz			
Operating temperature Range	-10	23	70	$^{\circ}$ C

- Notes:
1. All specifications for a device matched to a 50 ohm system impedance.
 2. Average of Lower and Upper 3 dB Frequencies.
 3. All dB values are referenced to the insertion loss.



PACKAGE OUTLINE

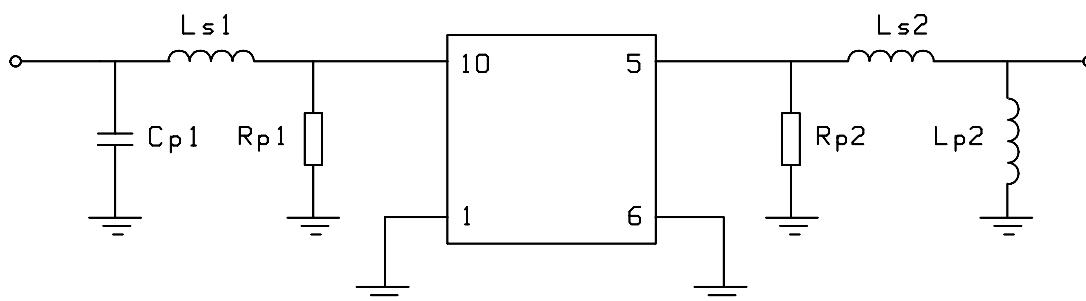


Units: mm

Pin Configuration:

Input: 10
Input Return: 1
Output: 5
Output Return: 6
Ground: 2,3,4,7,8,9

MATCHING CIRCUIT



Component values:

$L_{s1} = 230 \text{ nH}$	$L_{s2} = 176 \text{ nH}$	(Minimum Q = 45)
$C_{p1} = 100 \text{ pF}$	$L_{p2} = 47 \text{ nH}$	
$R_{p1} = 2.7 \text{ K}\Omega$	$R_{s2} = 3.0 \text{ K}\Omega$	

Notes

1. Recommend use of 2% tolerance components.
2. Optimum values depend on board layout. Values intended as guide only.

ISO 9001
Registered

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