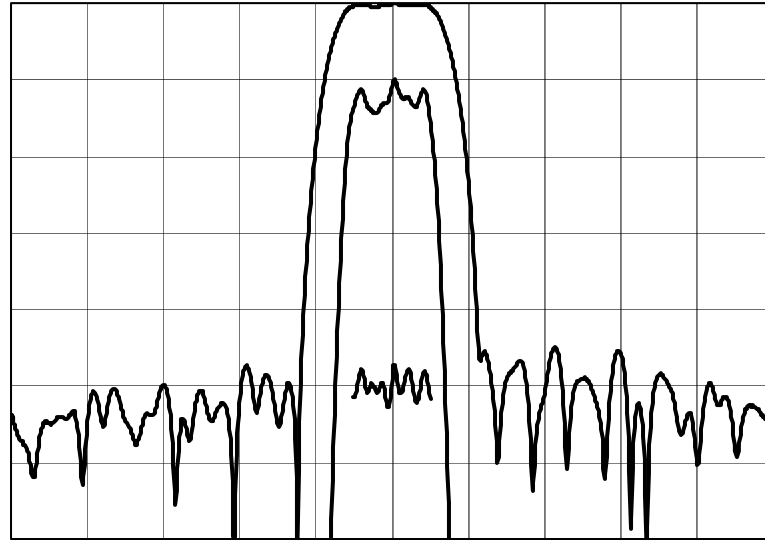


TYPICAL PERFORMANCE



Horizontal: 1 MHz/div Vertical (from top): Magnitude 10 dB/div
 Magnitude 1 dB/div
 Group Delay 200 ns./div

SPECIFICATION

NOTE – All electrical specifications apply over the whole of the operating temperature range.

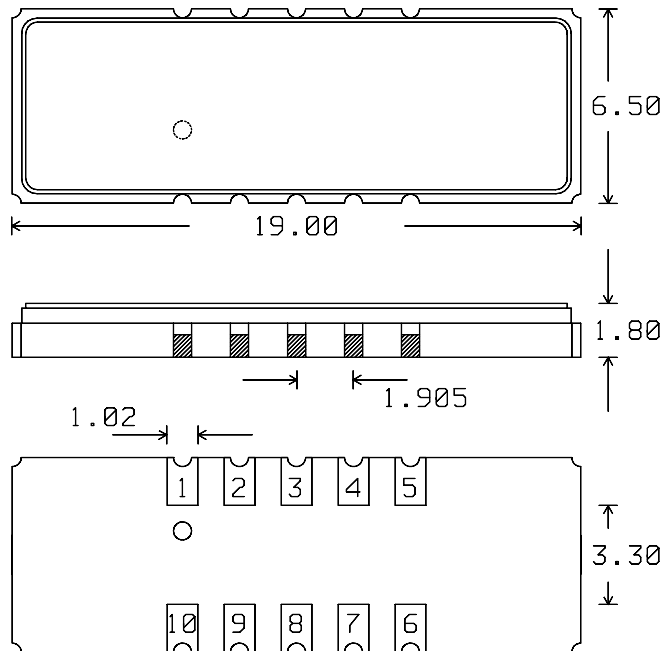
Parameter	Min	Typ	Max	Units
Center Frequency, F_c ¹		141		MHz
Passband Width ²		1.05		MHz
Insertion Loss at F_c		11.8	14	dB
3.75 dB Bandwidth	1.18	1.43		MHz
42.5 dB Bandwidth		2.29	2.5	MHz
Stopband Rejection, 79.75 – 139.75 MHz & 142.25 MHz - 202.25 MHz	42.5	45		dB
Stopband Rejection, 136 - 136.2 MHz & 145.8 MHz - 146 MHz	50	53		dB
Amplitude Ripple ³		0.8	1.0	dB p-p
Group Delay Ripple ³		125	300	ns p-p
Return Loss at Input and Output ^{3,5}	10	15		dB
Phase Linearity ⁴		0.6	2.0	deg rms
Absolute Delay	2.34	2.39	2.54	μ s
Source and Load Impedance		50		Ω
Operating Temperature Range	0		+85	$^{\circ}$ C

- Notes: 1. Fixed reference. All specified bandwidths are centered at this frequency.
 2. Defined bandwidth, centered at 141 MHz. Other parameters are specified over this bandwidth.
 3. Over the passband.
 4. Over 141 MHz +/- 630 kHz.
 5. When matched using external components as described below.

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PACKAGE OUTLINE

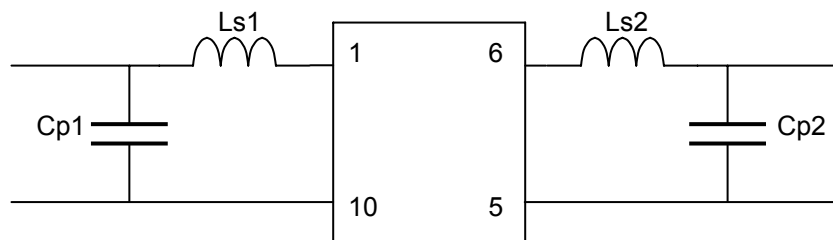


Units: mm

Pin Configuration:

Input:	1
Input Return:	10
Output:	6
Output Return:	5
Ground:	2, 3, 4, 7, 8, 9

MATCHING CIRCUIT



Typical component values: $L_{s1} = 118 \text{ nH}$ $L_{s2} = 108.2 \text{ nH}$
(Minimum $Q = 45$) $C_{p1} = 56 \text{ pF}$ $C_{p2} = 56 \text{ pF}$

Notes:

- Component values shown are those used in the Micro Networks test fixture. Values used in a particular application may differ from this, depending on board layout.
- 2% components or better are recommended in order to meet the return loss specification over the full operating temperature range.

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