

Preliminary

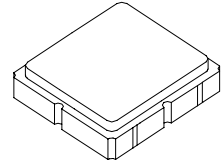


- Low Insertion Loss
- 3.8 x 3.8 x 1.2 mm Surface-Mount Case
- Differential or Single-Ended Operation
- Complies with Directive 2002/95/EC (RoHS)



SF2125D

305.00 MHz SAW Filter



SM3838-8

Absolute Maximum Ratings

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Maximum DC Voltage Between any Two Terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Suitable for Lead-free Soldering - Maximum Soldering Profile	260°C for 30 s	

Electrical Characteristics

Characteristic	Sym	Notes	Min	Typ	Max	Units
Center Frequency	f _C	1		305		MHz
Insertion Loss	IL			8.5	11.5	dB
3 dB Bandwidth			5	7.5		MHz
40 dB Bandwidth				14	26	MHz
Amplitude Ripple (p-p)	f _C ±2.5 MHz	1		0.7	1.5	dB
Low Side Attenuation (f _C - 8.2 MHz)			30			dB
Low Side Attenuation (f _C - 7.8 MHz)			25			
High Side Attenuation (f _C + 7.8 MHz)			25			
High Side Attenuation (f _C + 8.2 MHz)			30			
Temperature Coefficient of Frequency					-18	ppm/K
Delay Ripple (p-p)	f _C ±2.5 MHz	1, 2, 3			50	ns
Group Delay in Passband					350	
Case Style		6	SM3838-8 3.8 x 3.8 mm Nominal Footprint			
Lid Symbolization (Y=year, WW=week, S=shift) See note 4			TBD YWWS			

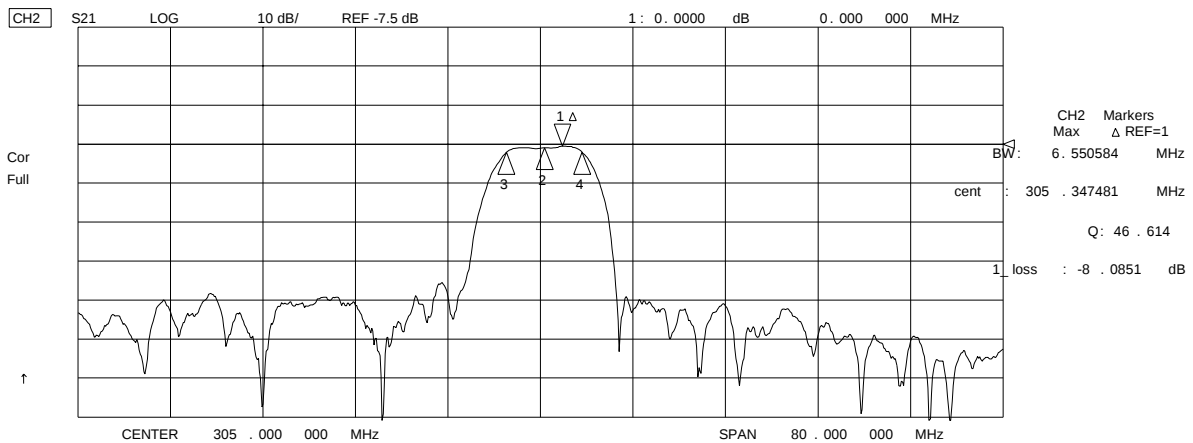
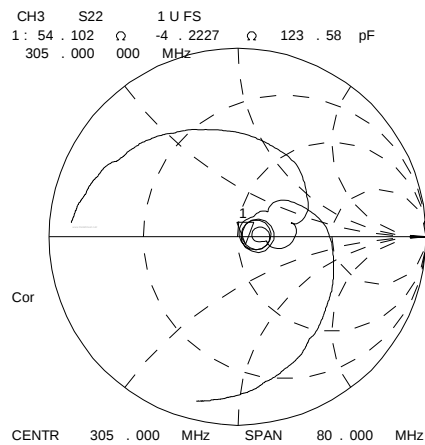
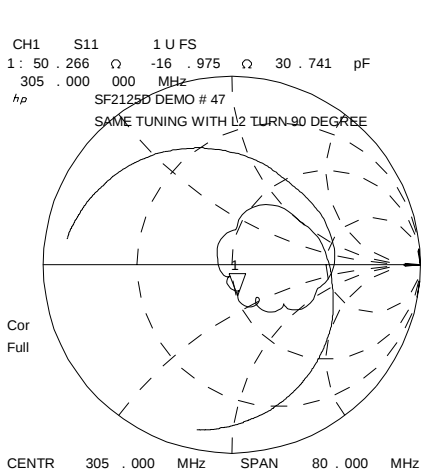
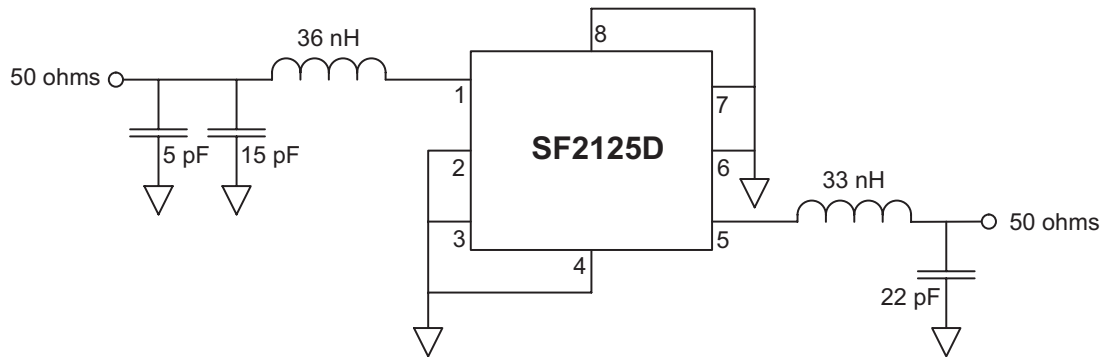


CAUTION: Electrostatic Sensitive Device. Observe precautions for handling.

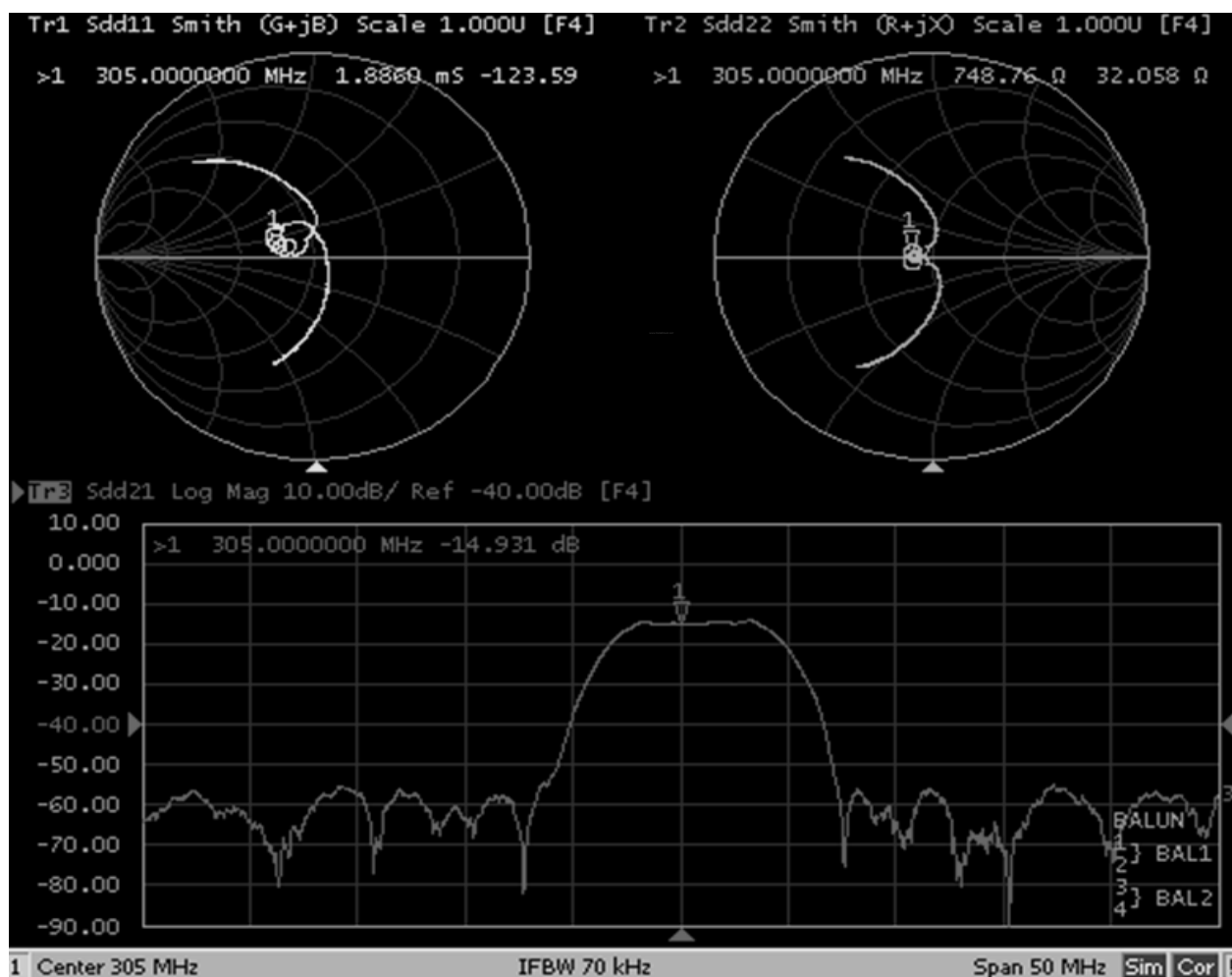
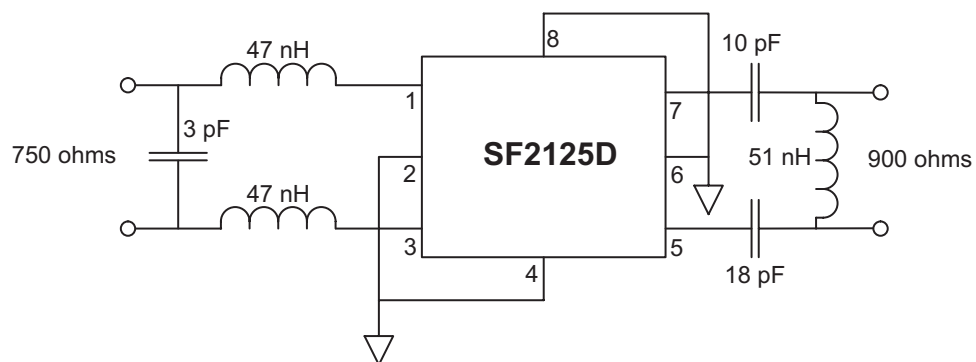
Notes:

1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, f_C .
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. The design, manufacturing process, and specifications of this filter are subject to change.
5. Tape and Reel Standard Per ANSI / EIA 481.
6. US and international patents may apply.
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Single-ended Demonstration Circuit



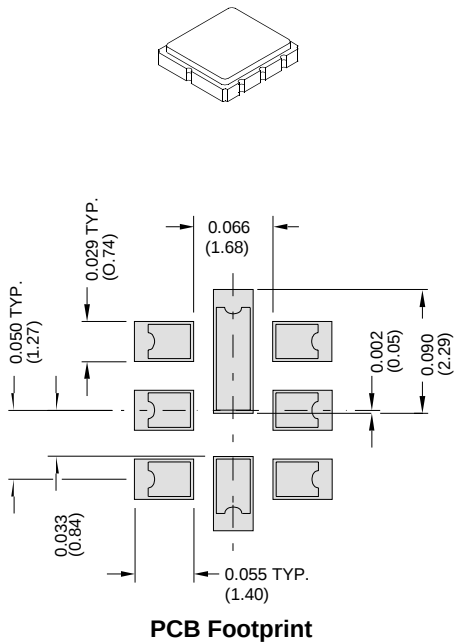
Balanced Demonstration Circuit



SM3838-8 Case

8-Terminal Ceramic Surface-Mount Case

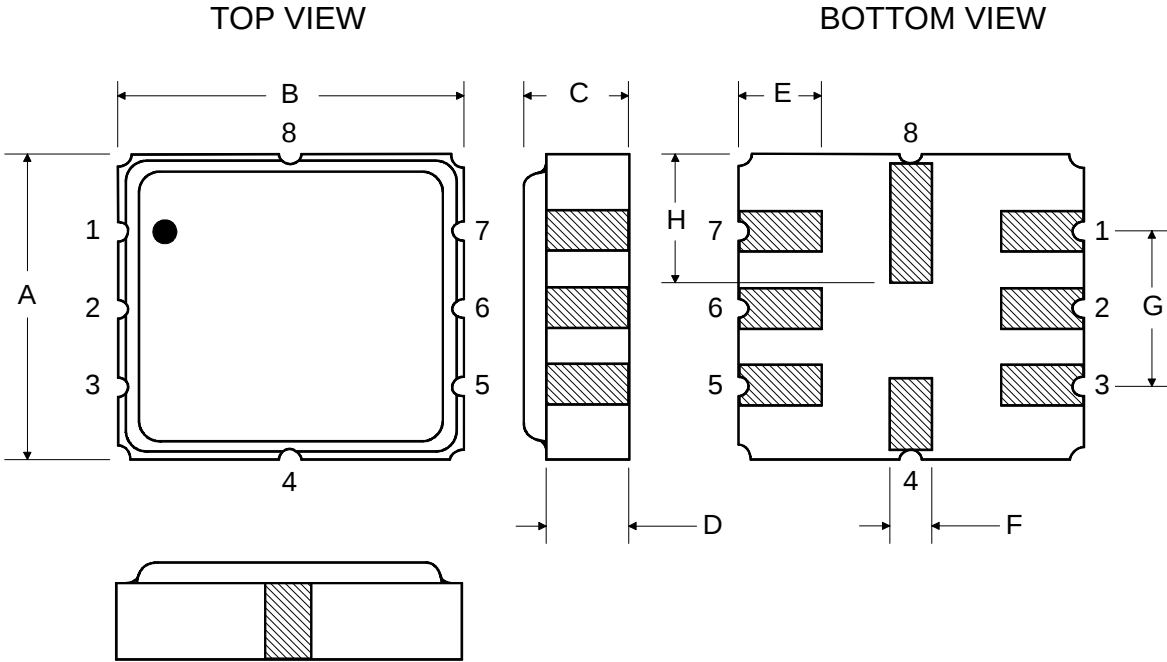
3.8 x 3.8 mm Nominal Footprint



Case Dimensions						
Dimension	mm			Inches		
	Min	Nom	Max	Min	Nom	Max
A	3.6	3.8	4.0	0.142	0.150	0.157
B	3.6	3.8	4.0	0.142	0.150	0.157
C	1.05	1.20	1.35	0.041	0.047	0.053
D	0.95	1.10	1.25	0.037	0.043	0.049
E	0.90	1.00	1.10	0.035	0.040	0.043
F	0.50	0.60	0.70	0.020	0.024	0.028
G	2.39	2.54	2.69	0.090	0.100	0.110
H	1.40	1.75	2.05	0.055	0.069	0.080

Electrical Connections		
	Connection	Terminals
Port 1	Differential Input	1, 3
Port 2	Differential Output	5, 7
	Ground	All Others
Single Ended Operation		Return is Ground
Differential Operation		Return is Hot
Dot Indicates Pin 1		

Materials	
Solder Pad Termination	Au plating 30 - 60 ulnches (76.2-152 uM) over 80-200 ulnches (203-508 uM) Ni.
Lid	Fe-Ni-Co Alloy Electroless Nickel Plate (8-11% Phosphorus) 100-200 ulnches Thick
Body	Al ₂ O ₃ Ceramic
Pb Free	



See Detail "A"

12.0

13.0

20.2

2.0

“B “		Quantity Per Reel
Inches	millimeters	
7	178	500
13	330	3000

Carrier Tape Dimensions	
Ao	4.25 mm
Bo	4.25 mm
Ko	1.6 mm
Pitch	8.0 mm
W	12.0 mm

