



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
TO:	Please return this copy as a certification of your approval
Part No.:	Checked & Approved by:
Customer's Part No.:	Date:

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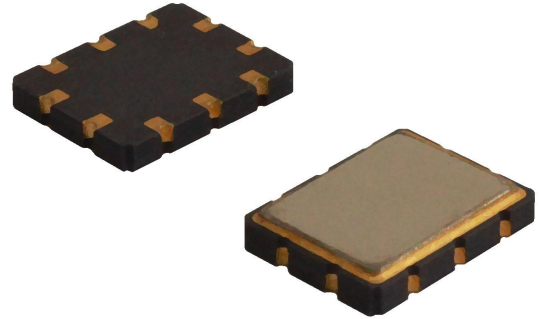


Part No.	:	SF6115
Pages	:	6
Date	:	2013/09/10
Revision	:	1.0

Prepared by:	
Checked by:	
Approved by:	

Application

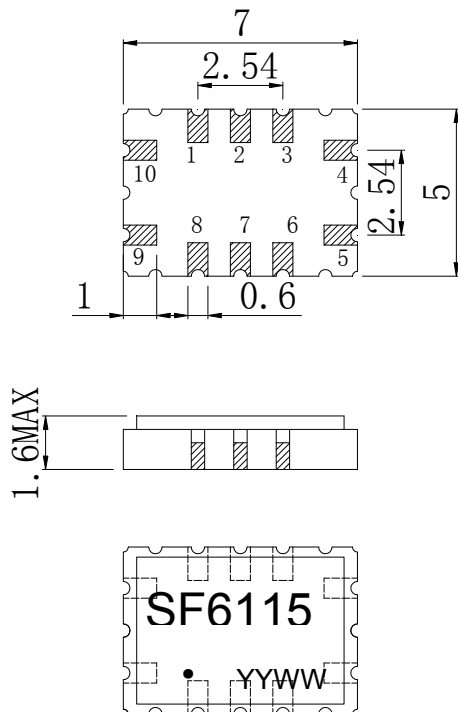
- Low-loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 6.5 MHz



Features

- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 7.00x5.00x1.60mm³
- Package Code QCC12C
- **Electrostatic Sensitive Device(ESD)**

Package Dimensions (Unit: mm)



Pin Configuration

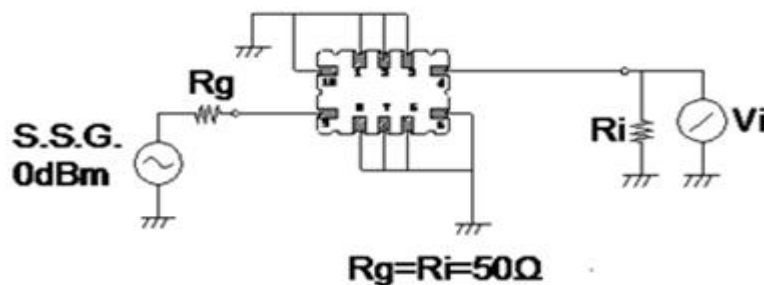
Pin No.	Description
9	Input
4	Output
1,2,3,6,7,8,	Case Ground
5,10	To be Grounded

Marking Description

SF	SF	Trademark
	F	SAW Filter
6115	Part Number	
●	Pin 1	
YYWW	Year Code & Week Code	

*Fig: If the products produced in 06th week of 2015, The year code & week code is 1506.

Test Circuit (Bottom View)



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V _{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C
RF Power Dissipation	P	15	dBm

Electronic Characteristics

Test Temperature: 25°C ± 2°C

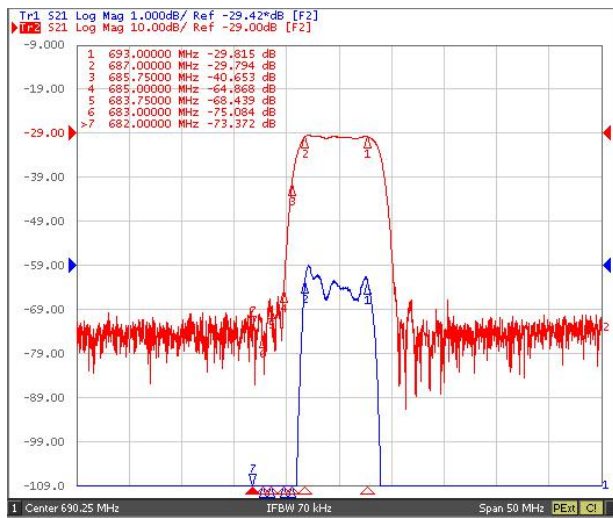
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

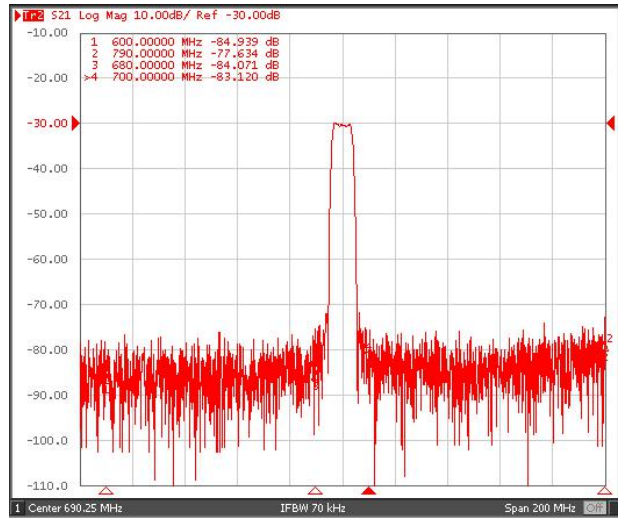
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f _c		690.25		MHz
Insertion Loss(min)	IL		30.0	34.0	dB
Amplitude Ripple (p-p) 687.25 - 693.75MHz	Δ _a		1.0	3.0	dB
Group Delay Ripple 687.25 - 693.75MHz	GDR		100.0	300.0	ns
Absolute Attenuation	α				
600.00 - 682.00MHz			40.0		dB
685.00MHz		30.0	35.0		dB
683.00MHz		37.0	40.0		dB
700.00-790.00MHz			40.0		dB

Frequency Characteristics

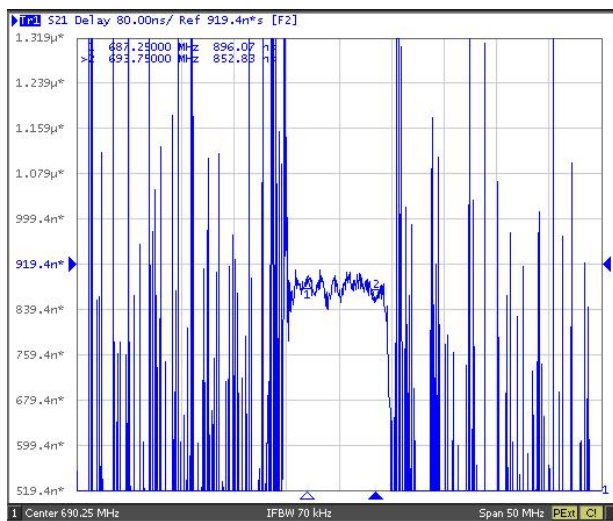
Frequency Response



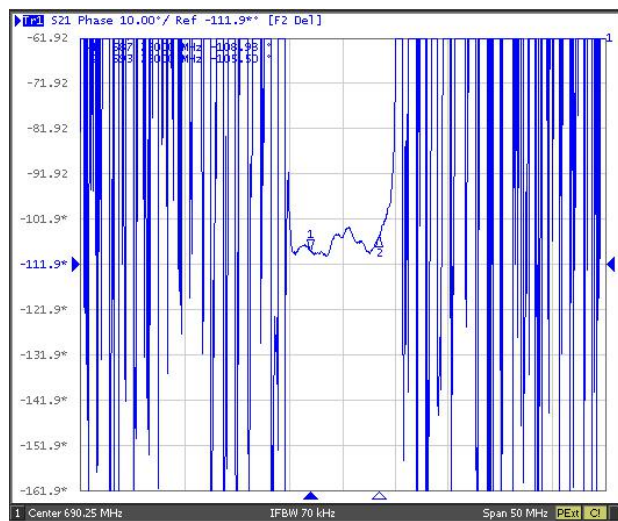
Frequency Response (wideband)



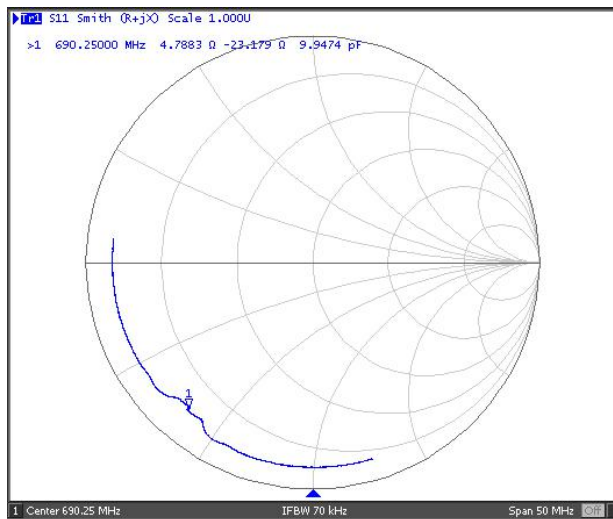
Delay Ripple & S11 VSWR



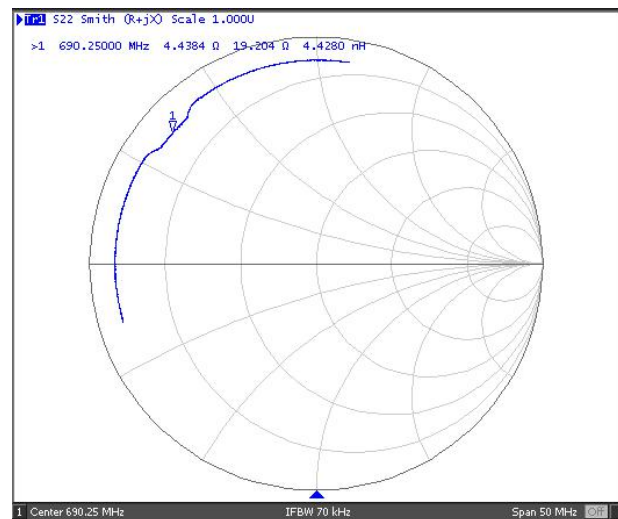
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.