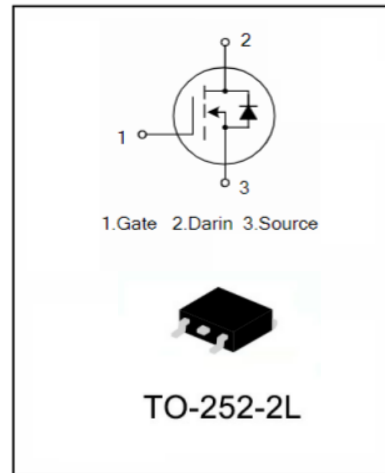


80A, 30V N-CHANNEL MOSFET

GENERAL DESCRIPTION

The SFD3008T uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.



FEATURES

- ◆ 80A,30V, $R_{DS(on)(typ.)}=4.8m\Omega@V_{GS}=10V$
- ◆ Excellent package for good heat dissipation
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ High density cell design for ultra low R_{dson}
- ◆ Special process technology for high ESD capability
- ◆ Exceptional on-resistance and maximum DC current capability

ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFD3008T	TO-252-2L	SFD3008T	Pb free	Reel

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ C$ unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	$T_C = 25^\circ C$	80	A
	$T_C = 70^\circ C$	46	
Drain Current Pulsed (Note 1)	I_{DM}	280	A
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	56	mJ
Maximum Power Dissipation	P_D	46	W
Operation Junction Temperature Range	T_J	$-55 \sim +150$	$^\circ C$
Storage Temperature Range	T_{stg}	$-55 \sim +150$	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.52	$^\circ C/W$

Electrical Characteristics (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA,	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current, Forward	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.5	2.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 30A	-	4.8	6	mΩ
R _{DS(on)}	(Note 3)	V _{GS} = 4.5V, I _D = 20A	-	7.5	12	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 15V, V _{GS} = 0V,	-	1614	-	pF
C _{oss}	Output Capacitance	f = 1.0MHz	-	245	-	pF
C _{rss}	Reverse Transfer Capacitance		-	215	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} = 15V, I _D = 30A, V _{GS} = 10V, R _{GEN} = 3.0Ω	-	7.5	-	ns
t _r	Turn-On Rise Time		-	14.5	-	ns
t _{d(off)}	Turn-Off Delay Time		-	35.2	-	ns
t _f	Turn-Off Fall Time		-	9.6	-	ns
Q _g	Total Gate Charge	V _{DS} = 15V, I _D = 30A, V _{GS} = 10V	-	33.7	-	nC
Q _{gs}	Gate-Source Charge		-	8.5	-	nC
Q _{gd}	Gate-Drain Charge		-	7.5	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	80	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				280	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: T_J = 25°C, V_{DD} = 15V, V_G = 10V, R_G = 25Ω, L = 0.5mH, I_{AS} = 15A
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%

Typical Performance Characteristics

Figure 1: Output Characteristics

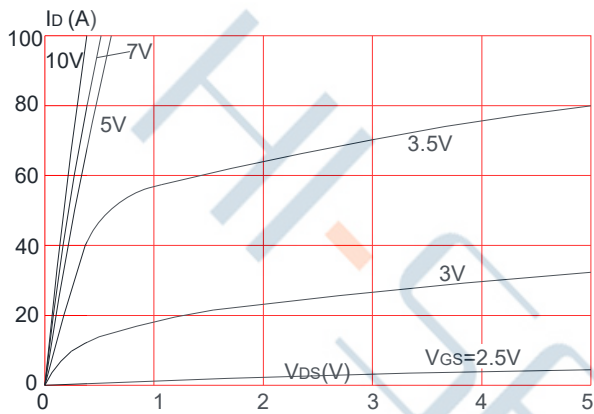


Figure 2: Typical Transfer Characteristics

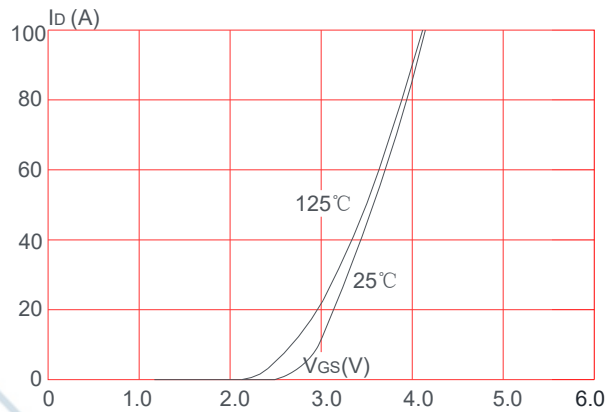


Figure 3: On-resistance vs. Drain Current

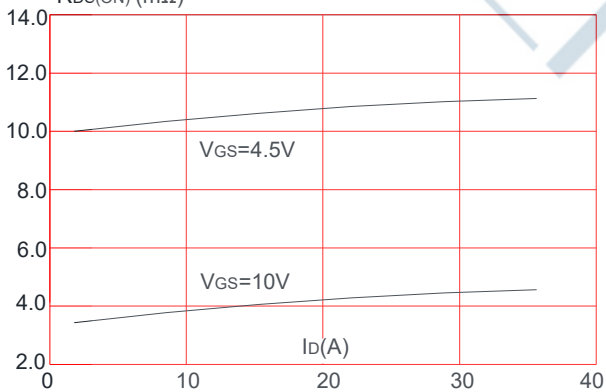


Figure 4: Body Diode Characteristics

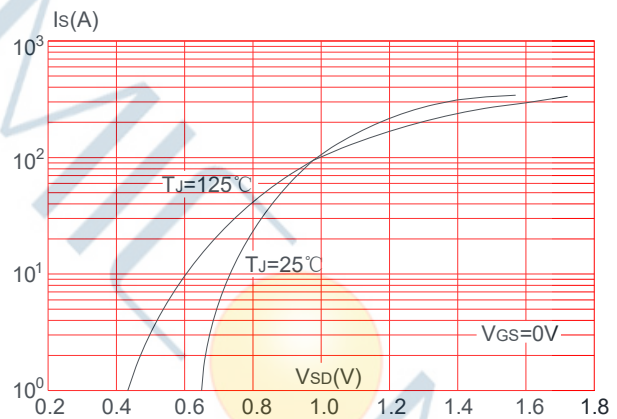


Figure 5: Gate Charge Characteristics

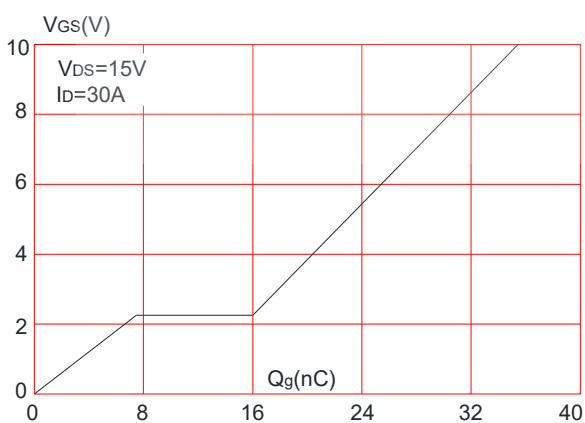


Figure 6: Capacitance Characteristics

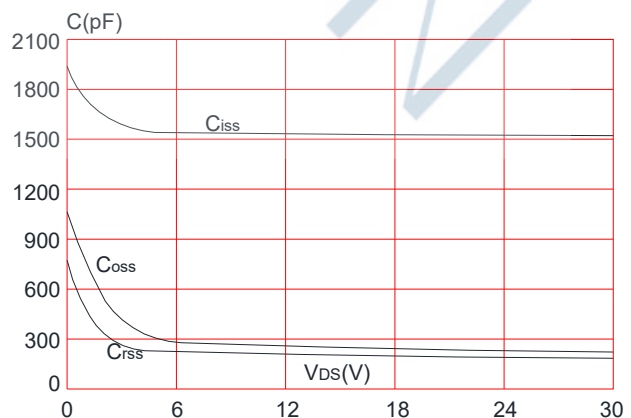


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

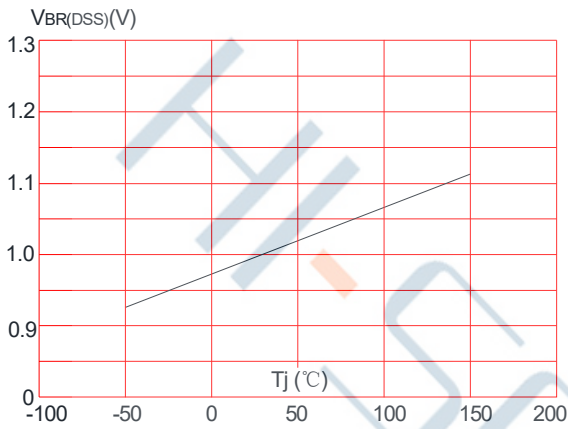


Figure 8: Normalized on Resistance vs. Junction Temperature

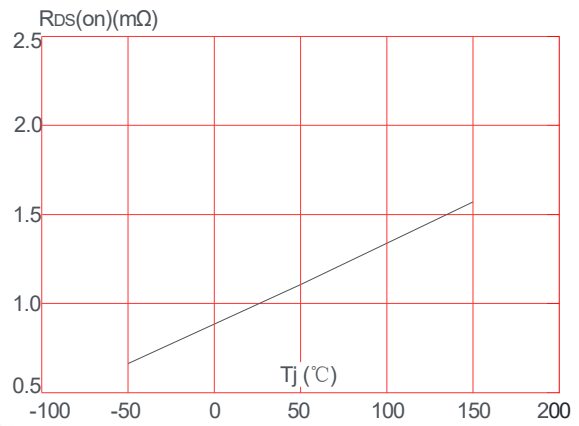


Figure 9: Maximum Safe Operating Area

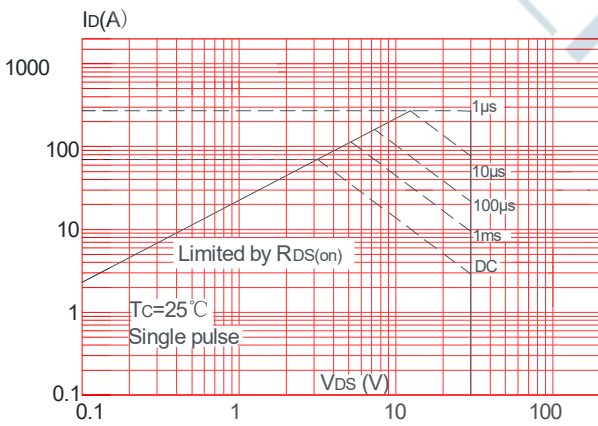


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

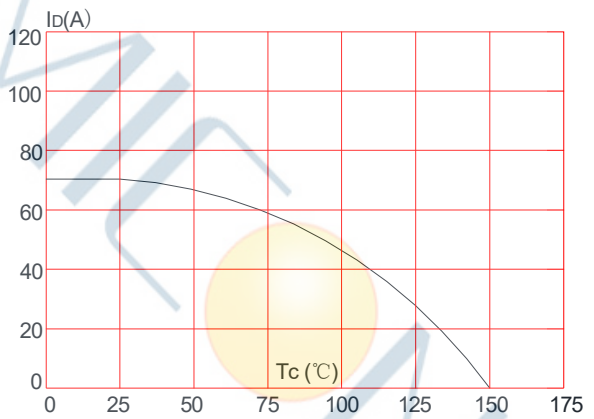
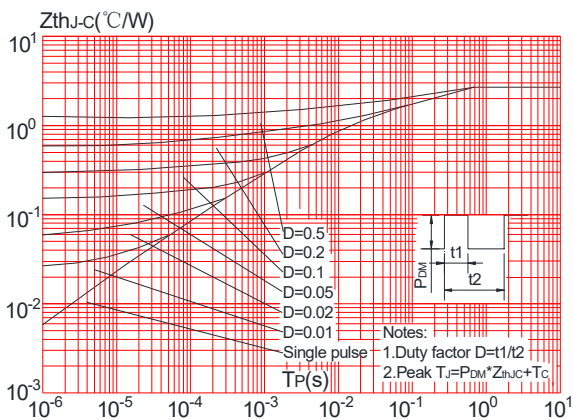


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Test Circuit

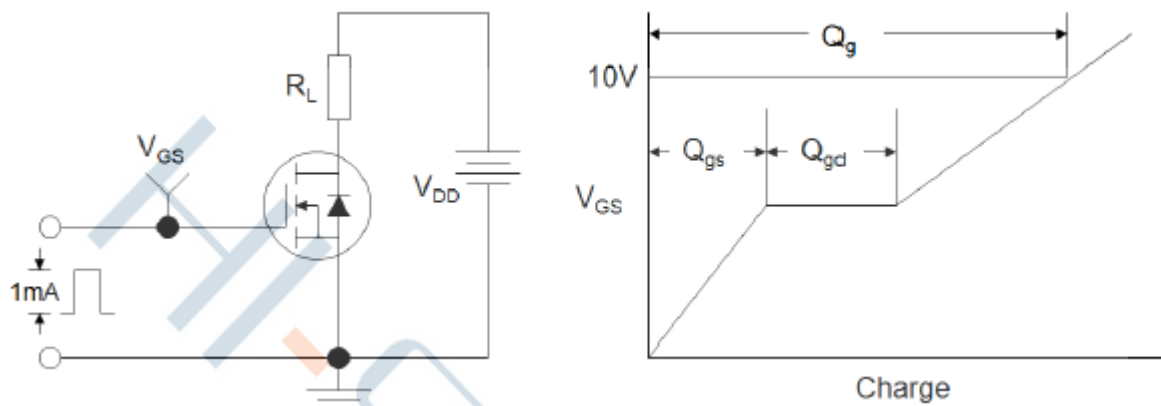


Figure1:Gate Charge Test Circuit & Waveform

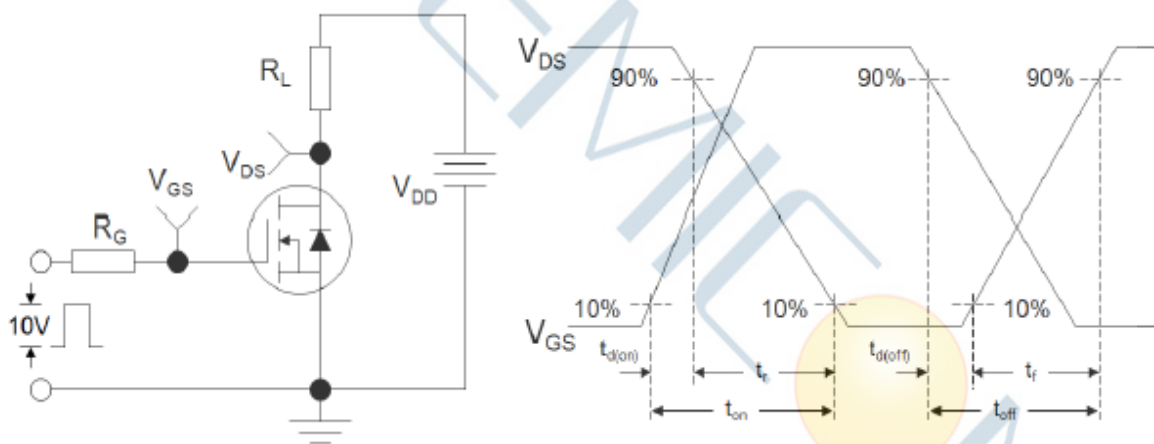


Figure 2: Resistive Switching Test Circuit & Waveforms

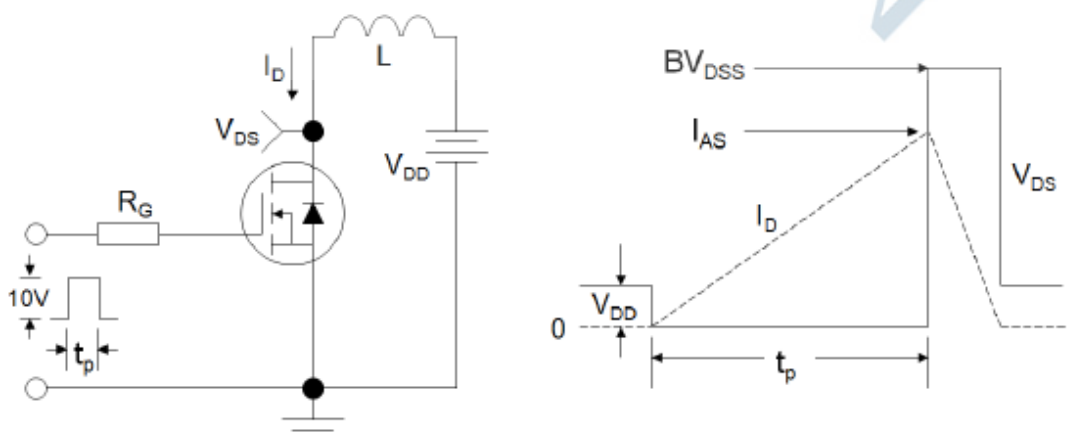


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

PACKAGE OUTLINE(continued)

