

SPECIFICATION

SFHX8817-070XS1

Customer Approved

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Date _____

By _____

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General Description:

This color TFT LCD timing control board apply to drive PVI's **PW070XS1(16:9)** series Color TFT LCD Panel, it supports 18 bits parallel RGB input interface, with the build-in color space conversion circuit, DAC, and operation amplified, the driving board performs gamma correction and polarity inverted functions to converts the digital data to the proper interface of LCD Panel .

Features:

- Fit PVI's **PW070XS1(16:9)** series TFT LCD or compatibility
- Ultra Compact
- Support 18 bits parallels data input
- Up/Down Display Reverse
- Left/Right Display Reverse

Applications:

- Security
- Video Game
- Door Phone
- Video Phone
- Portable TV
- Instrument Display

Video input signal:

- 18 bits parallel RGB input

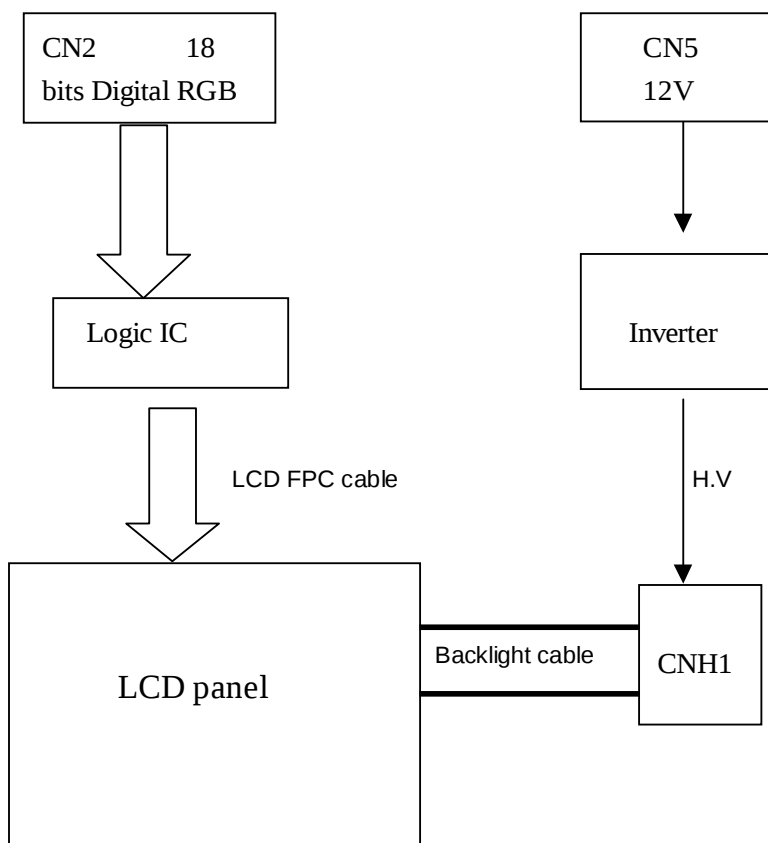
Work temperature:

- 20⁰C—+70⁰C

Storage temperature:

- 30⁰C—+80⁰C

Driving Board Function Block:



Input / Output Connector

1.) CN2: Power & 18 bits parallels RGB digital Video Input Connector

Pin No. : 31, Molex DB9A-31P or compatibility

2.) CN5: Invert power and control signal Input Connector

Pin No. : 4, Pitch: 2.5 mm, JST S4B-XH-A or compatibility

3.) CN1 : LCD Signal Output Connector

CN1 : FPC Down Connector, 30 Pins, Pitch: 0.5 mm, Fit **PW070XS1(16:9)** series.

4.) CNH1: Backlight High Voltage Connector

Pin No. : 2, Pitch: 3.5 mm JST BHSR-02VS-1or compatibility

Connector Pin Definition

CN2 pin definition

No.	Pin Name	NO.	Pin Name
1	GND	17	G4
2	CLK	18	G5
3	HS	19	GND
4	VS	20	B0
5	GND	21	B1
6	R0	22	B2
7	R1	23	B3
8	R2	24	B4
9	R3	25	B5
10	R4	26	GND
11	R5	27	DE
12	GND	28	VCC
13	G0	29	VCC
14	G1	30	L/R
15	G2	31	U/D
16	G3		

CN5 pin definition

No.	Pin Name	I/O	Pin description	Remark
1	+12V	I	12VDC POWER INPUT	
2	GND	I	INVERT POWER GROUND	
3	ON/OFF	I	INVERT ON/OFF CONTROL	Low: OFF; High: ON
4	BRIGHT	I	BACKLIGHT BRIGHTNESS ADJUST	Note

Note: Default is NC, Voltage=0v---5v(dark).

(1) CNH1 pin definition

No.	Pin Name	I/O		Remark
1	HV	O	Lamp driving terminal (high voltage)	
2	LV	O	Lamp driving terminal (low voltage)	

(2) CN1 & CN1B pin definition

CN1 Meet **PW070XS1(16:9)** input definition

Note 1:

R/L = Hi (+5V) for shift left, Low (GND) for shift right

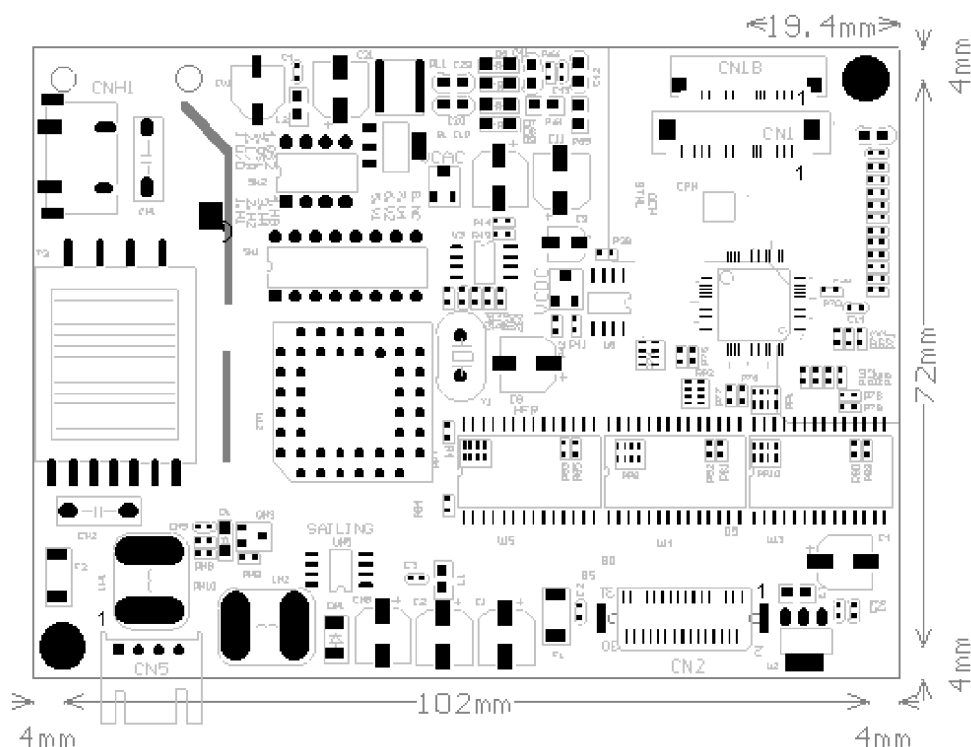
Note 2:

U/D = Hi (+5V) for down, Low (GND) for up

Note 3:

Timing conditions Meet **PW070XS1(16:9)** spec.

Outline Dimension



Description:

Outline: 110.0*80.0*10.1mm

Top Layer High (Max): 8.5mm

Board thickness: 1.6mm

5 Screws: M3.0

Weight: 0.05kg