



# Solid State Devices, Inc.

14701 Firestone Blvd \* La Mirada, CA 90638  
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## SFT502/G and SFT504/G Series

### 5 AMP, 200 Volts NPN HIGH SPEED POWER TRANSISTOR

#### DESIGNER'S DATA SHEET

**Part Number / Ordering Information** <sup>1/</sup>

SFT502/  
SFT504/

└─ Screening <sup>2/</sup> \_\_\_ = No Screening  
           TX = TX Level  
           TXV = TXV Level  
           S = S Level

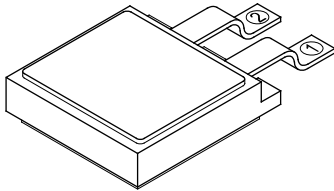
└─ Polarity \_\_\_ = Normal  
           R = Reverse

└─ Package  
       G = CERPACK

- Features:**
- BV<sub>CEO</sub> 150 V Minimum
  - Fast Switching
  - High Frequency, 50 MHz Typical
  - High Linear Gain (SFT504G)
  - Low Saturation Voltage and Leakage
  - 200°C Operating Temperature
  - Gold Eutectic Die Attach
  - TX, TXV, S-Level Screening Available
  - Designed for Complementary Use with SFT501/G and SFT503/G

Maximum Ratings <sup>3/</sup>	Symbol	Max	Units
Collector – Base Voltage	V <sub>CBO</sub>	200	Volts
Collector – Emitter Voltage	V <sub>CEO</sub>	150	Volts
Emitter – Base Voltage	V <sub>EBO</sub>	7.0	Volts
Continuous Collector Current	I <sub>C</sub>	5.0	Amps
Base Current	I <sub>B</sub>	1.0	Amps
Operating & Storage Temperature	T <sub>J</sub> & T <sub>STG</sub>	-65 to +200	°C
Total Power Dissipation @ T <sub>C</sub> = 100°C Derate above 100°C	P <sub>D</sub>	10 0.10	W W/°C
Thermal Resistance (Junction to Case)	R <sub>θJC</sub>	2.4	°C/W

CERPACK (G)



**NOTE:** All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

**DATA SHEET #: TR0078C**

**DOC**



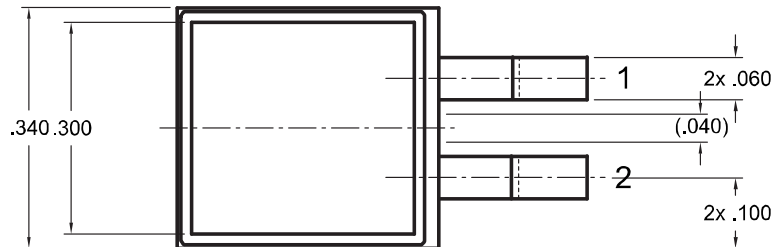
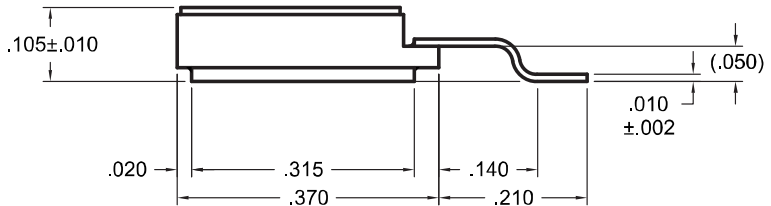
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**SFT502/G and SFT504/G Series**

Electrical Characteristics <sup>3/</sup>		Symbol	Min	Max	Units
Collector – Emitter Breakdown Voltage	$I_C = 50 \text{ mA}$	$BV_{CEO}$	150	-	Volts
Collector – Base Breakdown Voltage	$I_C = 200 \text{ } \mu\text{A}$	$BV_{CBO}$	200	-	Volts
Emitter – Base Breakdown Voltage	$I_E = 200 \text{ } \mu\text{A}$	$BV_{EBO}$	7	-	Volts
Collector Cutoff Current	$V_{CB} = 100 \text{ V}$	$I_{CBO}$	-	500	nA
Collector Cutoff Current	$V_{CE} = 100 \text{ V}$	$I_{CEO}$	-	1	$\mu\text{A}$
Emitter Cutoff Current	$V_{EB} = 6 \text{ V}$	$I_{EBO}$	-	500	nA
DC Current Gain* $V_{CE} = 5 \text{ V}$					
SFT502	$I_C = 50 \text{ mA}$	$H_{FE}$	20	-	
	$I_C = 2.5 \text{ A}$		30	-	
	$I_C = 5.0 \text{ A}$		20	-	
SFT504	$I_C = 50 \text{ mA}$		50	-	
	$I_C = 2.5 \text{ A}$		50	-	
	$I_C = 5 \text{ A}$	40	-		
Collector-Emitter Saturation Voltage*	$I_C = 2.5 \text{ A}, I_B = 250 \text{ mA}$ $I_C = 5.0 \text{ A}, I_B = 500 \text{ mA}$	$V_{CE(SAT)}$	-	0.75 1.5	V
Base-Emitter Saturation Voltage*	$I_C = 2.5 \text{ A}, I_B = 250 \text{ mA}$ $I_C = 5.0 \text{ A}, I_B = 500 \text{ mA}$	$V_{BE(SAT)}$	-	1.3 1.5	V
Current Gain Bandwidth Product*	$I_C = 500 \text{ mA}, V_{CE} = 5 \text{ V}, f = 10 \text{ MHz}$	$f_T$	70	-	MHz
Output Capacitance	$V_{CB} = 10 \text{ V}, I_E = 0 \text{ A}, f = 1.0 \text{ MHz}$	$C_{ob}$	-	225	pF
Input Capacitance	$V_{CB} = 10 \text{ V}, I_C = 0 \text{ A}, f = 1.0 \text{ MHz}$	$C_{ib}$	-	900	pF
Delay Time	$V_{CC} = 50 \text{ V}$ $I_C = 5 \text{ A}$ $I_{B1} = I_{B2} = 500 \text{ mA}$	$t_d$	-	50	ns
Rise Time		$t_r$	-	250	ns
Storage Time		$t_s$	-	1200	ns
Fall Time		$t_f$	-	300	ns

**CASE OUTLINE: CERPACK**



BOTTOM VIEW

**NOTES:**

- \*Pulse Test: Pulse Width = 300 $\mu\text{s}$ , Duty Cycle = 2%
- 1/ For ordering information, price, operating curves, and availability, contact factory.
- 2/ Screening based on MIL-PRF-19500. Screening flows available on request.
- 3/ Unless otherwise specified, maximum ratings/electrical characteristics at 25°C.

**PIN ASSIGNMENT**

CODE	FUNCTION	BASE	PIN 1	PIN 2
-	Normal	Collector	Emitter	Base
R	Reverse	Collector	Base	Emitter

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