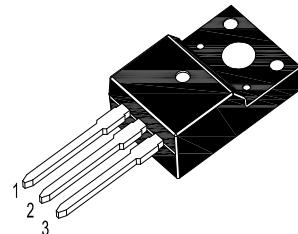
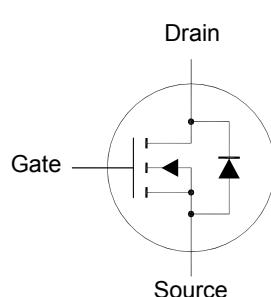


# SFTN0480

## N-Channel Enhancement Mode Power MOSFET



TO-220F Plastic Package  
1.Gate 2.Drain 3.Source

### Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	800	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Gate-Source Voltage AC( $f > 1$ Hz)	$V_{GS}$	$\pm 30$	V
Drain Current $T_C = 25^\circ C$ $T_C = 100^\circ C$	$I_D$	4 2.5	A
Peak Drain Current	$I_{DM}$	12	A
Power Dissipation $T_C = 25^\circ C$	$P_{tot}$	38	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 150	°C

### Thermal Characteristics

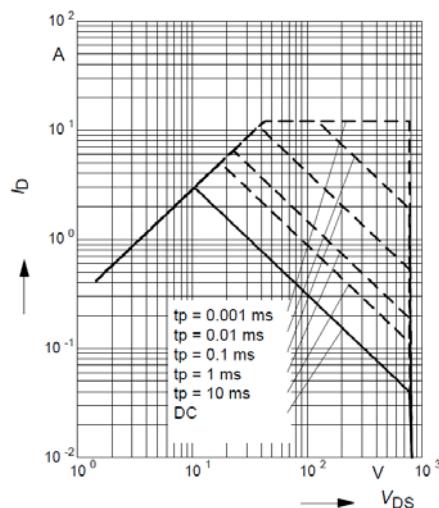
Parameter	Symbol	Max.	Unit
Maximum Thermal Resistance from Junction to Case	$R_{\theta JC}$	2	K/W
Maximum Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	62	K/W

# SFTN0480

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**Characteristics at  $T_J = 25^\circ\text{C}$  unless otherwise specified**

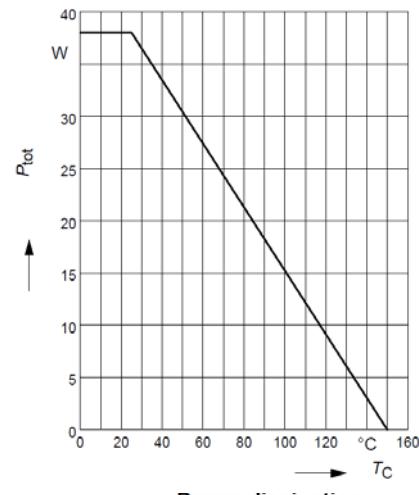
Parameter	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage at $I_D = 0.25 \text{ mA}$	$\text{BV}_{\text{DSS}}$	800	-	-	V
Drain-Source Leakage Current at $V_{\text{DS}} = 800 \text{ V}$ at $V_{\text{DS}} = 800 \text{ V}, T_J = 150^\circ\text{C}$	$I_{\text{DSS}}$	- -	- -	10 100	$\mu\text{A}$
Gate Leakage Current at $V_{\text{GS}} = 20 \text{ V}$	$I_{\text{GSS}}$	-	-	100	nA
Gate-Source Threshold Voltage at $V_{\text{DS}} = V_{\text{GS}}, I_D = 240 \mu\text{A}$	$V_{\text{GS}(\text{th})}$	2.1	-	3.9	V
Drain-Source On-State Resistance at $V_{\text{GS}} = 10 \text{ V}, I_D = 2.5 \text{ A}$ at $V_{\text{GS}} = 10 \text{ V}, I_D = 2.5 \text{ A}, T_J = 150^\circ\text{C}$	$R_{\text{DS}(\text{on})}$	- -	- 3	1.3 -	$\Omega$
Forward Transconductance at $V_{\text{DS}} \geq 2 \times I_D \times R_{\text{DS}(\text{on})\text{max}}, I_D = 2.5 \text{ A}$	$g_{\text{FS}}$	-	3	-	S
Diode Forward Voltage at $I_S = I_F, V_{\text{GS}} = 0 \text{ V}$	$V_{\text{SD}}$	-	-	1.2	V
Maximum Body-Diode Continuous Current	$I_S$	-	-	4	A
Input Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{iss}}$	-	570	-	pF
Output Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{oss}}$	-	240	-	pF
Reverse Transfer Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{rss}}$	-	12	-	pF
Turn-On Delay Time at $I_D = 4 \text{ A}, V_{\text{DD}} = 400 \text{ V}, V_{\text{GS}} = 0/10 \text{ V}, R_G = 22 \Omega$	$t_{\text{d(on)}}$	-	25	-	ns
Turn-On Rise Time at $I_D = 4 \text{ A}, V_{\text{DD}} = 400 \text{ V}, V_{\text{GS}} = 0/10 \text{ V}, R_G = 22 \Omega$	$t_r$	-	15	-	ns
Turn-Off Delay Time at $I_D = 4 \text{ A}, V_{\text{DD}} = 400 \text{ V}, V_{\text{GS}} = 0/10 \text{ V}, R_G = 22 \Omega$	$t_{\text{d(off)}}$	-	-	75	ns
Turn-Off Fall Time at $I_D = 4 \text{ A}, V_{\text{DD}} = 400 \text{ V}, V_{\text{GS}} = 0/10 \text{ V}, R_G = 22 \Omega$	$t_f$	-	-	16	ns



**Safe operating area FullPAK**

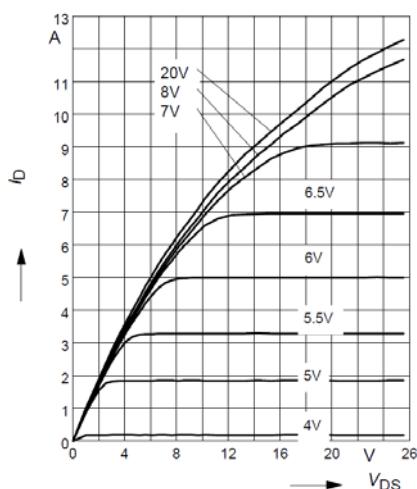
$$I_D = f(V_{DS})$$

parameter:  $D = 0$ ,  $T_C = 25^\circ\text{C}$



**Power dissipation**

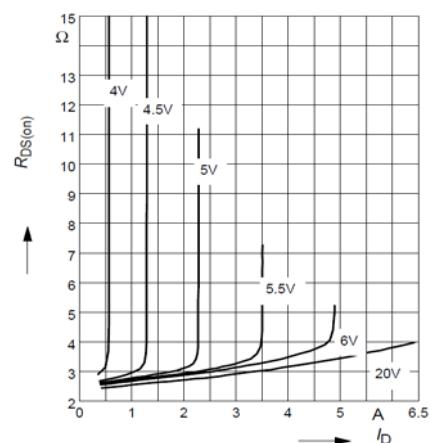
$$P_{\text{tot}} = f(T_C)$$



**Typ. output characteristic**

$$I_D = f(V_{DS}); T_j=25^\circ\text{C}$$

parameter:  $t_p = 10 \mu\text{s}$ ,  $V_{GS}$



**Typ. drain-source on resistance**

$$R_{DS(on)} = f(I_D)$$

parameter:  $T_j=150^\circ\text{C}$ ,  $V_{GS}$

## TO-220F Package Outline

