

60V, 50A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

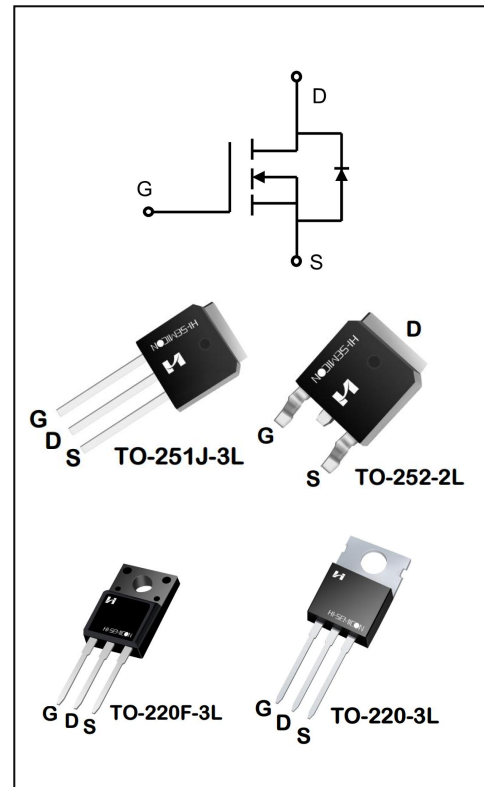
The SFX6005T uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=60V, I_D=50A$
- ◆ $R_{DS(on)}$
TYP: $12.0m\Omega @ V_{GS}=10V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFD6005T	TO-252-2L	SFD6005T	Pb Free	Reel
SFU6005T	TO-251J-3L	SFU6005T	Pb Free	Tube
SFF6005T	TO-220F-3L	SFF6005T	Pb Free	Tube
SFP6005T	TO-220-3L	SFP6005T	Pb Free	Tube

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Characteristics		Symbol	Ratings			Unit
			SFF6005T	SFP6005T	SFD/U6005T	
Drain-Source Voltage		V _{DS}	60			V
Gate-Source Voltage		V _{GS}	±20			V
Drain Current	T _C = 25°C	I _D	50			A
	T _C = 100°C		32			
Drain Current Pulsed(Note 1)		I _{DM}	200			A
Power Dissipation(T _C =25°C) -Derate above 25°C		P _D	91	105	82	W
			0.68	0.73	0.58	W/°C
Single Pulsed Avalanche Energy (Note 2)		E _{AS}	285			mJ
Operation Junction Temperature Range		T _J	-55~+150			°C
Storage Temperature Range		T _{stg}	-55~+150			°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		TL	300			°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX			Unit
		SFF6005T	SFP6005T	SFD/U6005T	
Thermal Resistance, Junction-to-Case	R _{θJC}	1.36	1.25	1.75	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62.5	62.5	62.5	°C/W

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B _{VDSS}	V _{GS} =0V, I _D =250μA	60	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	--	--	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	--	--	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0V	--	--	-100	
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	1.4	1.9	2.5	V
Static Drain- Source On State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A	--	12.0	20	mΩ
		V _{GS} =4.5V, I _D =10A	--	16.5	29	
Dynamic Characteristics						
Gate Resistance	R _g	V _{GS} =0V; f=1.0MHZ	1	6.5	10	Ω
Input Capacitance	C _{iss}	V _{DS} =25V V _{GS} =0V f=1.0MHZ	--	1750	--	pF
Output Capacitance	C _{oss}		--	125	--	
Reverse Transfer Capacitance	C _{rss}		--	105	--	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =30V, V _{GS} =10V R _G =25Ω; I _D =20A (Note 3.4)	--	15.3	--	ns
Turn-on Rise Time	t _r		--	72.6	--	
Turn-off Delay Time	t _{d(off)}		--	185	--	
Turn-off Fall Time	t _f		--	75.1	--	

Total Gate Charge	Q_g	$V_{DS}=30V, I_D=30A$ $V_{GS}=10V$ (Note 3.4)	--	49.5	--	nc
Gate-Source Charge	Q_{gs}		--	10.5	--	
Gate-Drain Charge	Q_{gd}		--	16.1	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	50	A
Pulsed Source Current	I_{SM}		--	--	200	
Diode Forward Voltage	V_{SD}	$I_S=30A, V_{GS}=0V$	--	0.82	1.2	V
Reverse Recovery Time	T_{rr}	$I_F=20A, V_R=15V,$ $dI_F/dt=100A/\mu S$	--	69	--	ns
Reverse Recovery Charge	Q_{rr}		--	42	--	nC

1. Pulse width limited by maximum junction temperature
2. $L=10mH, V_{DD}=50V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

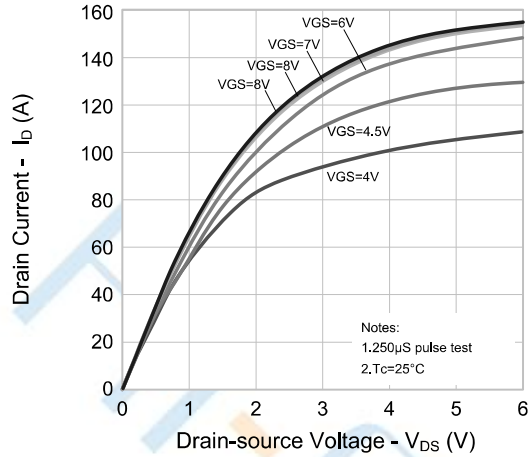


Figure 2. Transfer Characteristics

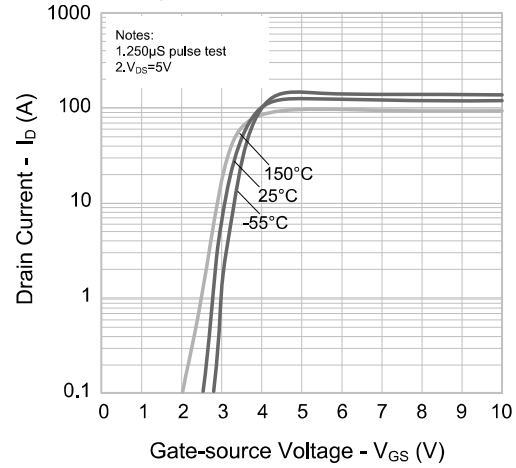


Figure 3. On-resistance vs. Drain Current

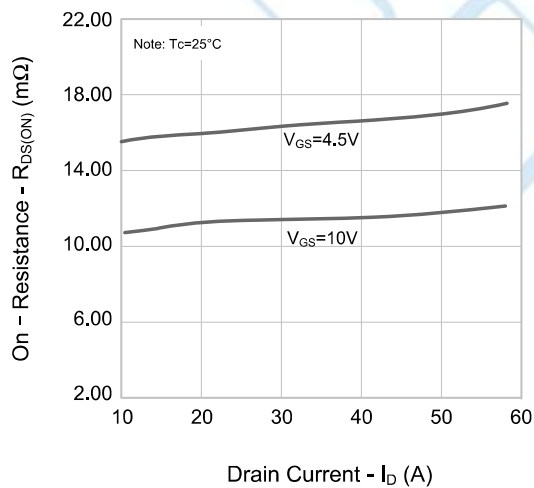


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

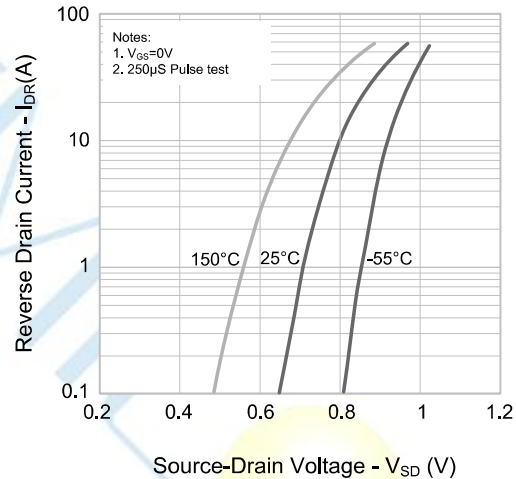


Figure 5. Capacitance Characteristics

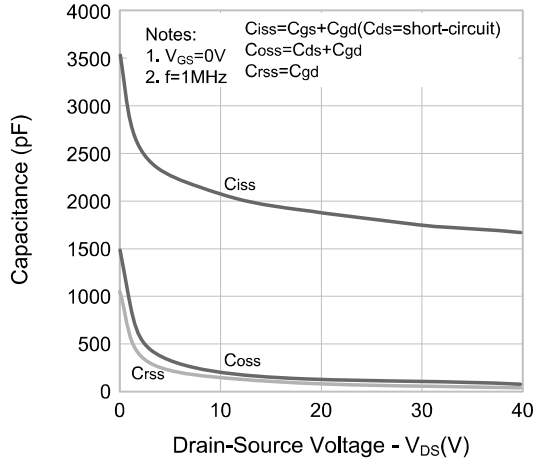
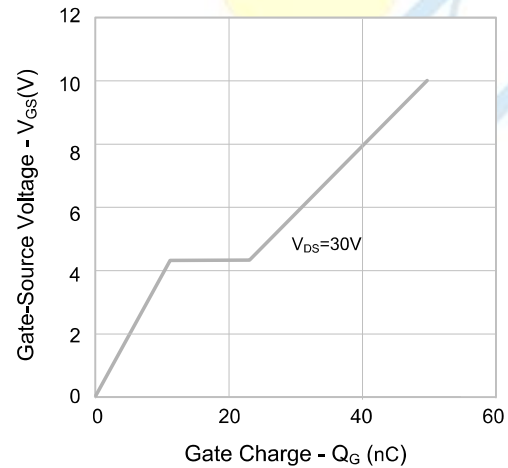


Figure 6. Gate Charge



Typical Performance Characteristics

Figure 7. Breakdown Voltage vs. Temperature Characteristics

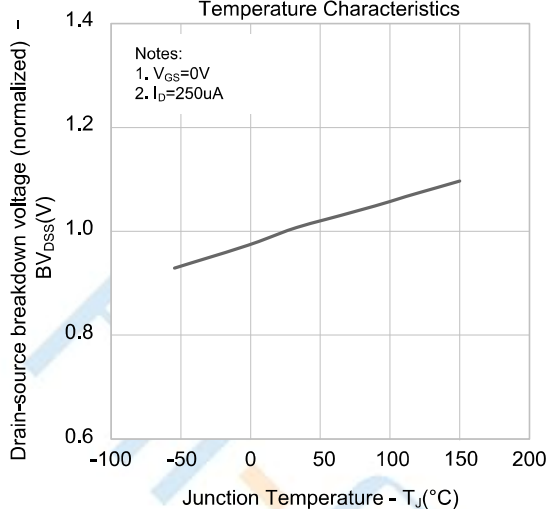


Figure 8. On-resistance vs. Temperature Characteristics

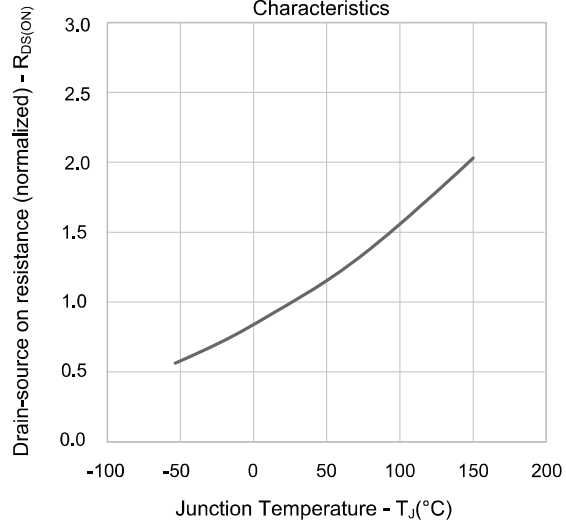
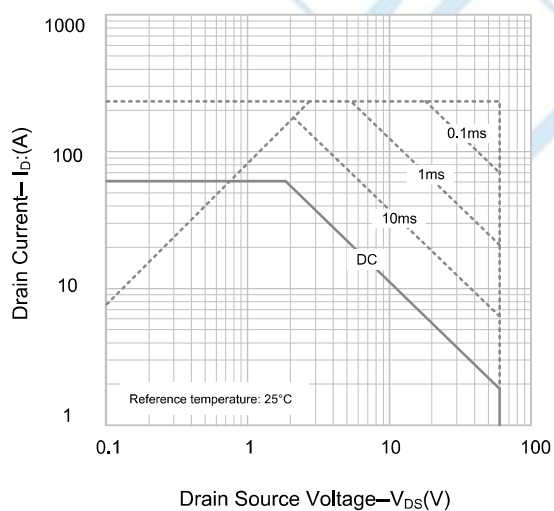
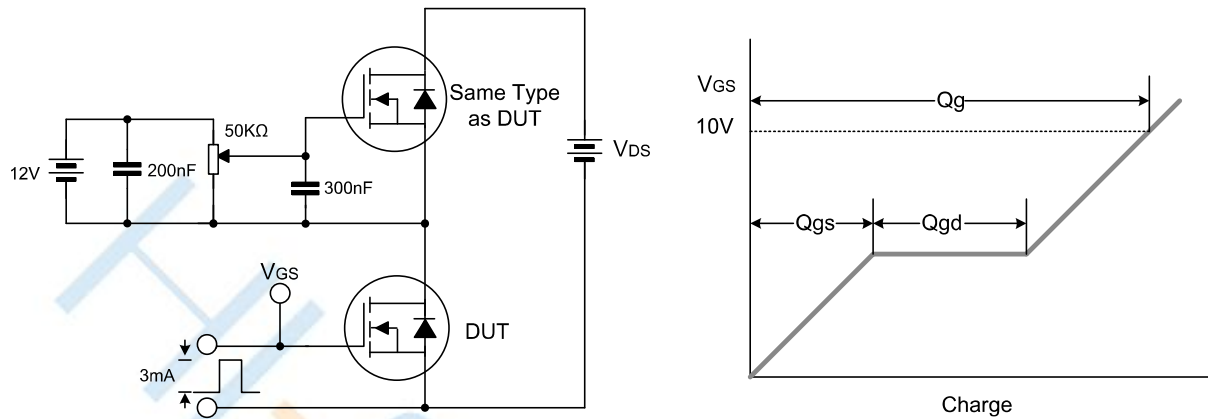


Figure 9. Max. Safe Operating Area

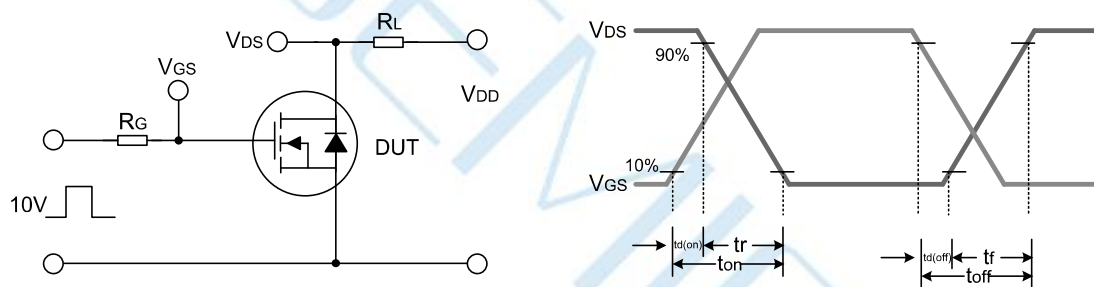


Test Circuit

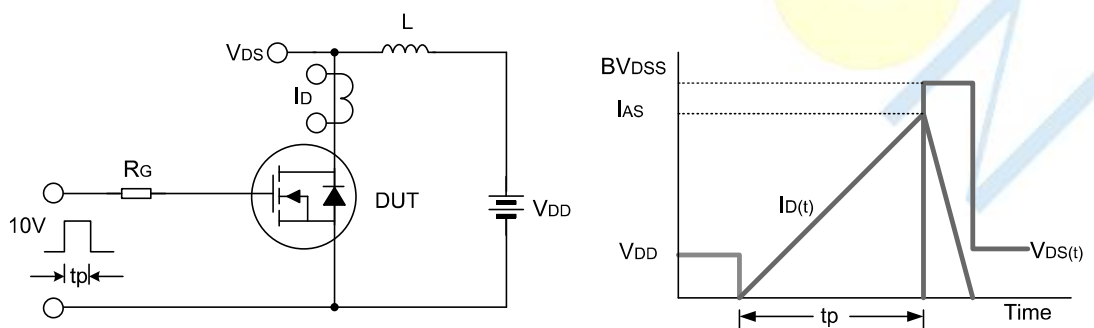
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

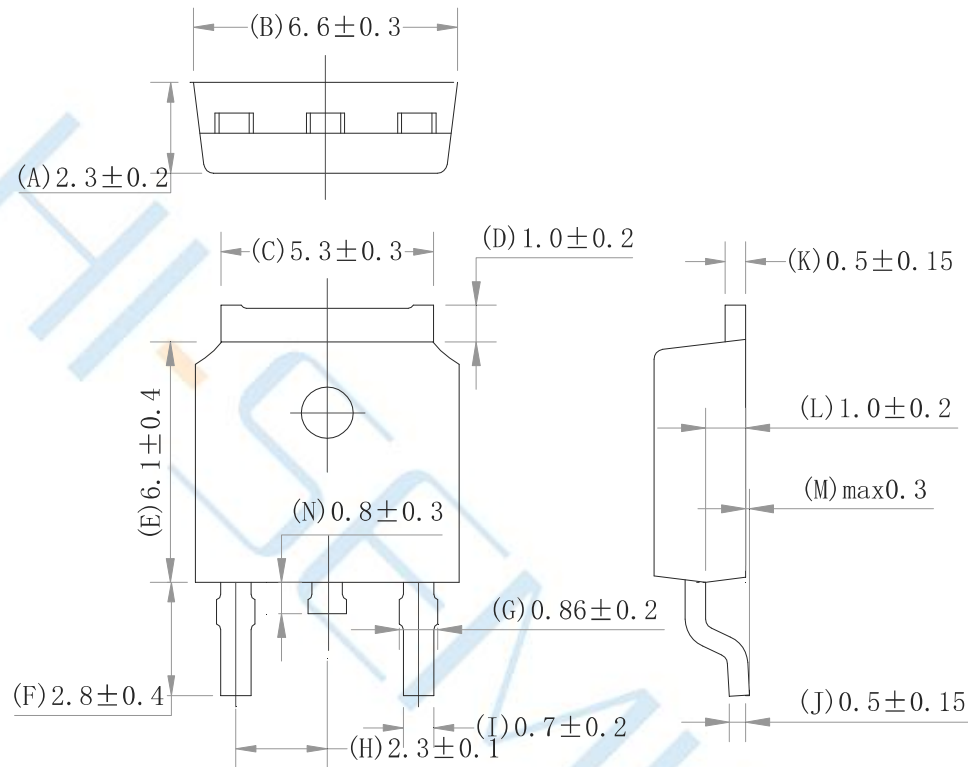


Unclamped Inductive Switching Test Circuit & Waveform



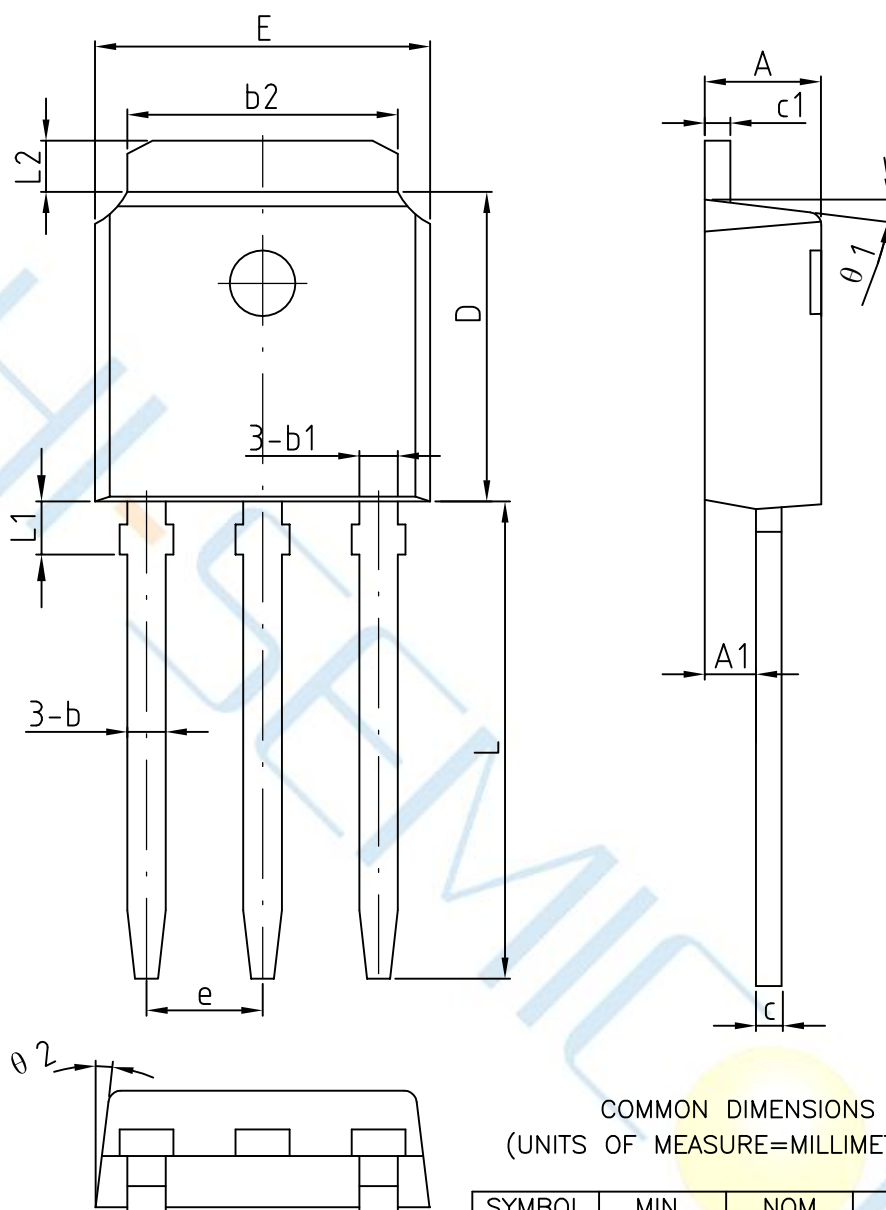
Package Dimensions of TO-252-2L

Unit:mm



Package Dimensions of TO-251J-3L

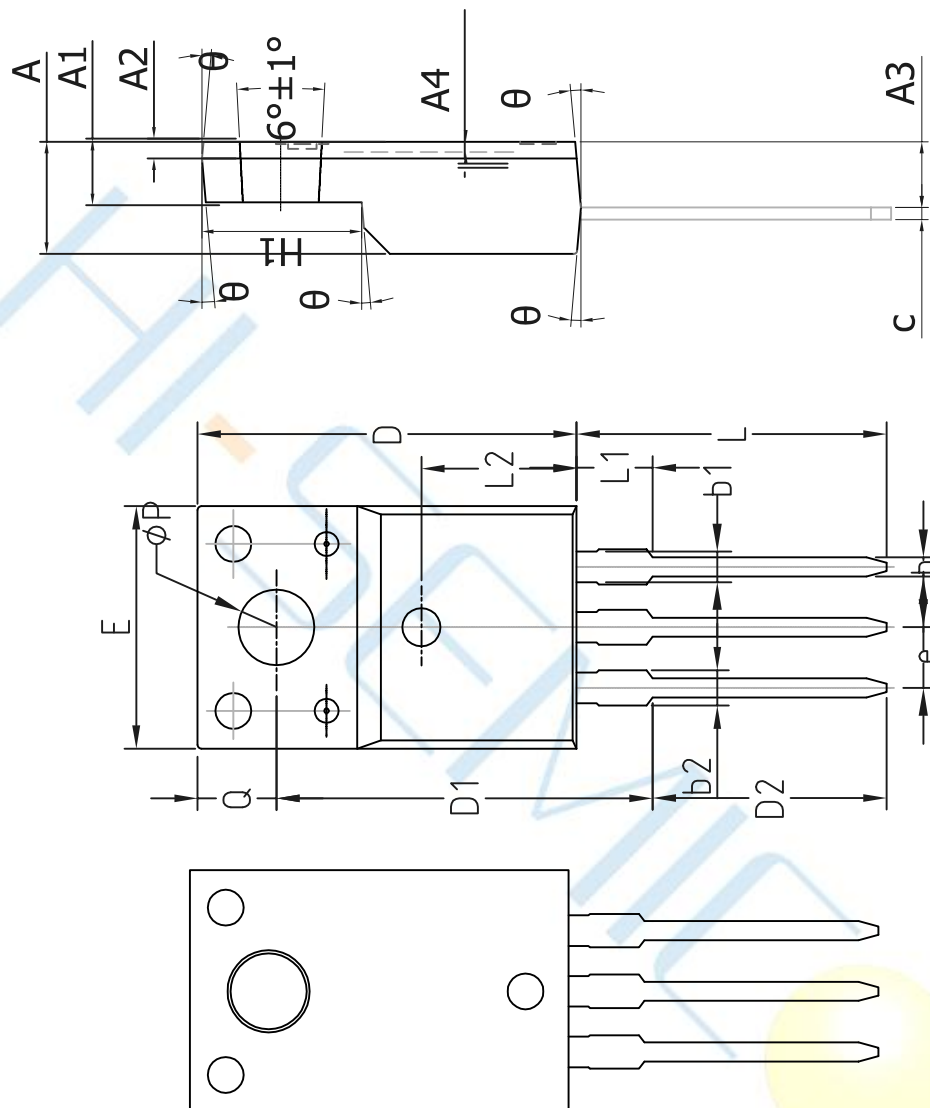
Unit:mm


COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	2.2	2.30	2.38
A1	0.90	1.01	1.10
b	0.71	0.76	0.86
b1	—	0.76	—
b2	5.13	5.33	5.46
c	0.46	0.50	0.60
c1	0.46	0.50	0.60
D	6.00	6.10	6.20
E	6.50	6.60	6.70
e	2.286BSC		
L	9.10	9.40	9.70
L1		1.05	
L2	0.90	—	1.25
θ 1		7°	
θ 2		7°	

Package Dimensions of TO-220F-3L

Unit:mm

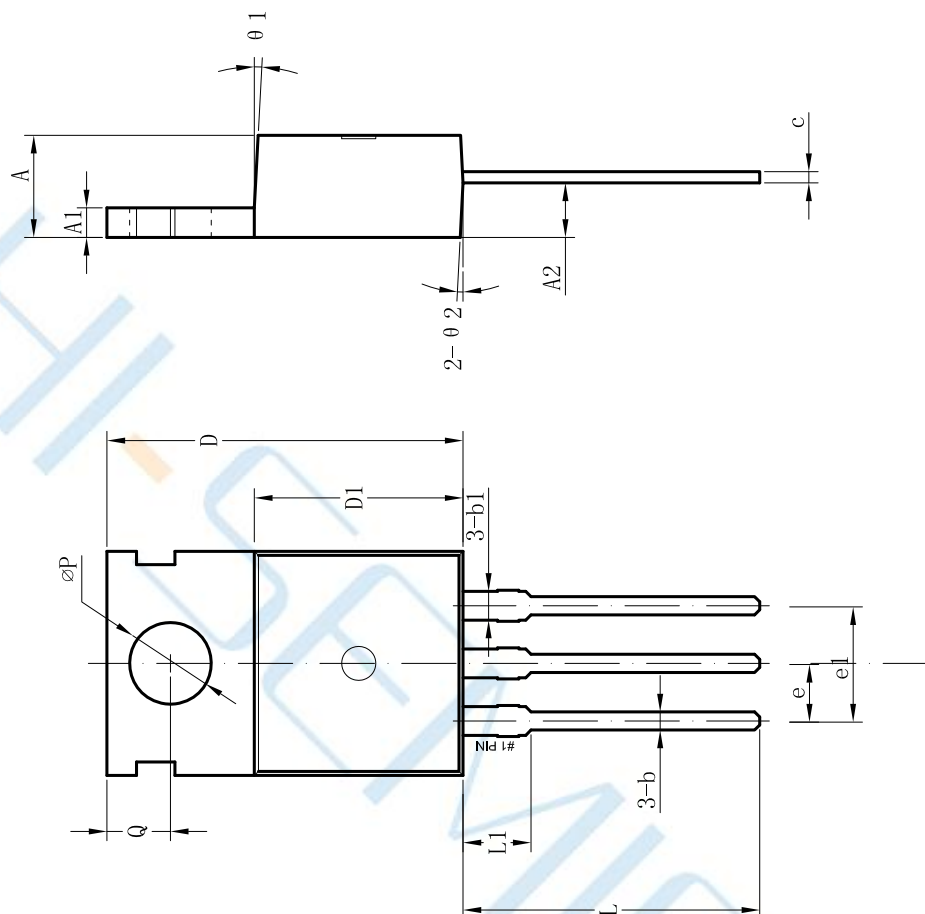


COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.96
b	0.70	0.80	0.90
b1	1.17	1.2	1.25
b2	1.17	1.2	1.25
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	10.0	10.2	10.4
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	3.50
L2	6.50REF		
φP	3.08	3.18	3.28
Q	3.20	3.30	3.40
θ 1	1°	3°	5°
A4	0.53	0.56	0.59

Package Dimensions of TO-220-3L

Unit:mm



Symbol	Mechanical Dimension/mm	
	Min	Max
A	4.30	4.70
A1	1.25	1.40
A2	2.20	2.60
b	0.70	0.95
b1		1.27
c	0.40	0.50
D	15.20	16.20
D1	9.00	9.40
E	9.70	10.10
e		2.54
e1		5.08
L	12.60	13.60
L1		3.00
φP	3.50	3.80
Q	2.60	3.00
θ1		3°
θ2		3°
θ3		3°

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