



SG12232Q (122 DOTS X 32 DOTS)

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FEATURES

- ◆ BUILT-IN CONTROLLER (SED1520 OR EQUIVALENT)
- ◆ +5 V POWER SUPPLY
- ◆ 1/32 DUTY CYCLE
- ◆ 8-BIT PARALLEL INTERFACE
- ◆ 4.2 V LED FORWARD VOLTAGE

Mechanical Data

ITEM	DIMENSIONS	UNIT
Module Size (W x H x T)	66.2 x 33.2 x 8.8	mm
Viewing Area (W x H)	60.5 x 18.5	mm
Active Area (W x H)	53.64 x 15.64	mm
Dot Size (W x H)	0.40 x 0.45	mm
Dot Pitch (W x H)	0.44 x 0.49	mm

INTERFACE PIN CONNECTIONS

NO.	SYMBOL	LEVEL	FUNCTION
1	Vss	0V	Power Supply Ground
2	VDD	5V	Power Supply Voltage
3	Vo	-	Contrast Adjustment Voltage
4	Ao	H/L	Data Type Select Signal
5	R/W	H/L	H : Read / L : Write
6	E1	H	Chip Select Signal For IC1
7	E2	H	Chip Select Signal For IC2
8	K	0V	LED Power (-)
9~16	DB0~DB7	H/L	Data Bus Line

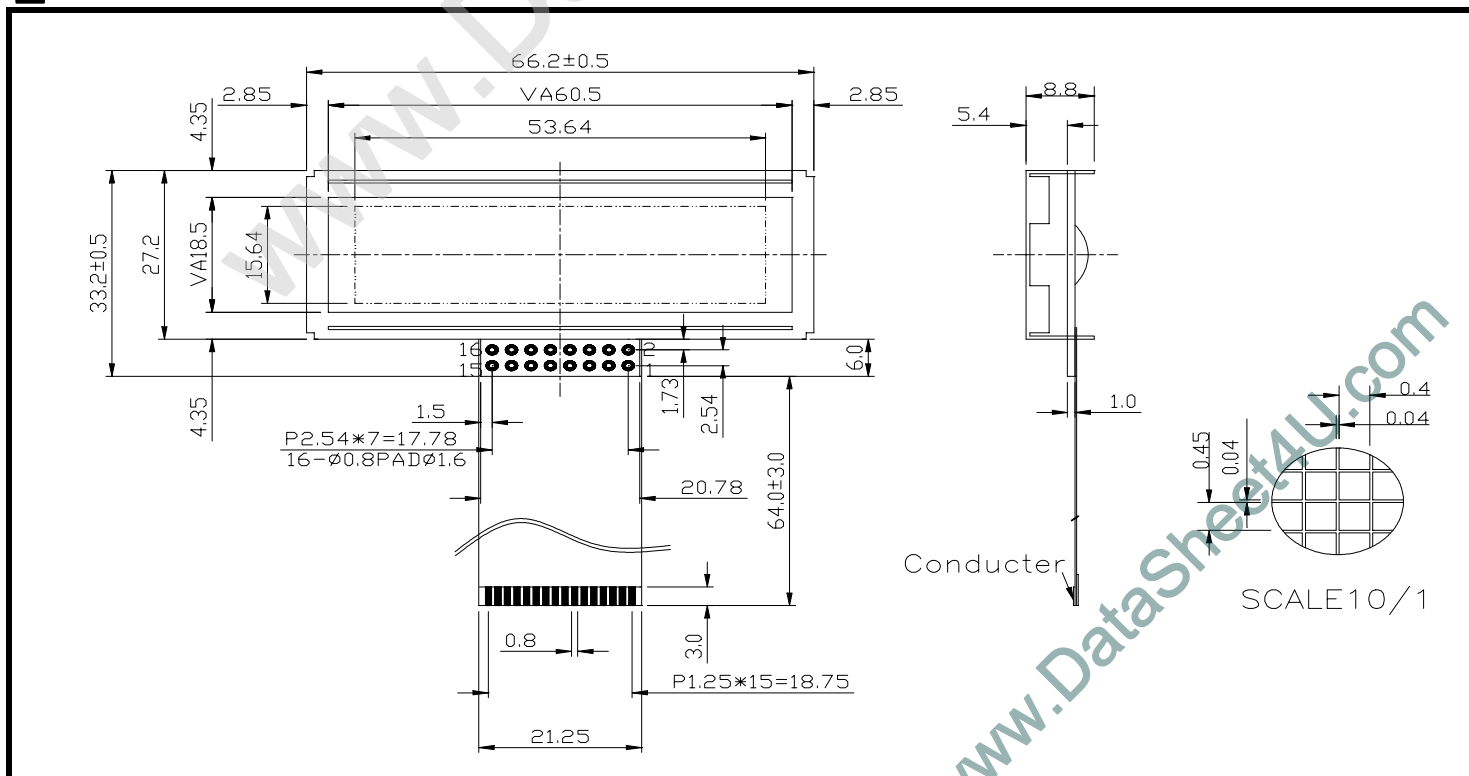
ABSOLUTE MAXIMUM RATINGS

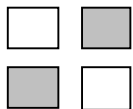
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic	VDD-Vss	0	-	7	V
Supply Voltage For LCD Drive	VDD-Vo	0	-	12	V
Input Voltage	Vi	Vss	-	VDD	V

ELECTRICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic		VDD-VSS	-	4.5	5	5.5	V
Supply Voltage For LCD		VDD-Vo	VDD=5V Ta=25°C	4.4	4.8	5.2	V
Supply Current		IDD	VDD=5V	-	2.5	4.5	mA
Input Voltage	"HIGH" Level	VIH	-	2.2	-	VDD	V
	"LOW" Level	VIL	-	-	-	0.6	V
Output Voltage	"HIGH" Level	VOH	-	2.4	-	-	V
	"LOW" Level	VOL	-	-	-	0.4	V

EXTERNAL DIMENSIONS

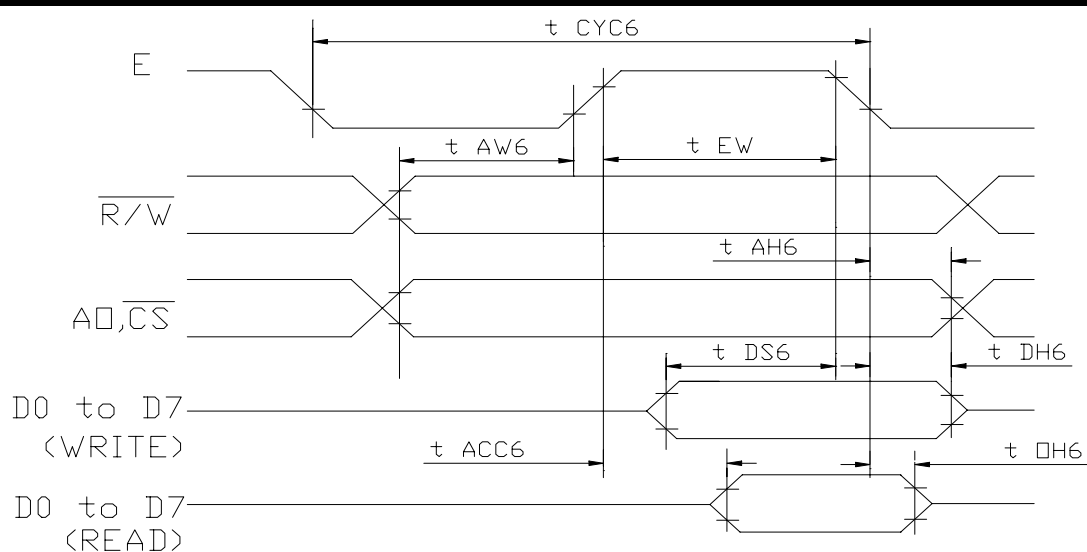




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TIMING CHARACTERISTICS

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT.
System Cycle Time		t_{CYC6}	1000	-	-	ns
Address Set-up Time		t_{AW6}	20	-	-	ns
Address Hold Time		t_{AH6}	10	-	-	ns
Data Set-up Time		t_{DS6}	80	-	-	ns
Data Hold Time		t_{DH6}	10	-	-	ns
Output Disable Time		t_{OH6}	10	-	60	ns
Access Time		t_{ACC6}	-	-	90	ns
Enable Pulse Width	Read	t_{EW}	100	-	-	ns
	Write		80	-	-	ns



BLOCK DIAGRAM

