



CRYSTAL OSCILLATOR (SPXO)

OUTPUT : LV-PECL, LVDS

NEW

Product Number (please contact us)

SG3225EAN: X1G004251xxxx00

SG3225VAN: X1G004241xxxx00

SG5032EAN: X1G004271xxxx00

SG5032VAN: X1G004261xxxx00

SG7050EAN: X1G004291xxxx00

SG7050VAN: X1G004281xxxx00

SG3225EAN / VAN
SG5032EAN / VAN
SG7050EAN / VAN

Distributed by:



www.texim-europe.com

SG3225EAN/VAN (3.2 × 2.5 × 1.05 mm)
SG5032EAN/VAN (5.0 × 3.2 × 1.0 mm)
SG7050EAN/VAN (7.0 × 5.0 × 1.4 mm)
Actual size

- Frequency range : 73.5 MHz to 700 MHz
- Supply voltage : 2.5 V to 3.3 V
- Function : Output enable (OE)
- Output : LV-PECL or LVDS

SG3225EAN/VAN	SG5032EAN/VAN	SG7050EAN/VAN

Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks	
		LV-PECL	LVDS		
		SG3225EAN / SG5032EAN / SG7050EAN	SG3225VAN / SG5032VAN / SG7050VAN		
Output frequency range	f _o	73.5MHz to 700 MHz		Please contact us about available frequencies.	
Supply voltage	V _{cc}	K: 2.5V to 3.3V		V _{cc} =± 10%	
Storage temperature	T _{stg}	-40 °C to +125 °C		Storage as single product.	
Operating temperature	T _{use}	B: -20 °C to +70 °C, G: -40 °C to +85 °C			
Frequency tolerance	f _{tol}	J: ± 50 × 10 ⁻⁶ , E: ± 30 × 10 ⁻⁶ , C: ± 20 × 10 ⁻⁶			
Current consumption	I _{cc}	65 mA Max.	30 mA Max.	OE=V _{cc} , L _{ECL} =50 Ω or L _{LVDS} =100 Ω	
Disable current	I _{dis}	20 mA Max.		OE=GND	
Symmetry	SYM	45 % to 55 %		At outputs crossing point	
Output voltage (LV-PECL)	V _{OH}	V _{cc} -1.0 V to V _{cc} -0.8 V	—	DC characteristics	
	V _{OL}	V _{cc} -1.78 V to V _{cc} -1.62 V	—		
Output voltage (LVDS)	V _{OD}	—	250 mV to 450mV	V _{OD1} , V _{OD2}	DC characteristics
	dV _{OD}	—	50 mV Max.	dV _{OD} = V _{OD1} -V _{OD2}	
	V _{OS}	—	1.15 V to 1.35 V	V _{OS1} , V _{OS2}	
	dV _{OS}	—	150 mV Max.	dV _{OS} = V _{OS1} -V _{OS2}	
Output load condition (ECL) / (LVDS)	L _{ECL}	50 Ω	—	Terminated to V _{cc} -2.0 V	
	L _{LVDS}	—	100 Ω	Connected between OUT to <u>OUT</u>	
Input voltage	V _{IH}	70 % V _{cc} Min.		OE terminal	
	V _{IL}	30 % V _{cc} Max.			
Rise time / Fall time	t _r / t _f	350 ps Max.	300 ps Max.	Between 20 % and 80 % of (V _{OH} -V _{OL}). Between 20 % and 80 % of Differential Output peak to peak voltage	
Start-up time	t _{str}	3 ms Max.		Time at minimum supply voltage to be 0 s	
Phase Jitter	t _{PJ}	0.7 ps Max.		Offset frequency: 12 kHz to 20 MHz	
Frequency aging	f _{aging}	± 5 × 10 ⁻⁶ / year Max.		+25 °C, First year, V _{cc} =2.5 V,3.3 V	

Part number **SG3225EAN** **156.250000MHZ** **K J G A** * Unavailable Combination : KCGA

Version code : A

Model Name

Frequency

Operating temperature

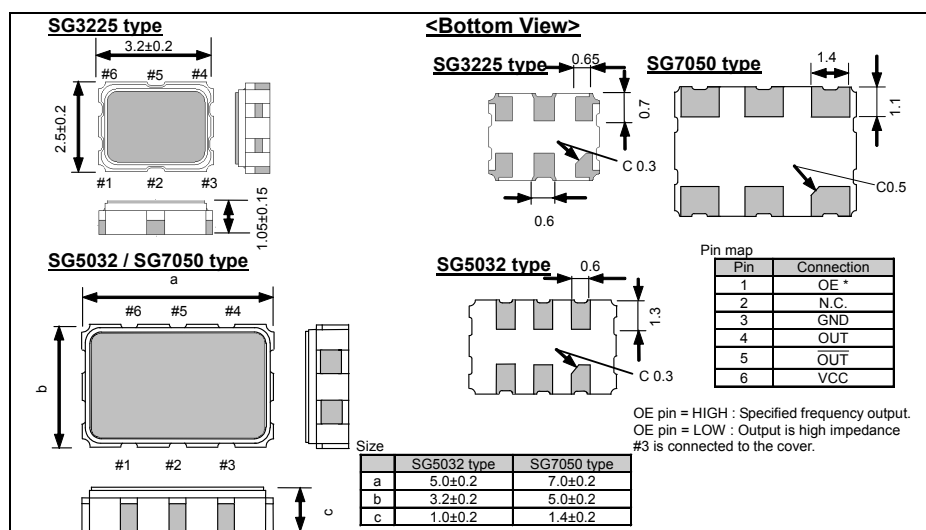
B: -20 °C to +70 °C, G: -40 °C to +85 °C

Frequency tolerance J: ±50 × 10⁻⁶, E: ±30 × 10⁻⁶, C: ±20 × 10⁻⁶

Supply Voltage K: 2.5 to 3.3 V

External dimensions

(Unit:mm)

**Footprint (Recommended)**

(Unit:mm)

