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### DC to 5000 MHz, CASCADABLE SiGe HBT MMIC AMPLIFIER

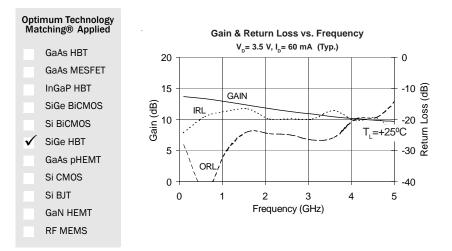


# Package: SOT-86



### **Product Description**

The SGA5286Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high  $F_{\rm T}$  and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.



#### Features

- High Gain: 12.0dB at 1950MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

### **Applications**

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Parameter	Specification			Unit	Condition		
Farameter	Min.	Тур.	Max.	Unit	Condition		
Small Signal Gain	12.2	13.5	14.9	dB	850MHz		
		12.0		dB	1950MHz		
		11.5		dB	2400MHz		
Output Power at 1dB Compression		17.0		dBm	850MHz		
		14.0		dBm	1950MHz		
Output Third Intercept Point		31.0		dBm	850MHz		
		27.2		dBm	1950MHz		
Bandwidth Determined by Return Loss		5000		MHz	>10dB		
Input Return Loss		19.5		dB	1950MHz		
Output Return Loss		24.4		dB	1950MHz		
Noise Figure		4.9		dB	1950MHz		
Device Operating Voltage	3.1	3.5	3.9	V			
Device Operating Current	54	60	66	mA			
Thermal Resistance (Junction - Lead)		97		°C/W			

Test Conditions:  $V_S = 8V$ ,  $I_D = 60$  mA Typ., OIP<sub>3</sub> Tone Spacing=1MHz,  $P_{OUT}$  per tone=-5dBm,  $R_{BIAS} = 75\Omega$ ,  $T_L = 25$  °C,  $Z_S = Z_L = 50\Omega$ 

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#### **Absolute Maximum Ratings**

5				
Parameter	Rating	Unit		
Max Device Current (I <sub>D</sub> )	120	mA		
Max Device Voltage (V <sub>D</sub> )	5	V		
Max RF Input Power	+16	dBm		
Max Junction Temp (T <sub>J</sub> )	+150	°C		
Operating Temp Range (T <sub>L</sub> )	-40 to +85	°C		
Max Storage Temp	+150	°C		

Operation of this device beyond any one of these limits may cause permanent dam-age. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression:

 $I_D V_D < (T_J - T_L) / R_{TH}$ , j-l

#### **Typical Performance at Key Operating Frequencies**

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating condi-tions is not implied.

RoHS status based on EU Directive 2002/95/EC (at time of this document revision).

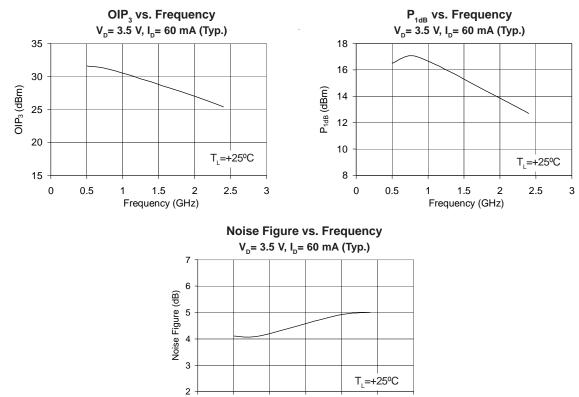
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Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain	dB	13.7	13.6	13.5	12.0	11.5	10.5
Output Third Order Intercept Point	dBm		31.6	31.0	27.2	25.4	
Output Power at 1dB Compression	dBm		16.5	17.0	14.0	12.7	
Input Return Loss	dB	24.2	20.4	17.1	19.5	19.4	16.7
Output Return Loss	dB	28.2	34.5	35.3	24.4	23.8	26.2
Reverse Isolation	dB	17.7	18.0	18.4	19.2	19.5	19.5
Noise Figure	dB		4.1	4.1	4.9	5.0	

Test Conditions:  $V_S = 8V$ ,  $I_D = 60$  mA Typ., OIP<sub>3</sub> Tone Spacing=1MHz,  $P_{OUT}$  per tone=-5dBm,  $R_{BIAS} = 75\Omega$ ,  $T_L = 25$  °C,  $Z_S = Z_L = 50\Omega$ 





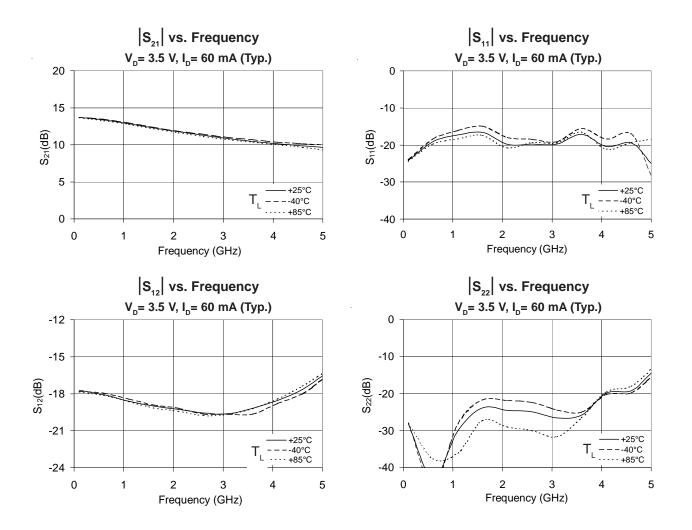


0.5 1 1.5 2 2.5 Frequency (GHz)

3

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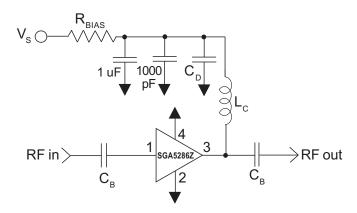




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Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefor a DC-blocking capacitor is necessary for proper opera- tion.

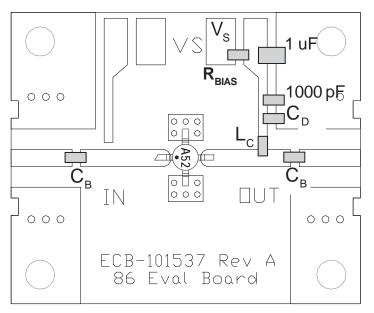
**Application Schematic** 



#### Frequency (Mhz) Reference Designator 850 1950 2400 3500 500 C<sub>B</sub> 220 pF 100 pF 68 pF 56 pF 39 pF $\mathbf{C}_{\mathrm{D}}$ 22 pF 100 pF 68 pF 22 pF 15 pF L 68 nH 33 nH 22 nH 18 nH 15 nH

Recommended Bias Resistor Values for $I_p$ =60mA $R_{BIAS}$ =( $V_s$ - $V_p$ ) / $I_p$				
Supply Voltage(V <sub>s</sub> )	6 V	8 V	10 V	12 V
R <sub>BIAS</sub>	43 Ω	75 Ω	110Ω	150 Ω
Note: R <sub>BIAS</sub> provides DC bias stability over temperature.				

# **Evaluation Board Layout**

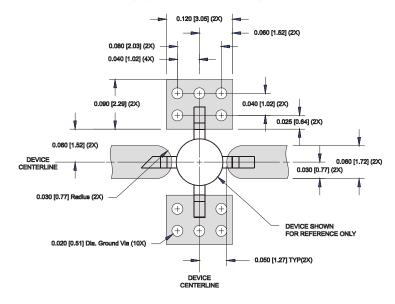


### **Mounting Instructions**

- 1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
- 2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.



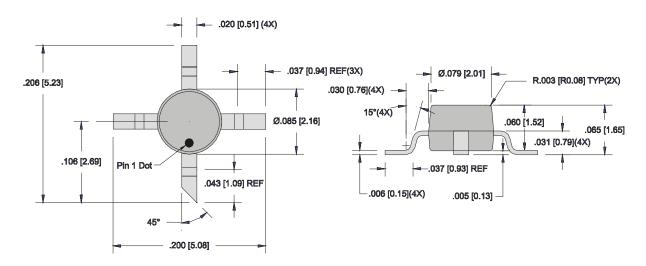




# **Suggested Pad Layout**

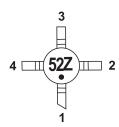
## **Package Drawing**

Dimensions in inches (millimeters) Refer to drawing posted at www.rfmd.com for tolerances.





# **Part Identification**



# **Ordering Information**

Ordering Code	Description	
SGA5286Z	13" Reel with 3000 pieces	
SGA5286ZSQ	Sample bag with 25 pieces	
SGA5286ZSR	7" Reel with 100 pieces	
SGA5286ZPCK1	850MHz, 8V Operation PCBA with 5-piece sample bag	