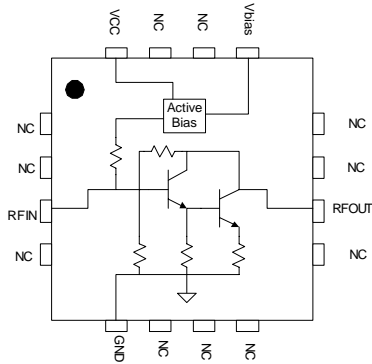


## Product Description

Sirenza Microdevices' SGB-6433 is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply the SGB-6433 does not require a drop resistor as compared to typical Darlington amplifiers. This robust amplifier features a Class 1C ESD rating, low thermal resistance, and unconditional stability. The SGB-6433 product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is on chip matched to 50 ohm and an external bias inductor choke is required for the application band.

This product is available in a RoHS Compliant and Green package with matte tin finish, designated by the "Z" package suffix.

**Functional Block Diagram**

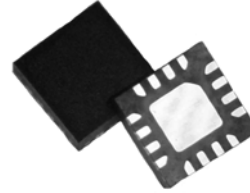


## SGB-6433

## SGB-6433Z



### DC–3.5 GHz Active Bias Gain Block



### Product Features

- Available in Lead Free, RoHS compliant, & Green Packaging
- High reliability SiGe HBT Technology
- Robust Class 1C ESD
- Simple and small size
- P1dB = 18.5 dBm @ 1950MHz
- IP3 = 31 dBm @ 1950MHz
- Low Thermal Resistance = 60 C/W

### Applications

- 5V applications
- LO buffer amp
- RF pre-driver and RF receive path

### Key Specifications

| Symbol        | Parameters: Test Conditions<br>$Z_0 = 50\Omega$ , $V_{CC} = 5.0V$ , $I_c = 88mA$ , $T = 30^\circ C$ | Unit         | Min. | Typ. | Max. |
|---------------|---|--------------|------|------|------|
| $f_O$         | Frequency of Operation  | MHz          | DC   |      | 3500 |
| $S_{21}$      | Small Signal Gain – 850MHz  | dB           |      | 20.0 |      |
|               | Small Signal Gain – 1950MHz   |              | 14.5 | 16.0 | 17.5 |
|               | Small Signal Gain – 2400MHz   |              |      | 15.0 |      |
| $P_{1dB}$     | Output Power at 1dB Compression – 850MHz  | dBm          |      | 18.5 |      |
|               | Output Power at 1dB Compression – 1950MHz   |              | 16.5 | 18.5 |      |
|               | Output Power at 1dB Compression – 2400MHz   |              |      | 17.5 |      |
| OIP3          | Output IP3 – 850MHz   | dB           |      | 33.0 |      |
|               | Output IP3 – 1950MHz  |              | 28.5 | 31.0 |      |
|               | Output IP3 – 2400MHz  |              |      | 31.0 |      |
| IRL           | Input Return Loss @1950MHz  | dB           | 12.0 | 15   |      |
| ORL           | Output Return Loss @1950MHz   | dB           | 8.5  | 11.5 |      |
| $I_c$         | Current   | mA           | 76   | 88   | 98   |
| NF            | Noise Figure @1950MHz   | dB           |      | 4.1  | 5.1  |
| $R_{th, j-l}$ | Thermal Resistance (junction - lead)  | $^\circ C/W$ |      | 60   |      |

The information provided herein is believed to be reliable at press time. Sirenza Microdevices assumes no responsibility for inaccuracies or omissions.

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Phone: (800) SMI-MMIC

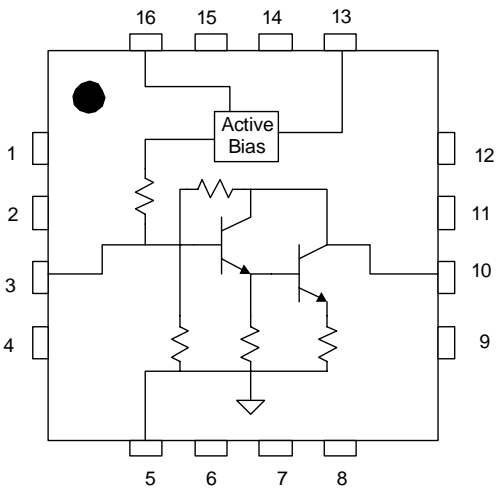
### Detailed Performance Table: Vcc=5V, Ic=88mA, T=25C, Z=50ohms

| Symbol | Parameter                        | Units | 100MHz | 500MHz | 850MHz | 1950MHz | 2400MHz | 3500MHz |
|--------|----------------------------------|-------|--------|--------|--------|---------|---------|---------|
| G      | Small Signal Gain                | dB    | 21.2   | 20.7   | 20.0   | 16.0    | 15.0    | 12.3    |
| OIP3   | Output 3rd Order Intercept Point | dBm   |        | 34.0   | 33.0   | 31.0    | 31.0    |         |
| P1dB   | Output Power at 1dB Compression  | dBm   |        | 18.9   | 18.5   | 18.5    | 17.5    |         |
| IRL    | Input Return Loss                | dB    | 43.6   | 33.3   | 25.6   | 15.0    | 13.8    | 9.7     |
| ORL    | Output Return Loss               | dB    | 15.8   | 13.9   | 12.2   | 11.5    | 10.2    | 11.3    |
| S12    | Reverse Isolation                | dB    | 24.4   | 24.6   | 25.0   | 24.4    | 23.8    | 22.5    |
| NF     | Noise Figure                     | dB    | 5.1    | 3.6    | 3.6    | 4.1     | 4.6     | 5.2     |

### Pin Out Description

| Pin #                | Function | Description   |
|----------------------|----------|---|
| 1,2,4,6,7,8,11,12,14 | NC       | These are no connect pins. Leave them unconnected on the PC board.  |
| 3                    | RFIN     | RF input pin. A DC voltage should not be connected externally to this pin   |
| 5                    | GND      | An extra ground pin that is connected to the backside exposed paddle. Connection is optional.   |
| 10                   | RFOUT    | RF Output pin. Bias is applied to the Darlington stage thru this pin.   |
| 13                   | VBIAS    | This pin sources the current from the active bias circuit. Connect to pin 10 thru an inductor choke.  |
| 16                   | VCC      | This is Vcc for the active bias circuit.  |
| Back-side            | GND      | The backside exposed paddle is the main electrical GND and requires multiple vias in the PC board to GND. It is also the main thermal path. |

### Simplified Device Schematic



### Caution: ESD Sensitive

Appropriate precaution in handling, packaging and testing devices must be observed.

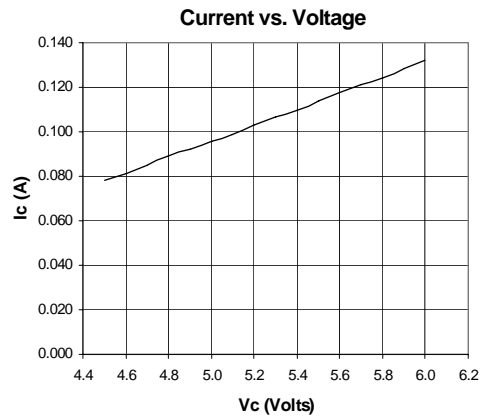
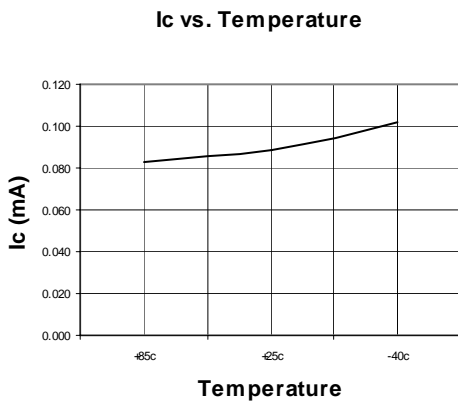
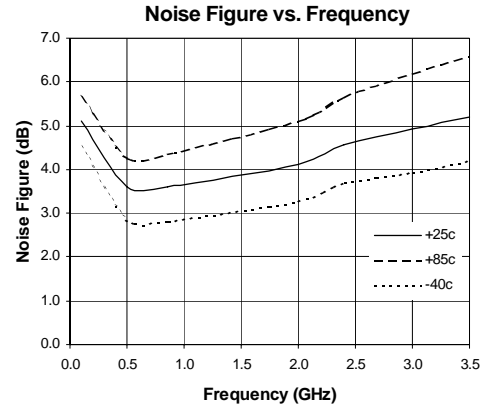
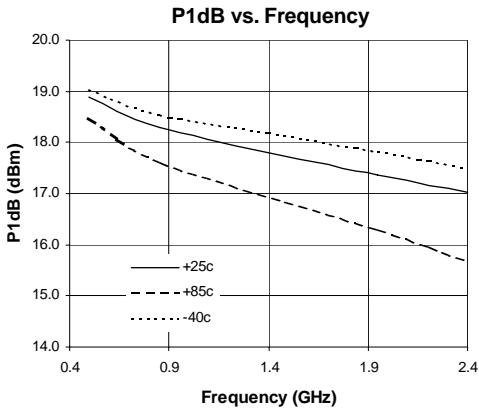
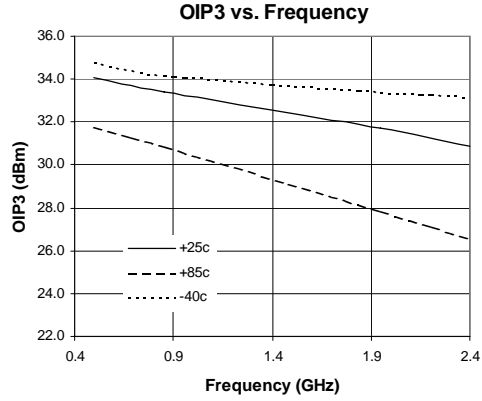
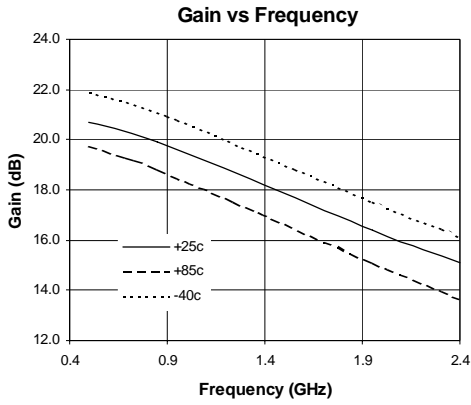
### Absolute Maximum Ratings

| Parameters                                       | Value       | Unit |
|--|-------------|------|
| Current (Ic total)                               | 150         | mA   |
| Device Voltage (V <sub>D</sub> )                 | 6.5         | V    |
| Power Dissipation                                | 0.75        | W    |
| Operating Lead Temperature (T <sub>L</sub> )     | -40 to +85  | °C   |
| RF Input Power                                   | 20          | dBm  |
| Storage Temperature Range                        | -40 to +150 | °C   |
| Operating Junction Temperature (T <sub>J</sub> ) | +150        | °C   |

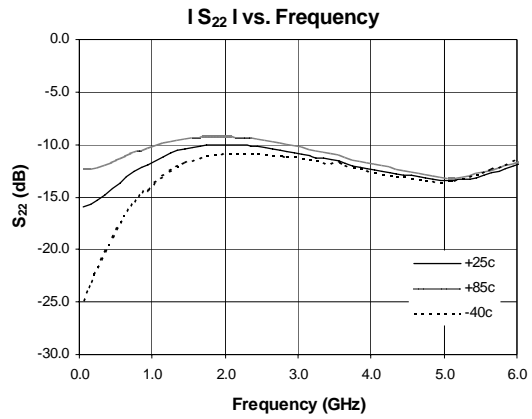
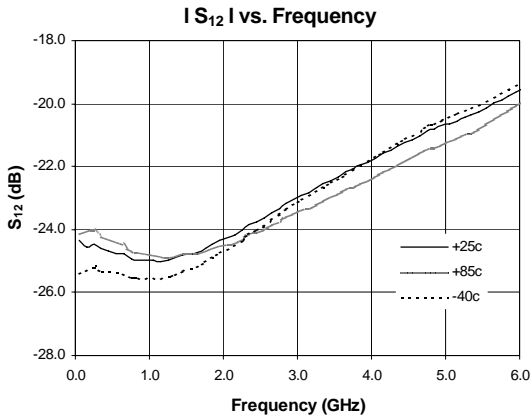
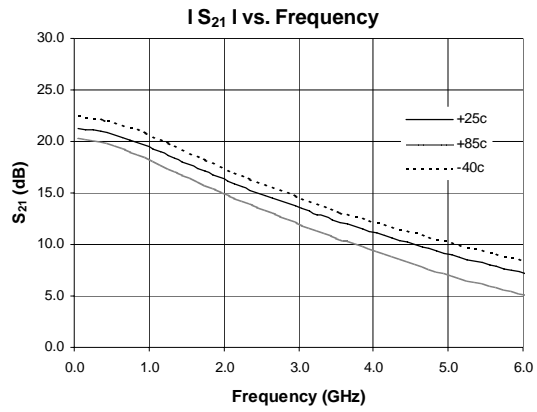
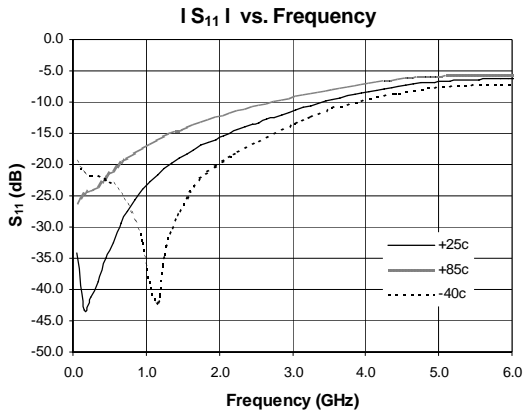
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias conditions should also satisfy the following expression:  
 $I_D V_D < (T_J - T_L) / R_{TH} \cdot I$

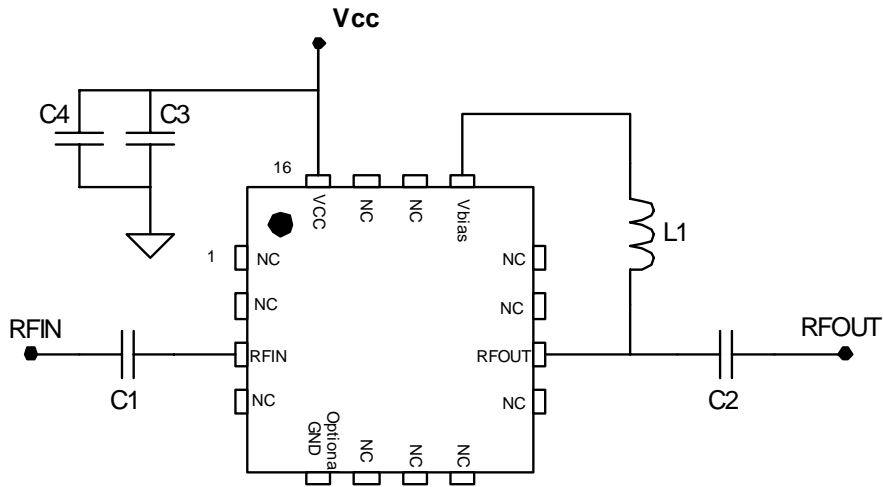
Evaluation Board Data ( $V_{CC}=V_{BIAS} = 5.0V$ ,  $I_c = 88mA$ ) Bias Tee substituted for DC feed inductor (L1)



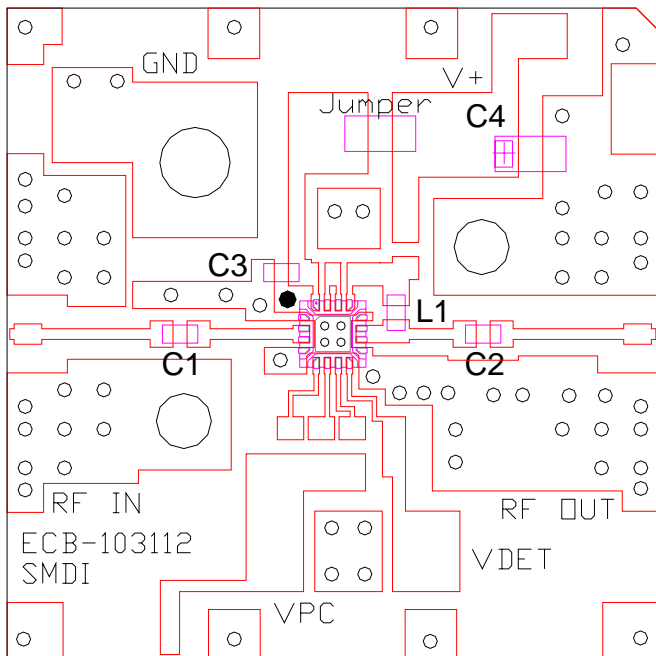
Evaluation Board Data ( $V_{CC}=V_{BIAS} = 5.0V$ ,  $I_C = 88mA$ ) Bias Tee substituted for DC feed inductor (L1)



Typical Evaluation Board Schematic for 5.0V



Evaluation Board - Board material GETEK, 31mil thick, Dk=4.2, 1 oz. copper



Component Values By Band

| Designator | 500MHz | 850MHz | 1950MHz | 2400MHz |
|------------|--------|--------|---------|---------|
| C3         | 1000pF | 1000pF | 1000pF  | 1000pF  |
| C4*        | 1uF    | 1uF    | 1uF     | 1uF     |
| C1, C2     | 220pF  | 68pF   | 43pF    | 22pF    |
| L1         | 68 nH  | 33nH   | 22nH    | 18nH    |

\* C4 is optional depending on application and filtering. Not required for SGB device operation.

**Note:** The amplifier can be run from a 8V supply by simply inserting a 33 ohm resistor in series with Vcc.

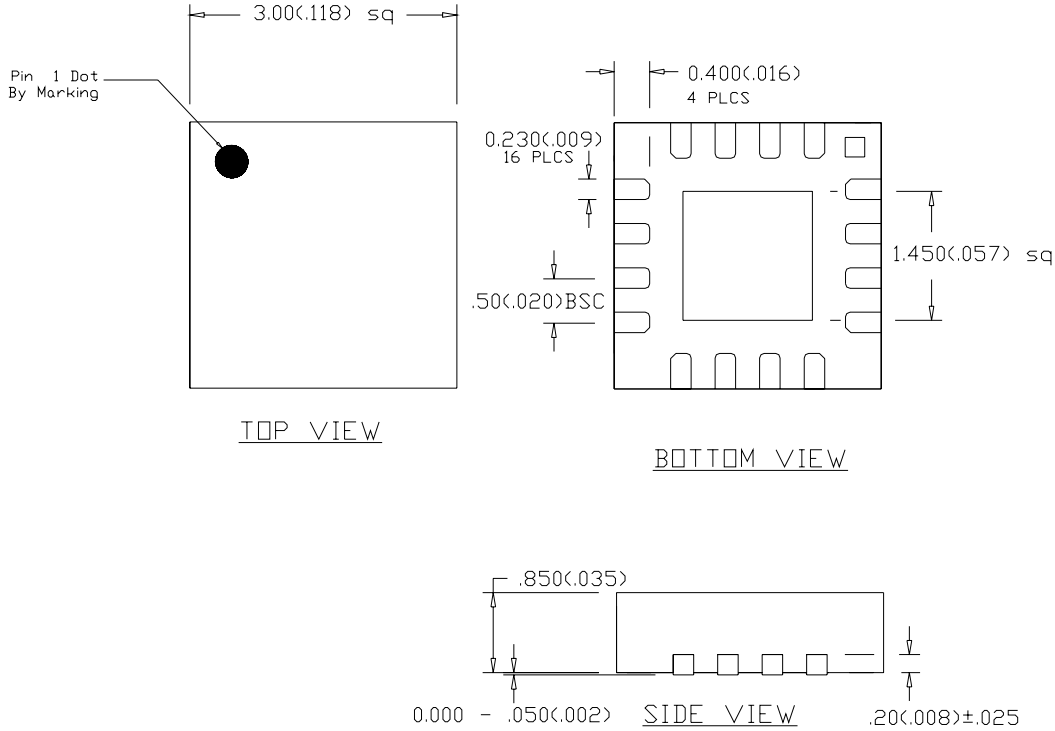
**Part Marking**

The part will be symbolized with an "SGB-6433" for Sn/Pb plating or "SGB-64Z" for RoHS green compliant product. Marking designator will be on the top surface of the package.

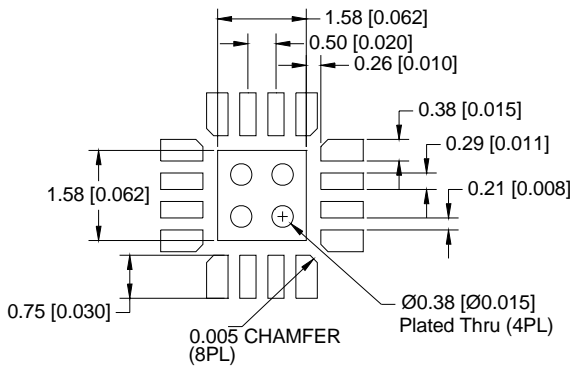
**Part Number Ordering Information**

| Part Number | Reel Size | Devices/Reel |
|-------------|-----------|--------------|
| SGB-6433    | 13"       | 3000         |
| SGB-6433Z   | 13"       | 3000         |

**Package Outline Drawing (Dimensions in mm)**



**Recommended Land Pattern (dimensions in mm[in.]):**



**Recommended PCB Soldermask (SMOBC) for Land Pattern(dimensions in mm[in.]):**

