

GENERAL DESCRIPTION

The SGM8044 is guaranteed to operate with a single supply voltage as low as 1.4V, while drawing less than 670nA (TYP) of quiescent current per amplifier. This device is also designed to support rail-to-rail input and output operation. This combination of features supports battery-powered and portable applications.

The SGM8044 has a gain-bandwidth product of 15kHz (TYP) and is unity gain stable. These specifications make this operational amplifier appropriate for low frequency applications, such as battery current monitoring and sensor conditioning.

The SGM8044 is offered in quad configuration. It is specified for the extended industrial (-40°C to +85°C) temperature range. The SGM8044 is available in the Green SOIC-14, TSSOP-14 and TQFN-3x3-16L packages.

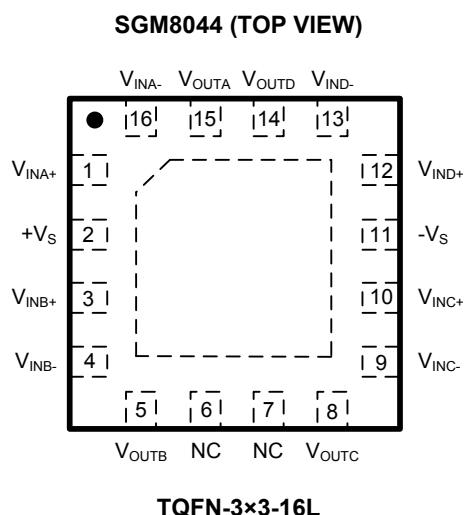
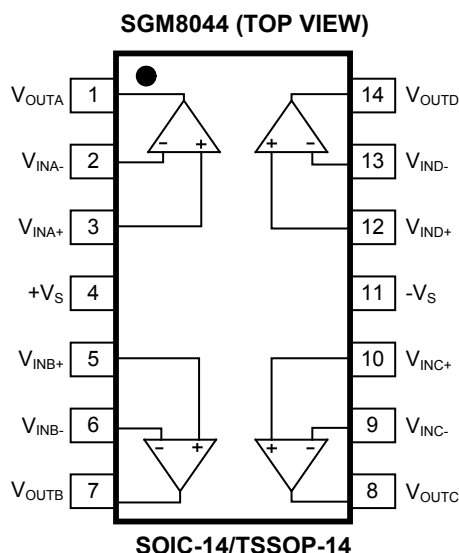
FEATURES

- **Low Quiescent Current: 670nA/Amplifier (TYP)**
- **Rail-to-Rail Input and Output**
- **Gain-Bandwidth Product: 15kHz at $V_S = 5V$ (TYP)**
- **Wide Supply Voltage Range: 1.4V to 5.5V**
- **Unity Gain Stable**
- **-40°C to +85°C Operating Temperature Range**
- **Available in Green SOIC-14, TSSOP-14 and TQFN-3x3-16L Packages**

APPLICATIONS

- Toll Booth Tags
- Wearable Products
- Temperature Measurement
- Battery Powered System

PIN CONFIGURATIONS



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8044	SOIC-14	-40°C to +85°C	SGM8044YS14G/TR	SGM8044YS14 XXXXX	Tape and Reel, 2500
	TSSOP-14	-40°C to +85°C	SGM8044YTS14G/TR	SGM8044 YTS14 XXXXX	Tape and Reel, 3000
	TQFN-3x3-16L	-40°C to +85°C	SGM8044YTQ16G/TR	8044TQ XXXXX	Tape and Reel, 4000

NOTE: XXXXX = Date Code and Vendor Code.

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

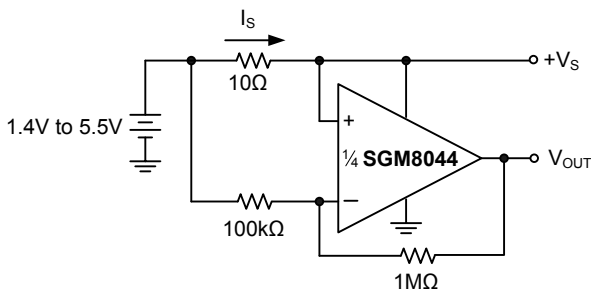
ABSOLUTE MAXIMUM RATINGS

Supply Voltage.....6V
 Analog Inputs (V_{IN+}, V_{IN-})..... (-V_S) - 0.1V to (+V_S) + 0.1V
 Differential Input Voltage..... |(-V_S) - (+V_S)|
 Storage Temperature Range.....-65°C to +150°C
 Junction Temperature+150°C
 Lead Temperature (Soldering, 10s)+260°C
 ESD Susceptibility
 HBM.....4000V
 MM.....400V

RECOMMENDED OPERATING CONDITIONS

Supply Voltage Range1.4V to 5.5V
 Operating Temperature Range-40°C to +85°C

TYPICAL APPLICATION



$$I_s = \frac{+V_S - V_{OUT}}{(10V/V) \cdot (10\Omega)}$$

High Side Battery Current Sensor

OVERSTRESS CAUTION

Stresses beyond those listed may cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational section of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time.

ELECTRICAL CHARACTERISTICS

$+V_S = +1.4V$ to $+5.0V$, $-V_S = GND$, $T_A = +25^\circ C$, $V_{CM} = +V_S/2$, $V_{OUT} \approx +V_S/2$ and $R_L = 1M\Omega$ to $+V_S/2$ ⁽¹⁾, unless otherwise noted.

PARAMETER		CONDITIONS	MIN	TYP	MAX	UNITS	
DC ELECTRICAL CHARACTERISTICS							
Input Offset Voltage (V_{OS})		$V_{CM} = +V_S/2$		0.7	2.5	mV	
Input Offset Voltage Drift ($\Delta V_{OS}/\Delta T$)		$V_{CM} = +V_S/2$, $-40^\circ C \leq T_A \leq +85^\circ C$		2.5		$\mu V/^\circ C$	
Power Supply Rejection Ratio (PSRR)		$+V_S = 1.4V$ to $5.5V$	74	80		dB	
Input Common Mode Voltage Range (V_{CMR})			$-V_S - 0.1$		$+V_S + 0.1$	V	
Common Mode Rejection Ratio (CMRR)		$+V_S = 5.0V$, $V_{CM} = -0.1V$ to $5.1V$	65	83		dB	
		$+V_S = 5.0V$, $V_{CM} = 2.5V$ to $5.1V$	65	82			
		$+V_S = 5.0V$, $V_{CM} = -0.1V$ to $2.5V$	70	80			
Large Signal Voltage Gain (A_{VO})		$+V_S = 1.4V$, $R_L = 50k\Omega$, $V_{OUT} = +V_S - 0.1V$	72	77		dB	
		$+V_S = 2.5V$, $R_L = 50k\Omega$, $V_{OUT} = +V_S - 0.1V$		87			
		$+V_S = 5.0V$, $R_L = 50k\Omega$, $V_{OUT} = +V_S - 0.1V$	82	93			
Input Bias Current (I_B)				1		pA	
Input Offset Current (I_{OS})				1		pA	
Maximum Output Voltage Swing		V_{OH}	$+V_S = 1.4V$, $R_L = 50k\Omega$	1.390	1.394		V
			$+V_S = 2.5V$, $R_L = 50k\Omega$		2.497		
			$+V_S = 5.0V$, $R_L = 50k\Omega$	4.990	4.996		
		V_{OL}	$+V_S = 1.4V$, $R_L = 50k\Omega$		5.4	10	mV
			$+V_S = 2.5V$, $R_L = 50k\Omega$		3.4		
			$+V_S = 5.0V$, $R_L = 50k\Omega$		3.7	10	
Short Circuit Current (I_{SC})		$+V_S = 2.5V$		5.2		mA	
		$+V_S = 5.0V$	22	23			
Supply Voltage			1.4		5.5	V	
Quiescent Current/Amplifier (I_Q)		$+V_S = 1.4V$		550		nA	
		$+V_S = 2.5V$		610			
		$+V_S = 5.0V$		670	1500		

ELECTRICAL CHARACTERISTICS (continued)

+V_S = +1.4V to +5.0V, -V_S = GND, T_A = +25°C, V_{CM} = +V_S/2, V_{OUT} ≈ +V_S/2 and R_L = 1MΩ to +V_S/2, C_L = 60pF⁽¹⁾, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
AC ELECTRICAL CHARACTERISTICS					
Gain-Bandwidth Product (GBP)	+V _S = 1.4V		13.5		kHz
	+V _S = 2.5V		14.5		
	+V _S = 5.0V		15		
Slew Rate (SR)	+V _S = 1.4V, V _{OUT} = 1V Step		3.0		V/ms
	+V _S = 2.5V, V _{OUT} = 1V Step		3.1		
	+V _S = 5.0V, V _{OUT} = 2V Step		3.4		
Phase Margin (PM)	+V _S = 1.4V to 5.5V		60		°
Input Voltage Noise (e _n p-p)	+V _S = 1.4V, f = 0.1Hz to 10Hz		3.5		μV _{P-P}
	+V _S = 2.5V, f = 0.1Hz to 10Hz		3.4		
	+V _S = 5.0V, f = 0.1Hz to 10Hz		3.2		
Input Voltage Noise Density (e _n)	+V _S = 1.4V, f = 1kHz		205		nV/√Hz
	+V _S = 2.5V, f = 1kHz		185		
	+V _S = 5.0V, f = 1kHz		190		

NOTE: 1. Refer to Figure 1 and Figure 2.

TEST CIRCUITS

The test circuits used for the DC and AC tests are shown in Figure 1 and Figure 2. The bypass capacitors are laid out according to the rules discussed in "Supply Bypass".

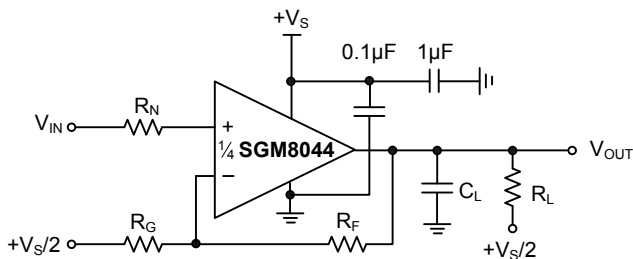


Figure 1. AC and DC Test Circuit for Most Non-Inverting Gain Conditions.

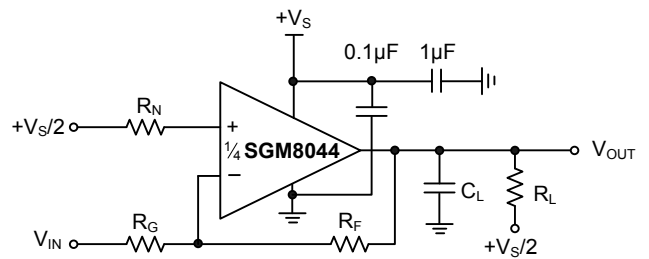
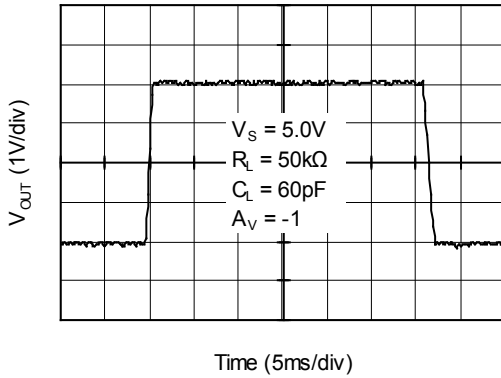


Figure 2. AC and DC Test Circuit for Most Inverting Gain Conditions.

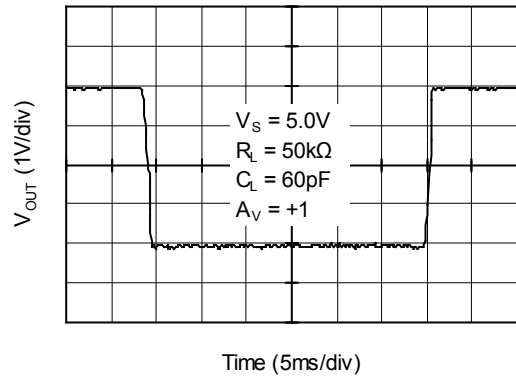
TYPICAL PERFORMANCE CHARACTERISTICS

$T_A = +25^\circ\text{C}$, $+V_S = +1.4\text{V}$ to $+5.0\text{V}$, $-V_S = \text{GND}$, $V_{CM} = +V_S/2$, $V_{OUT} \approx +V_S/2$ and $R_L = 1\text{M}\Omega$ to $+V_S/2$, $C_L = 60\text{pF}$, unless otherwise noted.

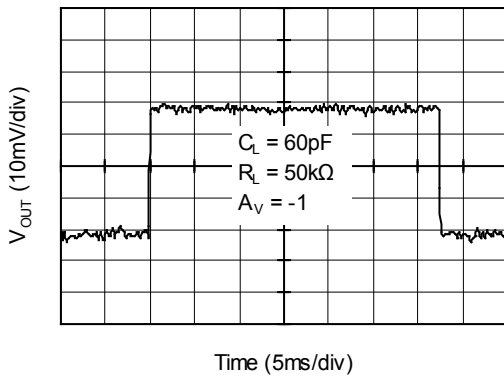
Large Signal Inverting Pulse Response



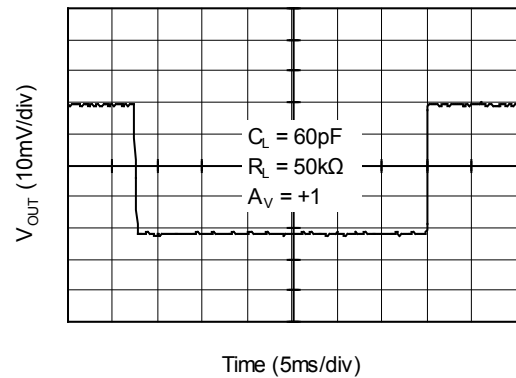
Large Signal Non-Inverting Pulse Response



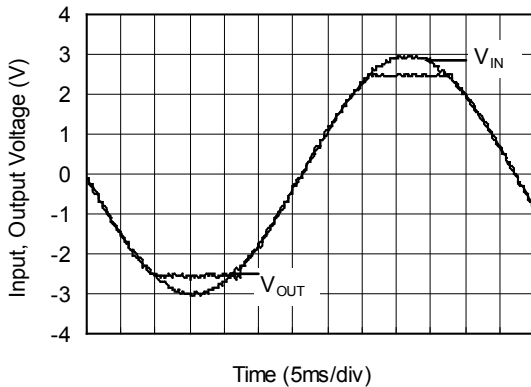
Small Signal Inverting Pulse Response



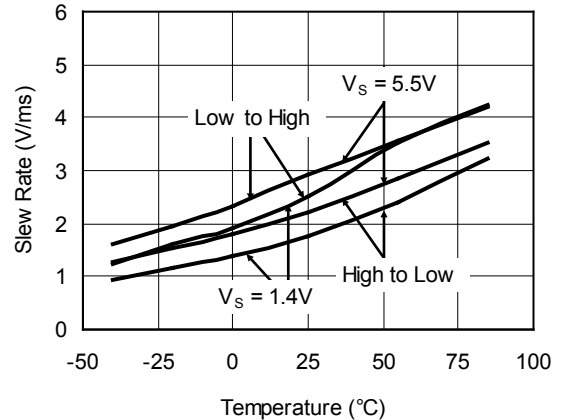
Small Signal Non-Inverting Pulse Response



No Phase Reversal

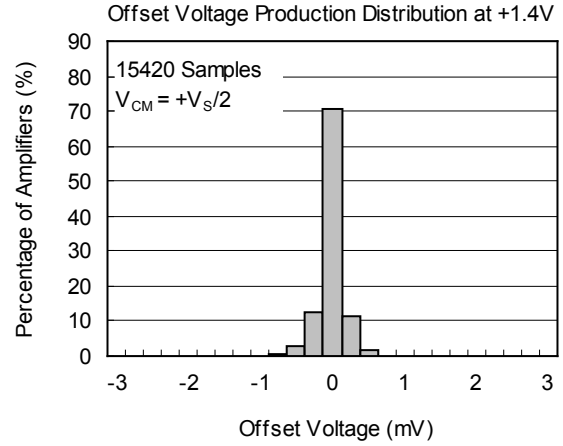
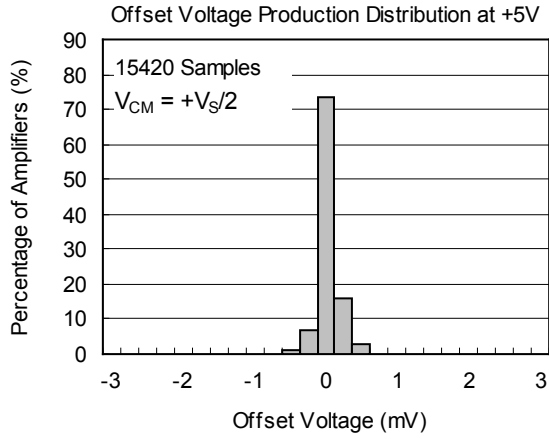


Slew Rate vs. Temperature



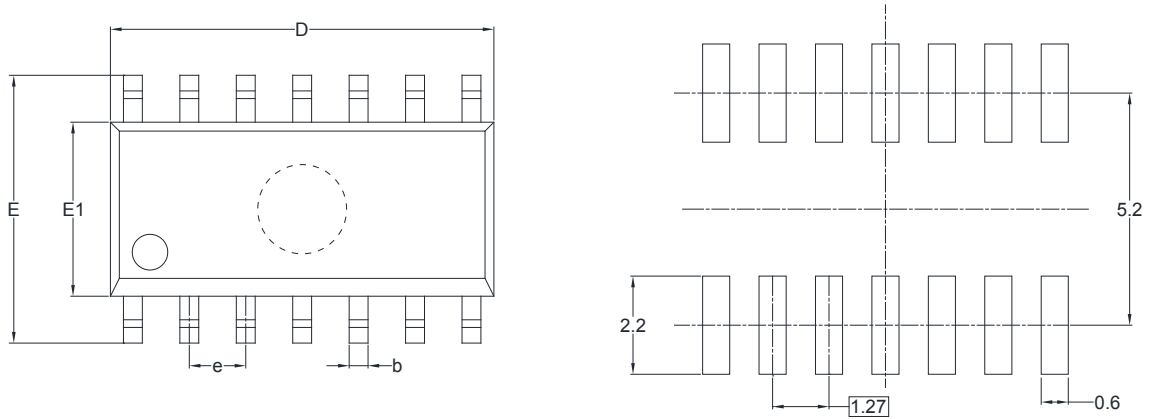
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$T_A = +25^\circ\text{C}$, $+V_S = +1.4\text{V}$ to $+5.0\text{V}$, $-V_S = \text{GND}$, $V_{CM} = +V_S/2$, $V_{OUT} \approx +V_S/2$ and $R_L = 1\text{M}\Omega$ to $+V_S/2$, $C_L = 60\text{pF}$, unless otherwise noted.

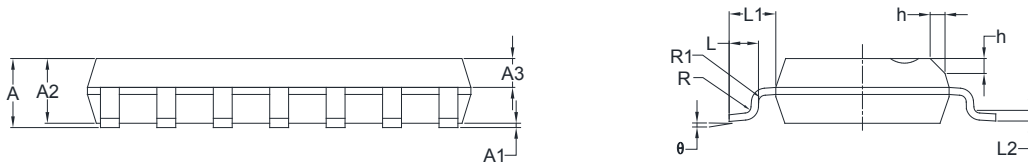


PACKAGE OUTLINE DIMENSIONS

SOIC-14



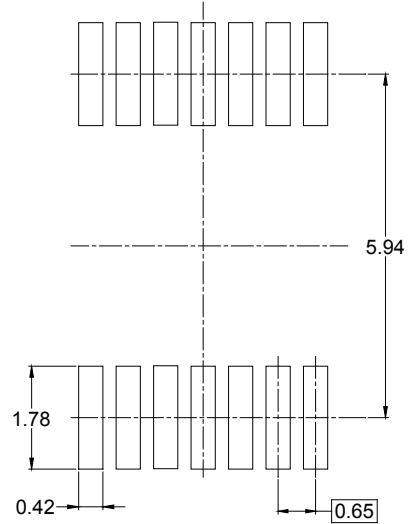
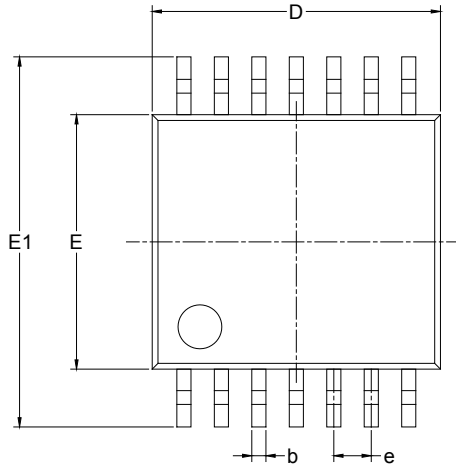
RECOMMENDED LAND PATTERN (Unit: mm)



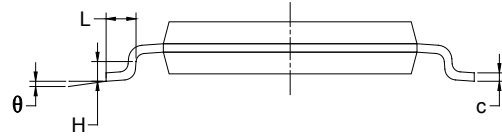
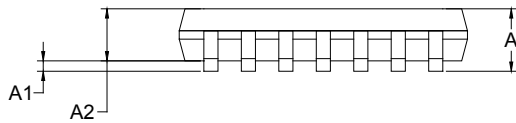
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	1.65	0.049	0.065
A3	0.55	0.75	0.022	0.030
b	0.36	0.49	0.014	0.019
D	8.53	8.73	0.336	0.344
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
L	0.45	0.80	0.018	0.032
L1	1.04 REF		0.040 REF	
L2	0.25 BSC		0.01 BSC	
R	0.07		0.003	
R1	0.07		0.003	
h	0.30	0.50	0.012	0.020
θ	0°	8°	0°	8°

PACKAGE OUTLINE DIMENSIONS

TSSOP-14



RECOMMENDED LAND PATTERN (Unit: mm)

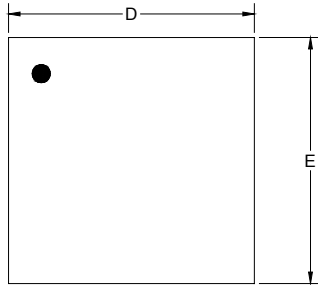


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.860	5.100	0.191	0.201
E	4.300	4.500	0.169	0.177
E1	6.250	6.550	0.246	0.258
e	0.650 BSC		0.026 BSC	
L	0.500	0.700	0.02	0.028
H	0.25 TYP		0.01 TYP	
θ	1°	7°	1°	7°

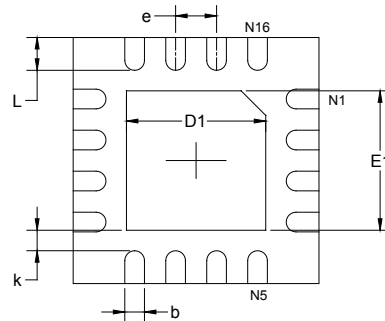
PACKAGE INFORMATION

PACKAGE OUTLINE DIMENSIONS

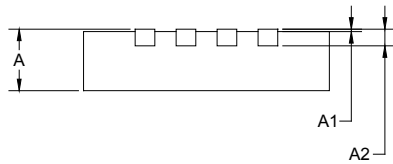
TQFN-3×3-16L



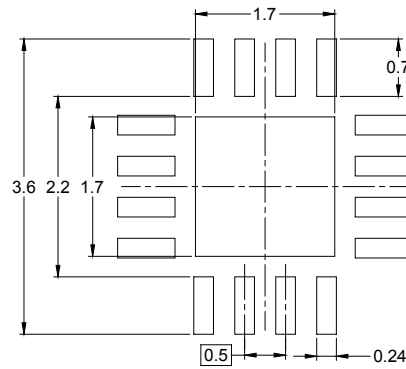
TOP VIEW



BOTTOM VIEW



SIDE VIEW

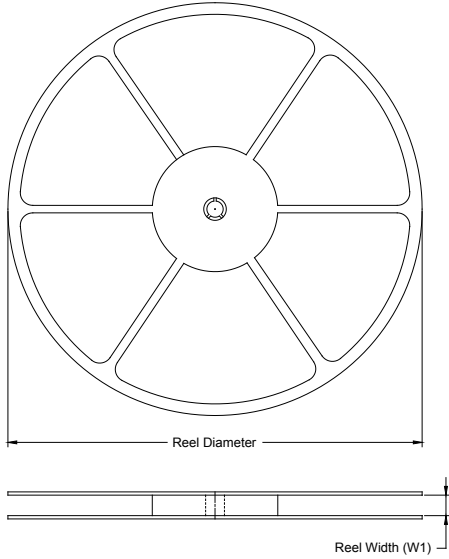


RECOMMENDED LAND PATTERN (Unit: mm)

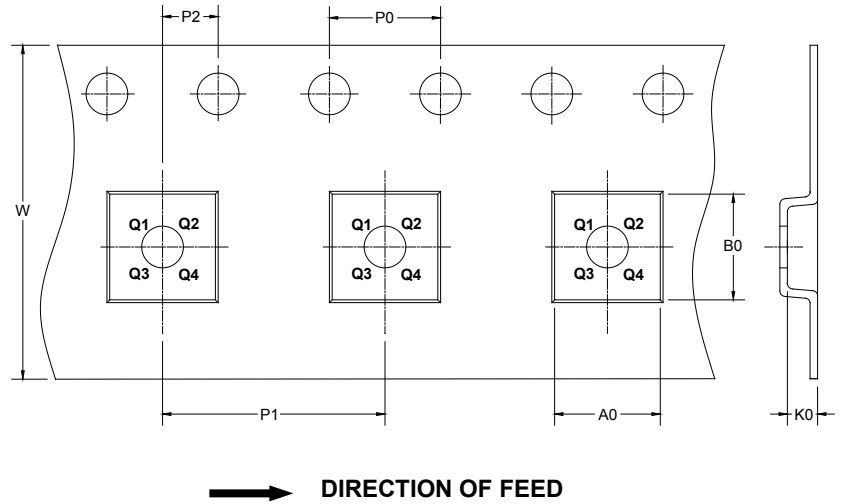
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203 REF		0.008 REF	
D	2.900	3.100	0.114	0.122
D1	1.600	1.800	0.063	0.071
E	2.900	3.100	0.114	0.122
E1	1.600	1.800	0.063	0.071
k	0.200 MIN		0.008 MIN	
b	0.180	0.300	0.007	0.012
e	0.500 TYP		0.020 TYP	
L	0.300	0.500	0.012	0.020

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-14	13"	16.4	6.60	9.30	2.10	4.0	8.0	2.0	16.0	Q1
TSSOP-14	13"	12.4	6.95	5.60	1.20	4.0	8.0	2.0	12.0	Q1
TQFN-3×3-16L	13"	12.4	3.35	3.35	1.13	4.0	8.0	2.0	12.0	Q1

DD0001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

DD0002