

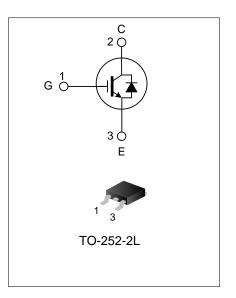
10A, 600V FIELD STOP IGBT

DESCRIPTION

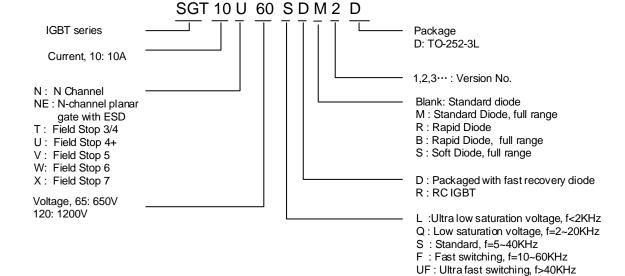
The SGT10U60SDM2D field stop IGBT adopts Silan Field Stop IV+ technology, features low conduction loss and switching loss, is applicable to UPS, SMPS, motor application and PFC fields.

FEATURES

- 10A, 600V, V_{CE(sat)(typ.)}=1.65V@I_C=10A
- Low conduction loss
- Fast switching
- High input impedance



NOMENCLATURE



ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type	
SGT10U60SDM2DTR	TO-252-2L	10U60SD2	Halogen free	Tape & reel	

HANGZHOU SILAN MICROELECTRONICS CO.,LTD



ABSOLUTE MAXIMUM RATINGS (T_C=25°C, UNLESS OTHERWISE NOTED)

Pa	rameter	Symbol	rmbol Ratings		
Collector to Emitter Voltage		V_{CE}	600	V	
Gate to Emitter Voltage		V_{GE}	±20	V	
Collector	T _C =25°C	ı	20	Α	
Current	T _C =100°C	I _C	10	4	
Pulsed Collector Current		I _{CM}	30	Α	
T _C =25°C			20	Α	
Diode current	T _C =100°C	- I _F	10	A	
Pulsed Diode Current		I _{FM}	30	Α	
Power Dissipation (T _C =25°C)		P _D	48	W	
Operating Junction Temperature		TJ	-55∼+150	°C	
Storage Temperature Range		T _{stg}	-55~+150		

THERMAL CHARACTERISTICS

Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Units
Thermal Resistance, Junction to Case (IGBT)	$R_{ heta JC}$				2.6	°C/W
Thermal Resistance, Junction to Case (FRD)	R ₀ JC				3.6	°C/W
Thermal Resistance, Junction to Ambient (IGBT)	$R_{\theta JA}$		1		72	°C/W
Soldering Temperature(SMD)	T_{sold}	Reflow soldering: 10 ± 1 sec, 3times	1		260	°C

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ELECTRICAL CHARACTERISTICS OF IGBT (Tc=25°C, UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Units
Collector to Emitter Breakdown Voltage	BV _{CE}	V _{GE} =0V, I _C =250μA	600			V
C-E Leakage Current	I _{CES}	V _{CE} =600V, V _{GE} =0V			200	μA
G-E Leakage Current	I _{GES}	V _{GE} =20V, V _{CE} =0V			±400	nA
G-E Threshold Voltage	$V_{GE(th)}$	I _C =250µA, V _{CE} =V _{GE}	3.5	5.5	6.5	V
Collector to Emitter		I _C =10A, V _{GE} =15V		1.65	2.2	V
Saturation Voltage	$V_{CE(sat)}$	I _C =10A, V _{GE} =15V, T _C =150°C		1.85		V
Input Capacitance	C _{ies}	V _{CE} =30V		853		
Output Capacitance	C _{oes}	V _{GE} =0V		22		pF
Reverse Transfer Capacitance	C _{res}	f=1MHz		10		
Turn-On Delay Time	T _{d(on)}			10		ns
Rise Time	Tr	V _{CE} =400V		18		
Turn-Off Delay Time	$T_{d(off)}$	$I_{C}=10A$ $R_{g}=10\Omega$ $V_{GE}=15V$ Inductive load $T_{C}=25^{\circ}C$		50		
Fall Time	T_f			100		
Turn-On Switching Loss	E _{on}			0.44		mJ
Turn-Off Switching Loss	E _{off}			0.13		
Total Switching Loss	E _{st}	1 _C =25 ⁻ C		0.57		
Turn-On Delay Time	T _{d(on)}			8.0		ns
Rise Time	Tr	V _{CE} =400V		8.0		
Turn-Off Delay Time	$T_{d(off)}$	$I_{C}=5.0A$ $R_{g}=10\Omega$ $V_{GE}=15V$ Inductive load $T_{C}=25^{\circ}C$		44		
Fall Time	T _f			154		
Turn-On Switching Loss	Eon			0.20		mJ
Turn-Off Switching Loss	E _{off}			0.05		
Total Switching Loss	E _{st}	1C=20.C		0.25		
Total Gate Charge	Qg			49		nC
Gate to Emitter Charge	Q _{ge}	V _{CE} = 400V, I _C =10A, V _{GE} =15V		9.0		
Gate to Collector Charge	Q _{gc}			23		

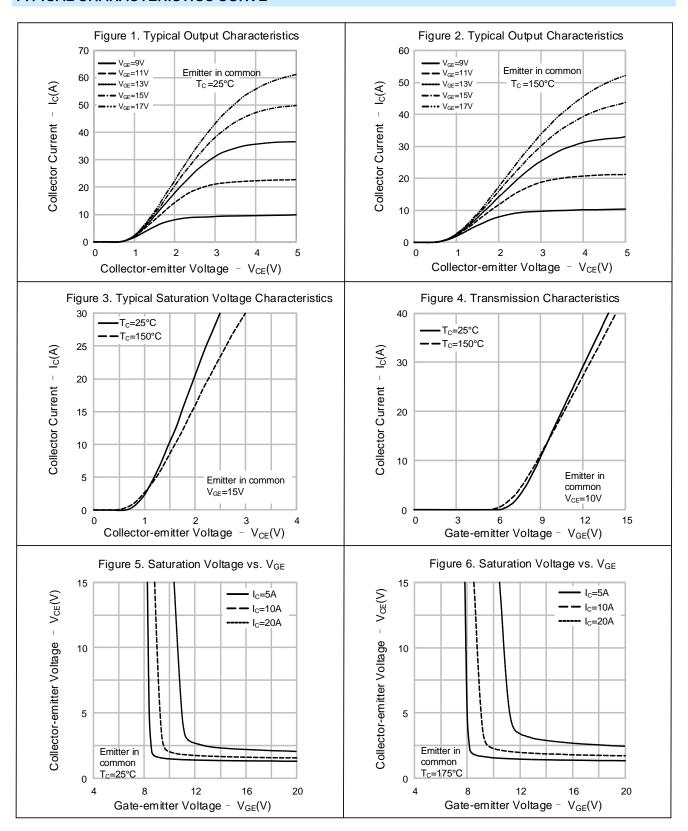
ELECTRICAL CHARACTERISTICS OF FRD (T_C=25°C, UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Units
Diode Forward Voltage	V_{FM}	I _F =10A, T _C =25°C		2.0	2.5	V
		I _F =10A, T _C =150°C		1.7		
Diode Reverse Recovery Time	T _{rr}			22		ns
Diode Reverse Recovery	0	I _{ES} =10A, dI _{ES} /dt=200A/μs		42		nC
Charge	Q_{rr}					

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TYPICAL CHARACTERISTICS CURVE

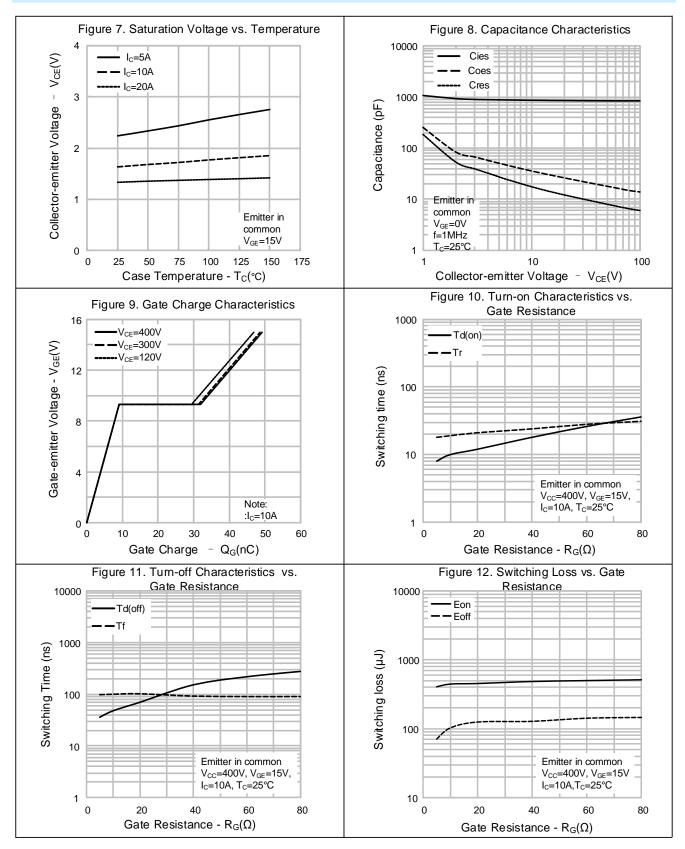


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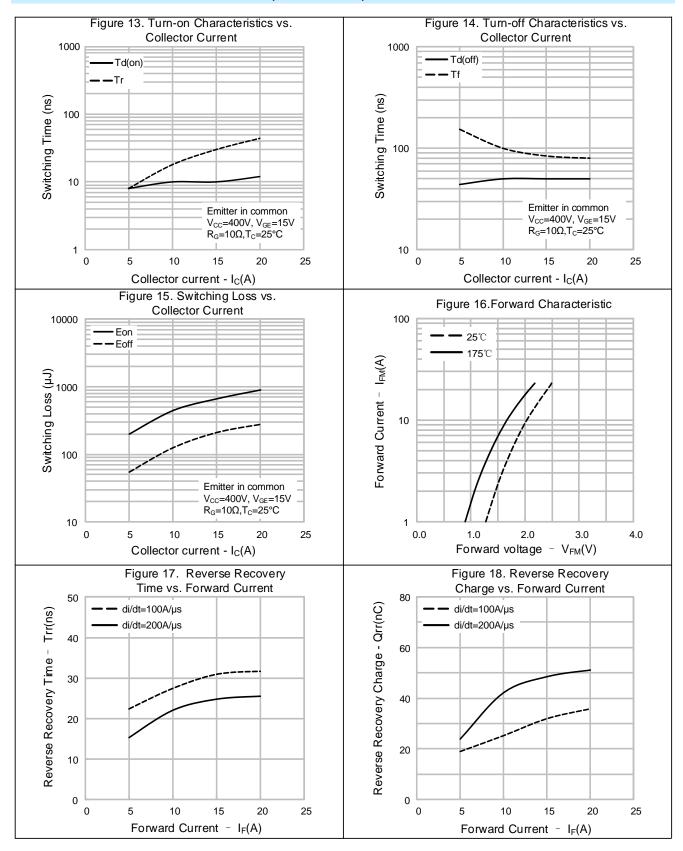
TYPICAL CHARACTERISTICS CURVE (CONTINUED)



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TYPICAL CHARACTERISTICS CURVE (CONTINUED)

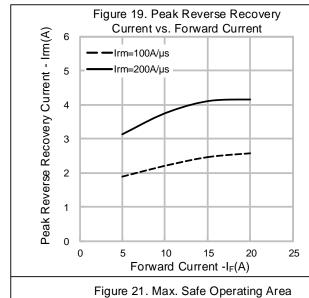


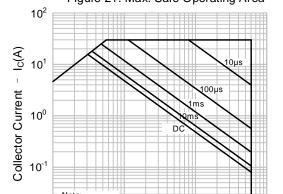
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TYPICAL CHARACTERISTICS CURVE (CONTINUED)





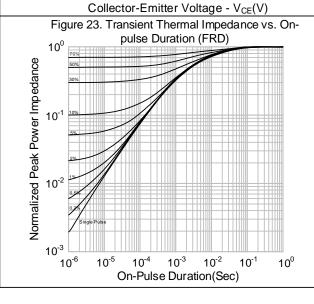
10¹

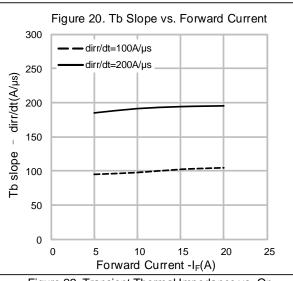
10²

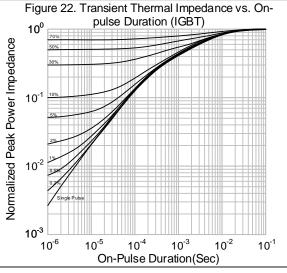
Tc=25°C

10⁻²

10°



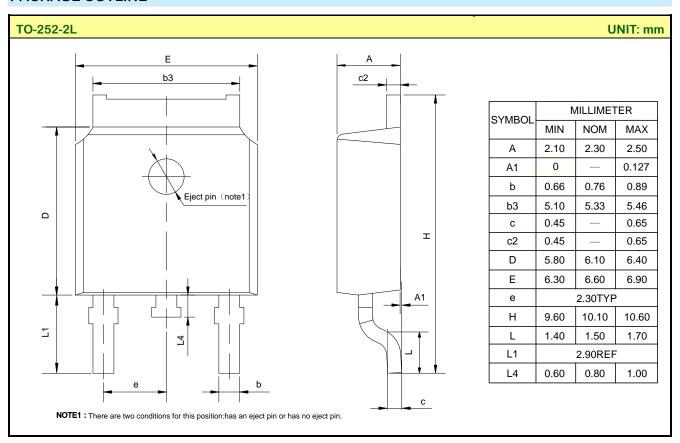




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PACKAGE OUTLINE





MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

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Important notice:

- Silan reserves the right to make changes of this instruction without notice.
- 2. Customers should obtain the latest relevant information when purchasing and should verify whether such information is latest and complete. Please read this instruction and application manual and related materials carefully before using products, including the circuit operation precautions, etc.
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First release

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