

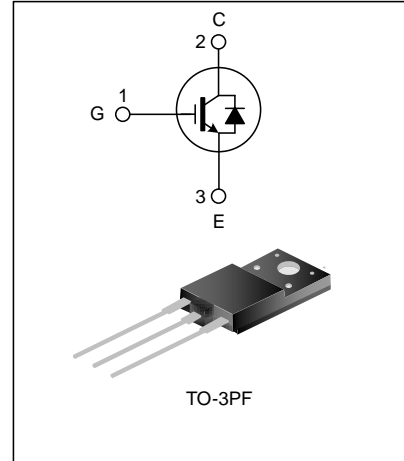
## 40A, 600V FIELD STOP IGBT

### DESCRIPTION

The SGTP40V60SD2PF field stop IGBT adopts Silan Field Stop V technology, features low conduction loss and switching loss. This device is applicable to photovoltaic, UPS, SMPS, and PFC fields.

### FEATURES

- ◆ 40A, 600V,  $V_{CE(sat)(typ.)}=1.35V@I_C=40A$
- ◆ Low conduction loss
- ◆ Ultra-fast switching
- ◆ High input impedance
- ◆  $T_{Jmax}=175^{\circ}C$



### NOMENCLATURE

<b>SGT P 40 V 60 S D 2 PF</b>	
IGBT series Technical grade Current, 40: 40A N : N Channel NE : N-channel planar gate with ESD T : Field Stop 3/4 U : Field Stop 4+ V : Field Stop 5 W: Field Stop 6 X : Field Stop 7 Voltage, 65: 650V, 120: 1200V	Package PF : TO-3PF 1,2,3... : Version No. Blank: Standard diode M : Standard Diode, full range R : Rapid Diode B : Rapid Diode, full range S : Soft Diode, full range D : Packaged with fast recovery diode R : RC IGBT L : Ultra low switching, recommended frequency ~2kHz Q : Low switching, recommended frequency 2~20kHz S : Standard frequency, recommended frequency 5~40kHz F : Fast switching, recommended frequency 10~60kHz UF : Ultra fast switching, recommended frequency 40kHz~

### ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SGTP40V60SD2PF	TO-3PF	40V60SD2	Halogen free	Tube

**ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED,  $T_C=25^\circ\text{C}$ )**

Characteristics	Symbol	Ratings	Unit
Collector to Emitter Voltage	$V_{CE}$	600	V
Gate to Emitter Voltage	$V_{GE}$	$\pm 20$	V
Transient Gate to Emitter Voltage ( $t_p \leq 10\mu\text{s}$ , $D < 0.010$ )	$V_{GE}$	$\pm 30$	V
Collector Current	$I_C$	$T_C=25^\circ\text{C}$	80
		$T_C=100^\circ\text{C}$	40
Pulsed Collector Current	$I_{CM}$	160	A
Diode Current	$I_F$	$T_C=25^\circ\text{C}$	10
		$T_C=100^\circ\text{C}$	5
Diode Pulsed Current	$I_{FM}$	15	A
Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	60	W
Operating Junction Temperature	$T_J$	$-40 \sim +175$	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	$-55 \sim +150$	$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction to Case (IGBT)	$R_{\theta JC}$	--	--	--	2.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (FRD)	$R_{\theta JC}$	--	--	--	5.3	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient (IGBT)	$R_{\theta JA}$	--	--	--	40	$^\circ\text{C/W}$
Soldering temperature (in line)	$T_{sold}$	$15^{+2}_{-0}$ sec, 1time	--	--	260	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED,  $T_C=25^\circ\text{C}$ )**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Collector to Emitter Breakdown Voltage	$BV_{CE}$	$V_{GE}=0V, I_C=250\mu A$	600	--	--	V
C-E Leakage Current	$I_{CES}$	$V_{CE}=600V, V_{GE}=0V$	--	--	100	$\mu A$
G-E Leakage Current	$I_{GES}$	$V_{GE}=20V, V_{CE}=0V$	--	--	$\pm 100$	nA
G-E Threshold Voltage	$V_{GE(th)}$	$I_C=250\mu A, V_{CE}=V_{GE}$	3.2	4.5	5.8	V
Collector to Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=40A, V_{GE}=15V, T_C=25^\circ\text{C}$	--	1.35	1.7	V
		$I_C=40A, V_{GE}=15V, T_C=175^\circ\text{C}$	--	1.55	--	V
Input Capacitance	$C_{ies}$	$V_{CE}=30V$	--	3018	--	pF
Output Capacitance	$C_{oes}$	$V_{GE}=0V$	--	53	--	
Reverse Transfer Capacitance	$C_{res}$	$f=1\text{MHz}$	--	10	--	
Turn-On Delay Time	$T_{d(on)}$	$V_{CE}=400V$ $I_C=40A$ $R_g=10\Omega$ $V_{GE}=15V$ inductive load $T_C=25^\circ\text{C}$	--	39	--	ns
Rise Time	$T_r$		--	46	--	
Turn-Off Delay Time	$T_{d(off)}$		--	150	--	
Fall Time	$T_f$		--	25	--	
Turn-On Switching Loss	$E_{on}$		$T_C=25^\circ\text{C}$	--	0.88	--
Turn-Off Switching Loss	$E_{off}$	--		0.62	--	
Total Switching Loss	$E_{st}$	--		1.50	--	
Turn-On Delay Time	$T_{d(on)}$	$V_{CE}=400V$ $I_C=20A$ $R_g=10\Omega$ $V_{GE}=15V$ inductive load $T_C=25^\circ\text{C}$	--	37	--	ns
Rise Time	$T_r$		--	19	--	
Turn-Off Delay Time	$T_{d(off)}$		--	156	--	
Fall Time	$T_f$		--	33	--	
Turn-On Switching Loss	$E_{on}$		$T_C=25^\circ\text{C}$	--	0.31	--
Turn-Off Switching Loss	$E_{off}$	--		0.32	--	
Total Switching Loss	$E_{st}$	--		0.63	--	
Total Gate Charge	$Q_g$	$V_{CE}=480V, I_C=40A, V_{GE}=15V$	--	110	--	nC
Gate to Emitter Charge	$Q_{ge}$		--	21	--	
Gate to Collector Charge	$Q_{gc}$		--	29	--	

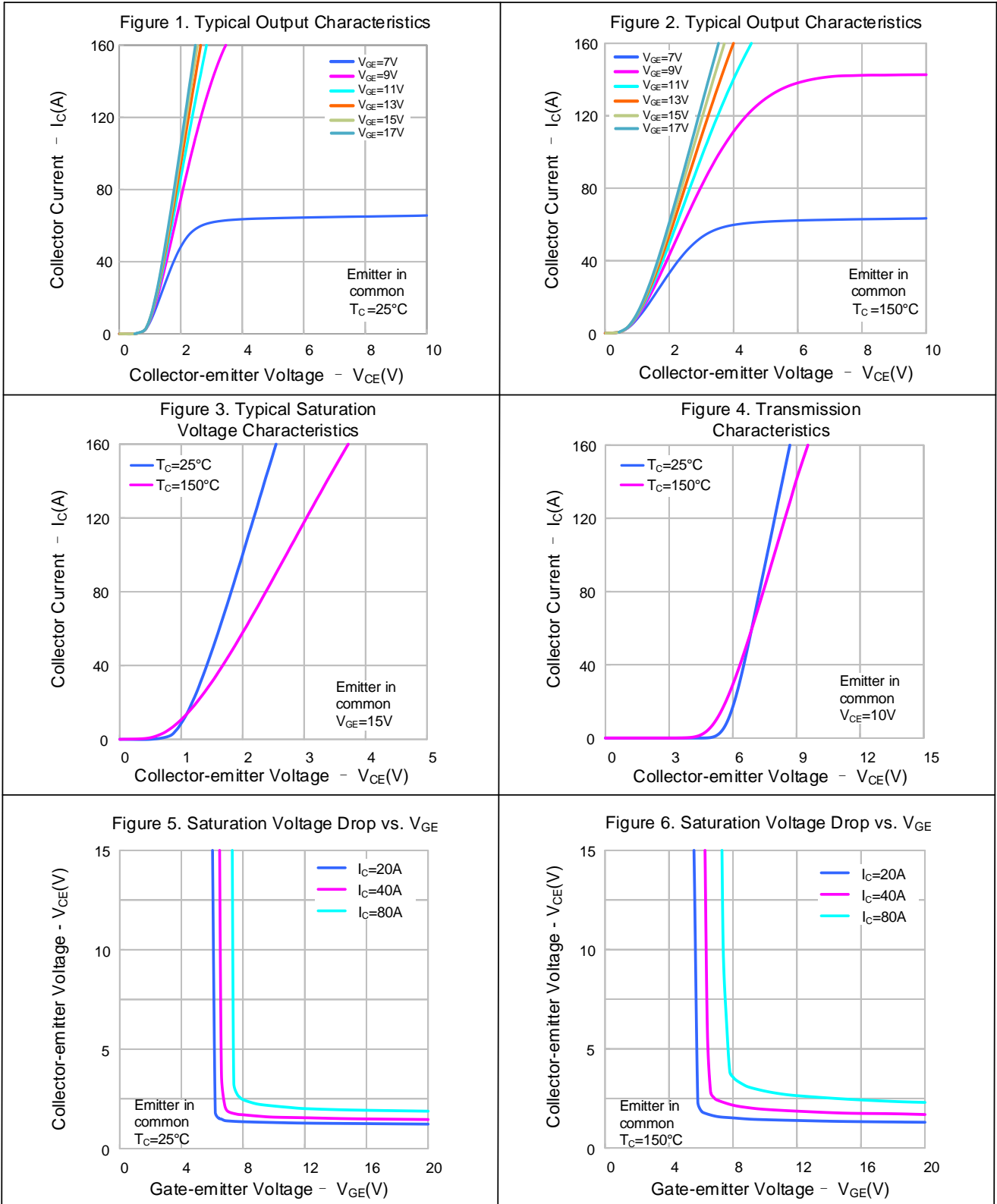
**ELECTRICAL CHARACTERISTICS OF FRD (UNLESS OTHERWISE NOTED, T<sub>C</sub>=25°C)**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Diode Forward Voltage	V <sub>FM</sub>	I <sub>F</sub> =5A, T <sub>C</sub> =25°C	--	1.4	2.0	V
		I <sub>F</sub> =5A, T <sub>C</sub> =175°C	--	1.2	--	
Diode Reverse Recovery Time	T <sub>rr</sub>	I <sub>ES</sub> =5A, dI <sub>ES</sub> /dt=100A/μs, V <sub>R</sub> =50V, T <sub>C</sub> =25°C	--	35	--	ns
Diode Reverse Recovery Charge	Q <sub>rr</sub>		--	42	--	nC
Diode Reverse Recovery Current	I <sub>rm</sub>		--	2.4	--	A

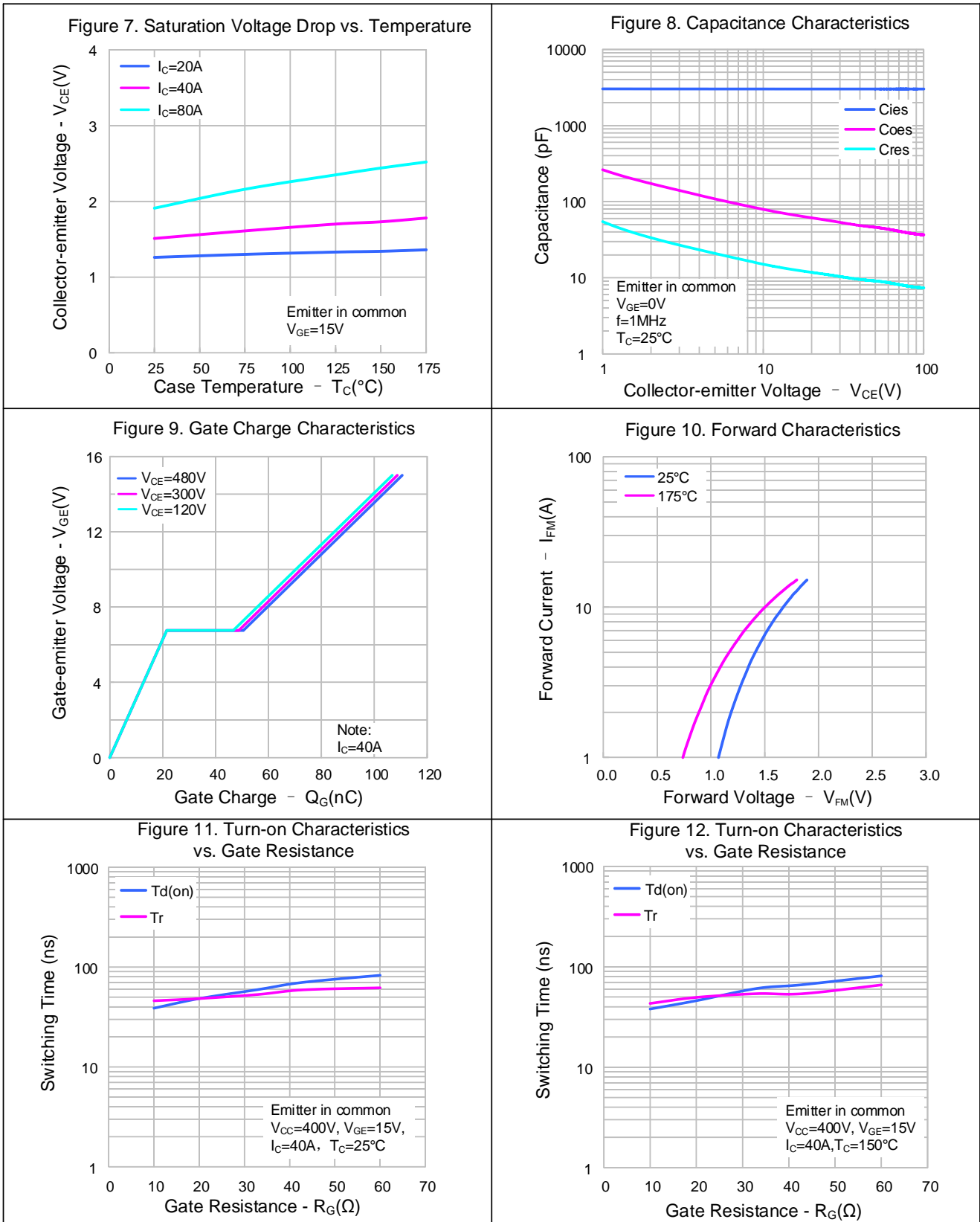
**ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED, T<sub>C</sub>=150°C)**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>CE</sub> =400V I <sub>C</sub> =40A R <sub>g</sub> =10Ω V <sub>GE</sub> =15V inductive load T <sub>C</sub> =150°C	--	38	--	ns
Rise Time	T <sub>r</sub>		--	43	--	
Turn-Off Delay Time	T <sub>d(off)</sub>		--	187	--	
Fall Time	T <sub>f</sub>		--	35	--	
Turn-On Switching Loss	E <sub>on</sub>	inductive load T <sub>C</sub> =150°C	--	0.92	--	mJ
Turn-Off Switching Loss	E <sub>off</sub>		--	0.93	--	
Total Switching Loss	E <sub>st</sub>		--	1.85	--	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>CE</sub> =400V I <sub>C</sub> =20A R <sub>g</sub> =10Ω V <sub>GE</sub> =15V inductive load T <sub>C</sub> =150°C	--	36	--	ns
Rise Time	T <sub>r</sub>		--	18	--	
Turn-Off Delay Time	T <sub>d(off)</sub>		--	201	--	
Fall Time	T <sub>f</sub>		--	43	--	
Turn-On Switching Loss	E <sub>on</sub>	inductive load T <sub>C</sub> =150°C	--	0.33	--	mJ
Turn-Off Switching Loss	E <sub>off</sub>		--	0.43	--	
Total Switching Loss	E <sub>st</sub>		--	0.76	--	

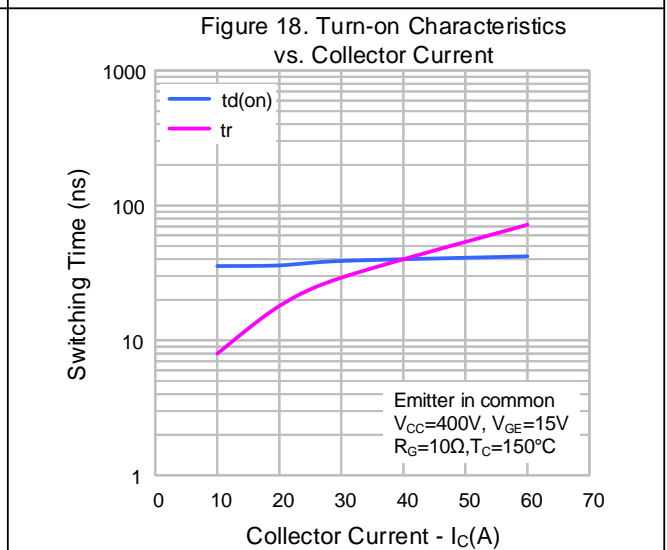
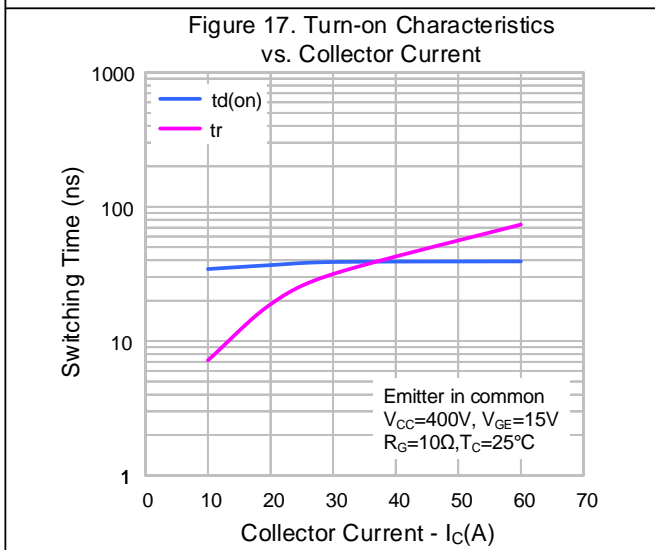
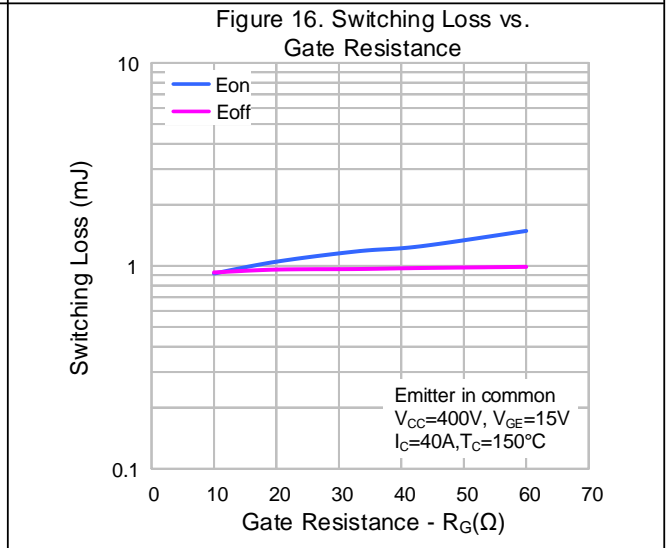
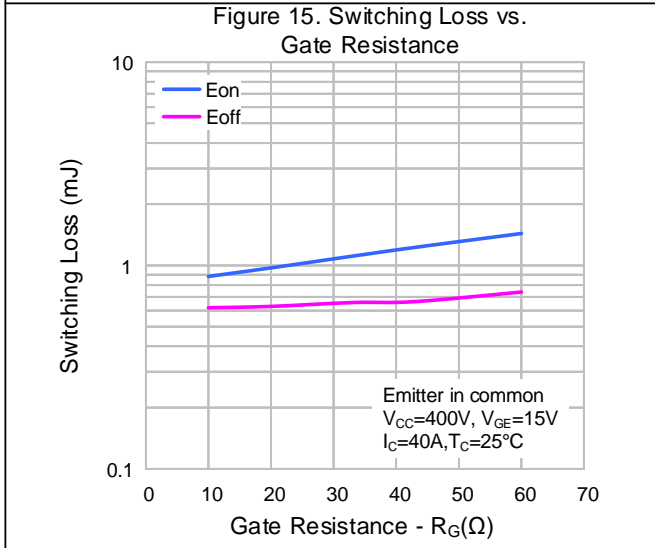
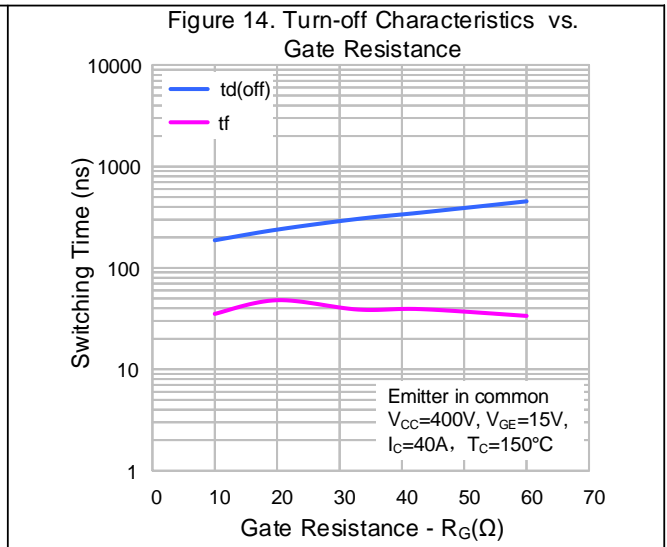
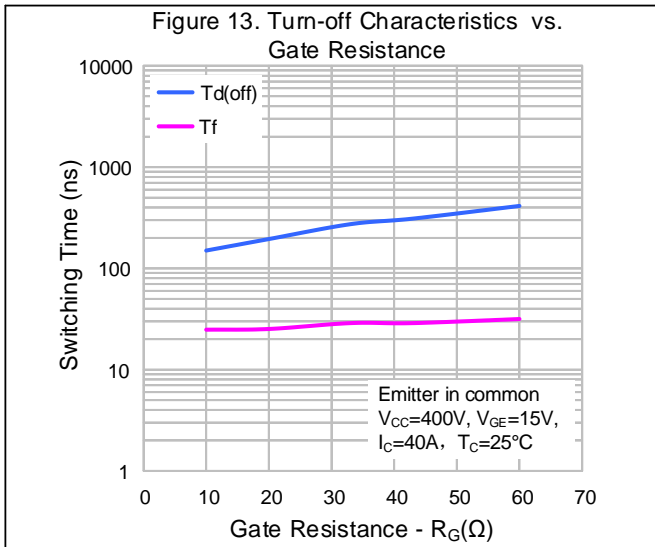
**TYPICAL CHARACTERISTICS**



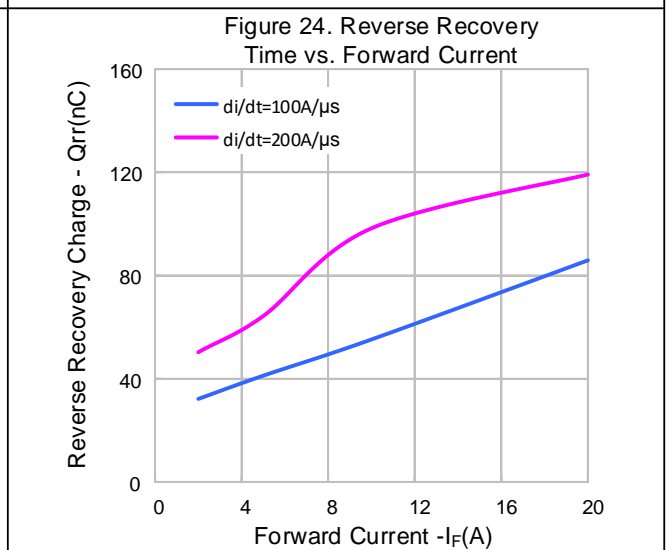
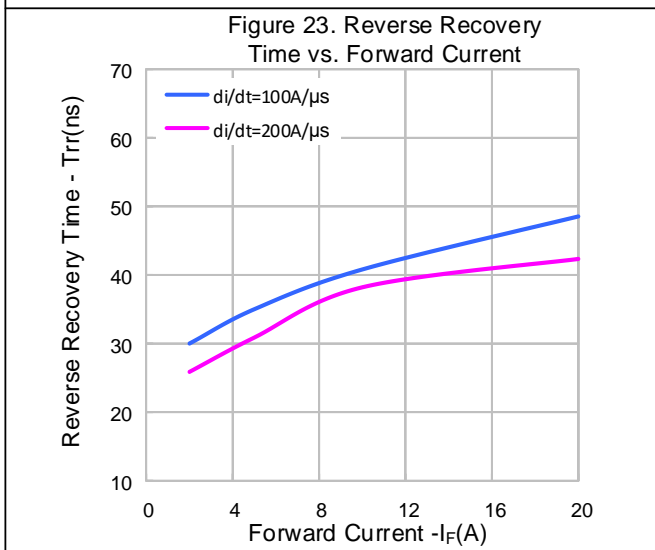
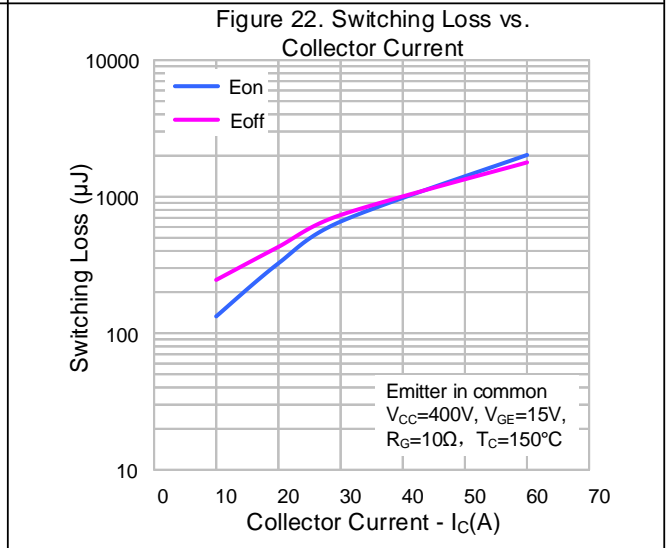
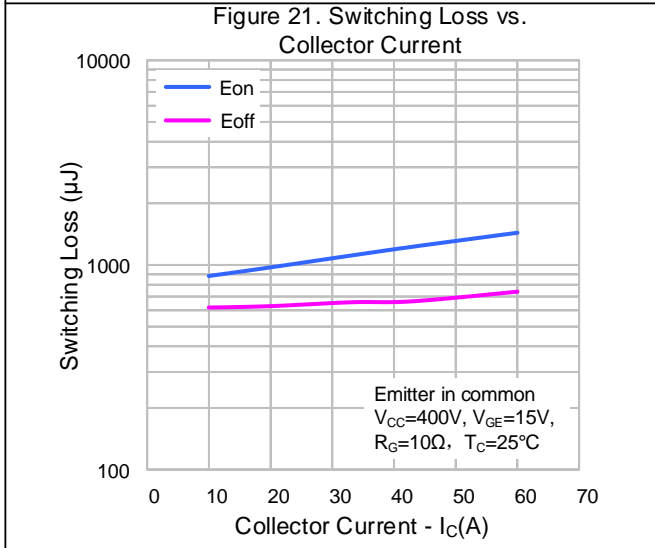
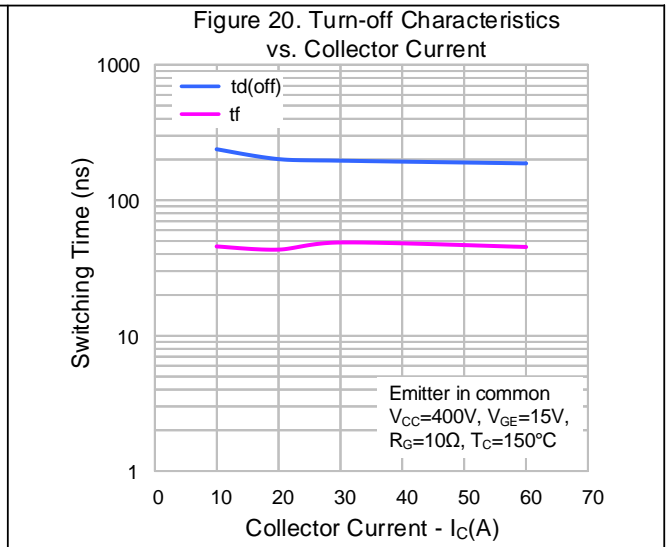
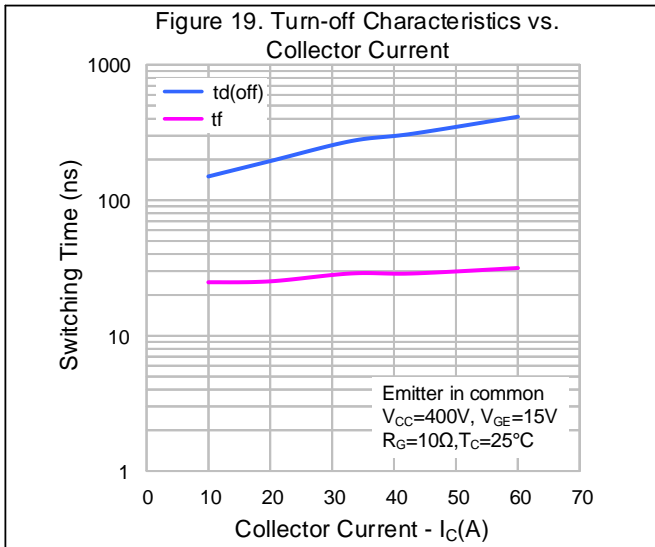
**TYPICAL CHARACTERISTICS (CONTINUED)**



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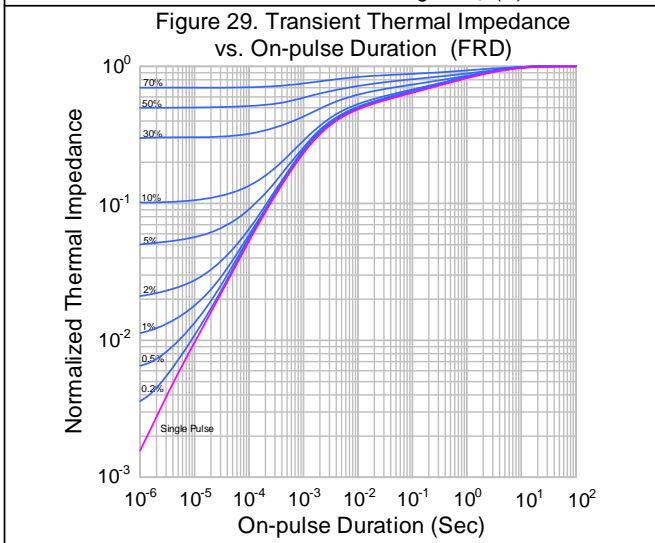
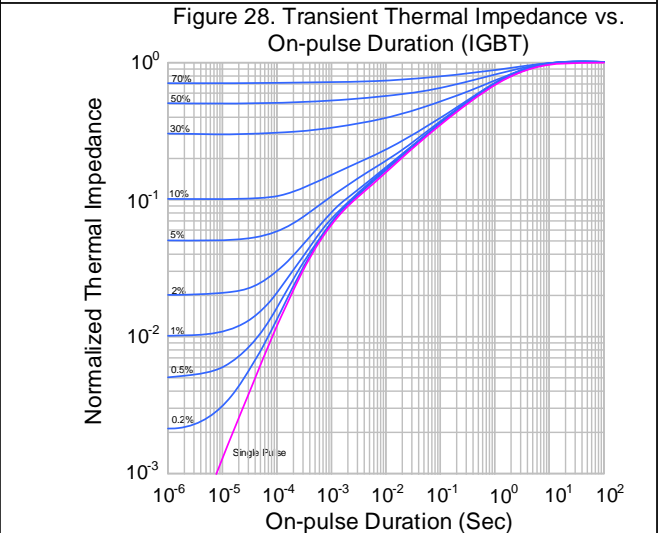
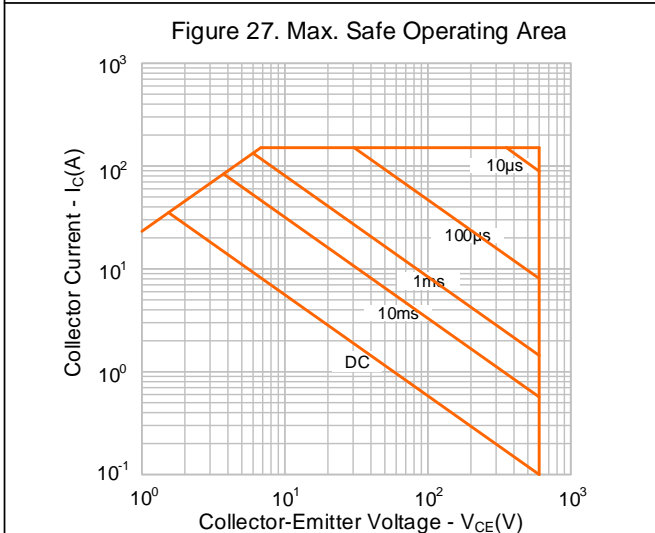
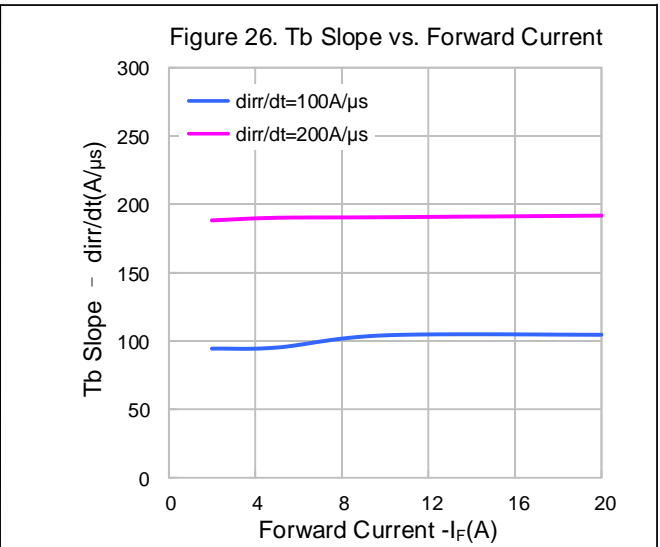
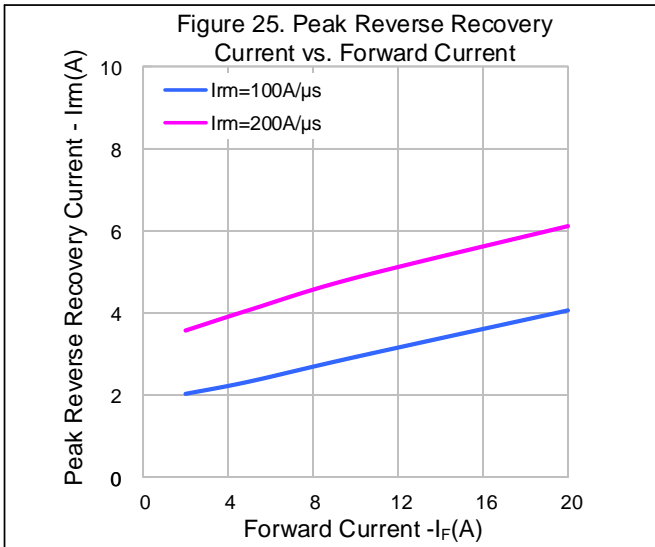


**TYPICAL CHARACTERISTICS (CONTINUED)**

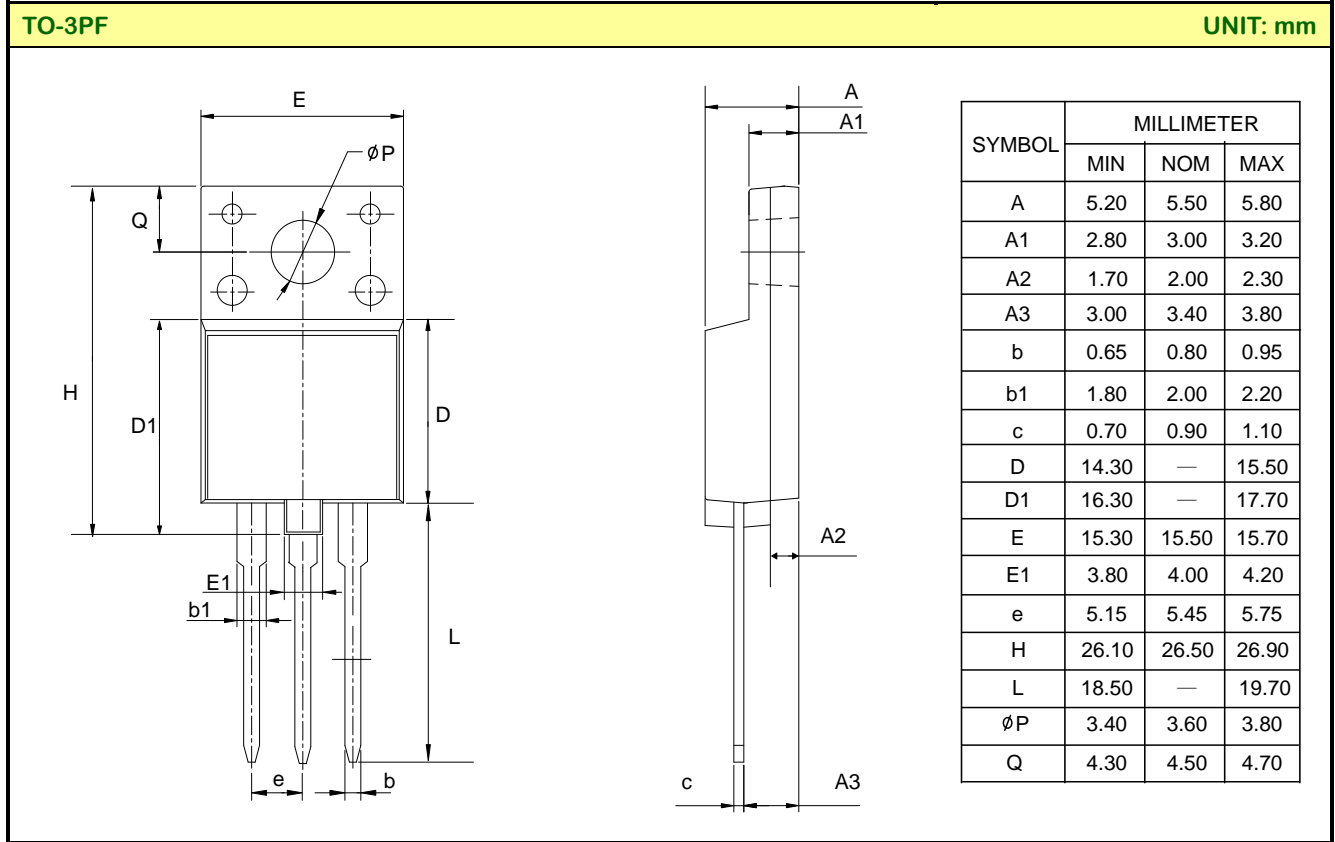




**TYPICAL CHARACTERISTICS (CONTINUED)**



**PACKAGE OUTLINE**



**MOS DEVICES OPERATE NOTES:**

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

**Important notice :**

1. Silan reserves the right to make changes of this instruction without notice.
2. Customers should obtain the latest relevant information when purchasing and should verify whether such information is latest and complete. Please read this instruction and application manual and related materials carefully before using products, including the circuit operation precautions, etc.
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8. Product promotion is endless, our company will wholeheartedly provide customers with better products!
9. Website: <http://www.silan.com.cn>

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Rev.: 1.0

Revision History:

1. First release
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