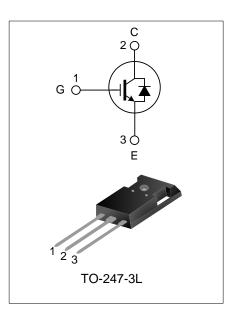
## 50A, 650V FIELD STOP IGBT

#### **DESCRIPTION**

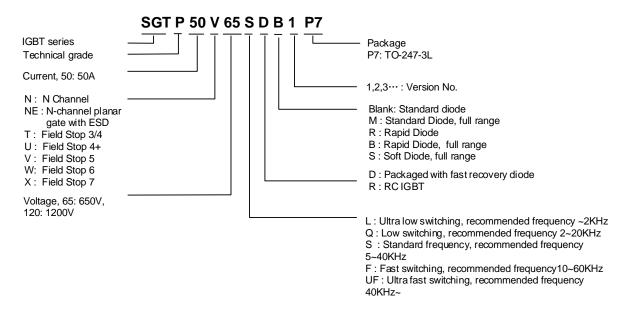
The SGTP50V65SDB1P7 field stop IGBT adopts Silan Field Stop V technology, features low conduction loss and switching loss. This device is applicable to photovoltaic, UPS, SMPS, and PFC fields.

### **FEATURES**

- 50A, 650V, V<sub>CE(sat)(typ.)</sub>=1.45V@I<sub>C</sub>=50A
- Low conduction loss
- Ultra-fast switching
- High input impedance
- T<sub>Jmax</sub>=175°C



#### **NOMENCLATURE**



### **ORDERING INFORMATION**

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SGTP50V65SDB1P7	TO-247-3L	P50V65SDB1	Halogen free	Tube

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### ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, Tc=25°C)

Character	istics	Symbol Ratings		Unit
Collector to Emitter Voltage		V <sub>CE</sub>	650	V
Gate to Emitter Volt	Gate to Emitter Voltage		±20	V
Transient Gate to Emitter Voltage (tp≤10µs, D<0.010)		V <sub>GE</sub>	±30	V
Tc=25°C			100	Δ
Collector Current	T <sub>C</sub> =100°C	lc	50	Α
Pulsed Collector Current		Ісм	150	Α
Tc=25°C		Ι <sub>Ε</sub>	100	Α
Diode Current	T <sub>C</sub> =100°C	IF	50	Α
Pulsed Diode Current		I <sub>FM</sub>	150	Α
Power Dissipation (Tc=25°C)		P <sub>D</sub>	273	W
Operating Junction Temperature		TJ	-40∼+175	°C
Storage Temperature Range		T <sub>stg</sub>	-55∼+150	°C

### THERMAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Thermal Resistance, Junction to	Para				0.55	°C/W
Case (IGBT)	Rелс				0.55	-0///
Thermal Resistance, Junction to	Rелс		-	-	0.65	°C/W
Case (FRD)						
Thermal Resistance, Junction to	RеJA				40	°C/W
Ambient (IGBT)						
Soldering Temperature (in line)	T <sub>sold</sub>	15 <sup>+2</sup> <sub>-0</sub> sec, 1time			260	°C



### ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED, Tc=25°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Collector to Emitter Breakdown Voltage	BV <sub>CE</sub>	V <sub>GE</sub> =0V, I <sub>C</sub> =250μA	650			V
C-E Leakage Current	Ices	Vce=650V, Vge=0V			50	μA
G-E Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> =20V, V <sub>CE</sub> =0V			±100	nA
G-E Threshold Voltage	V <sub>GE(th)</sub>	Ic=250uA, VcE=VgE	3.2	4.0	4.8	V
Collector to Emitter	.,	Ic=50A, V <sub>GE</sub> =15V, T <sub>C</sub> =25°C		1.45	1.9	V
Saturation Voltage	V <sub>CE(sat)</sub>	Ic=50A, V <sub>GE</sub> =15V, T <sub>C</sub> =175°C		1.65		V
Input Capacitance	Cies	V <sub>CE</sub> =30V		3492		
Output Capacitance	Coes	V <sub>GE</sub> =0V		87		pF
Reverse Transfer Capacitance	Cres	f=1MHz		13		
Turn-On Delay Time	T <sub>d(on)</sub>			30		ns
Rise Time	Tr	V <sub>CE</sub> =400V		27		
Turn-Off Delay Time	T <sub>d(off)</sub>	$R_g=10\Omega$		201		
Fall Time	Tf			27		
Turn-On Switching Loss	Eon	V <sub>GE</sub> =15V inductive load		0.48		
Turn-Off Switching Loss	E <sub>off</sub>	Tc=25°C		0.70		mJ
Total Switching Loss	Est	10=25*0		1.18		1
Turn-On Delay Time	T <sub>d(on)</sub>			28		
Rise Time	Tr	V <sub>CE</sub> =400V		16		
Turn-Off Delay Time	T <sub>d(off)</sub>	Ic=25A		217		ns
Fall Time	Tf	$R_g=10\Omega$		28		
Turn-On Switching Loss	Eon	V <sub>GE</sub> =15V inductive load T <sub>C</sub> =25°C		0.19		
Turn-Off Switching Loss	E <sub>off</sub>			0.37		mJ
Total Switching Loss	E <sub>st</sub>			0.56		
Total Gate Charge	Qg			132		
Gate to Emitter Charge	Qge	Vce=520V, Ic=50A, Vge=15V		25		nC
Gate to Collector Charge	Qgc			35		

### ELECTRICAL CHARACTERISTICS OF FRD (UNLESS OTHERWISE NOTED, Tc=25°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>FM</sub>	I <sub>F</sub> =50A, T <sub>C</sub> =25°C		1.5	1.9	- V
Diode Forward Voltage		I <sub>F</sub> =50A, T <sub>C</sub> =175°C		1.36		
Diode Reverse Recovery Time	Trr	1 500 dl /dt 2000///-		132		ns
Diode Reverse Recovery Charge	Qrr	les=50A, dles/dt=200A/µs,		0.35		μC
Diode Reverse Recovery Current	I <sub>rm</sub>	V <sub>R</sub> =50V, T <sub>C</sub> =25°C		6.0		А

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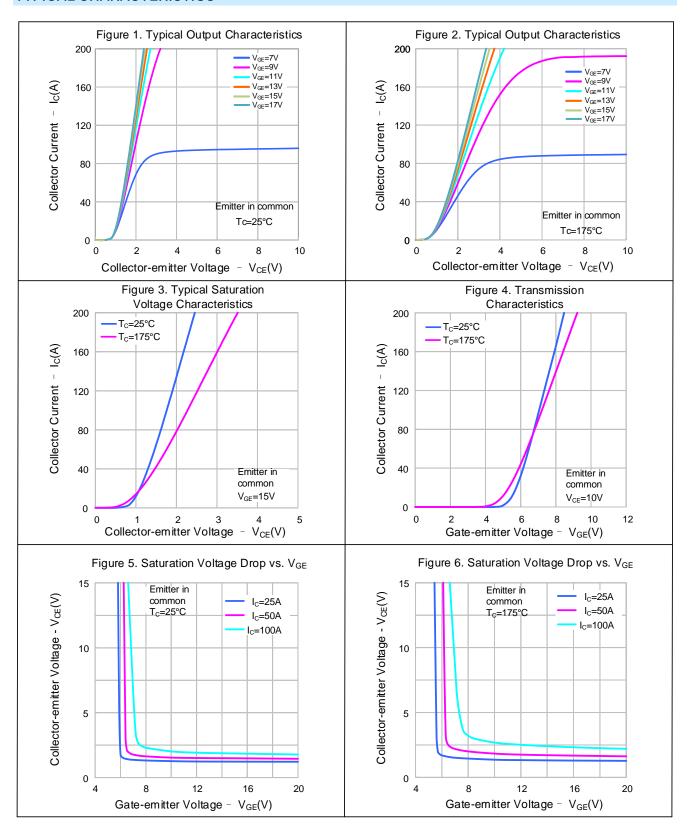
## ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED, Tc=175°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	$T_{d(on)}$			35		
Rise Time	Tr	V <sub>CE</sub> =400V		25		
Turn-Off Delay Time	$T_{d(off)}$	Ic=50A		244		ns
Fall Time	T <sub>f</sub>	$R_g=10\Omega$		34		
Turn-On Switching Loss	Eon	V <sub>GE</sub> =15V		0.61		
Turn-Off Switching Loss	E <sub>off</sub>	inductive load T <sub>C</sub> =175°C		1.20		mJ
Total Switching Loss	Est			1.81		
Turn-On Delay Time	T <sub>d(on)</sub>	.,,		28		
Rise Time	Tr	V <sub>CE</sub> =400V		15		no
Turn-Off Delay Time	$T_{d(off)}$	$R_g=10\Omega$		273		ns
Fall Time	Tf			39		
Turn-On Switching Loss	Eon	V <sub>GE</sub> =15V inductive load		0.22		
Turn-Off Switching Loss	E <sub>off</sub>	Tc=175°C		0.57		mJ
Total Switching Loss	Est	10-175 0		0.79		

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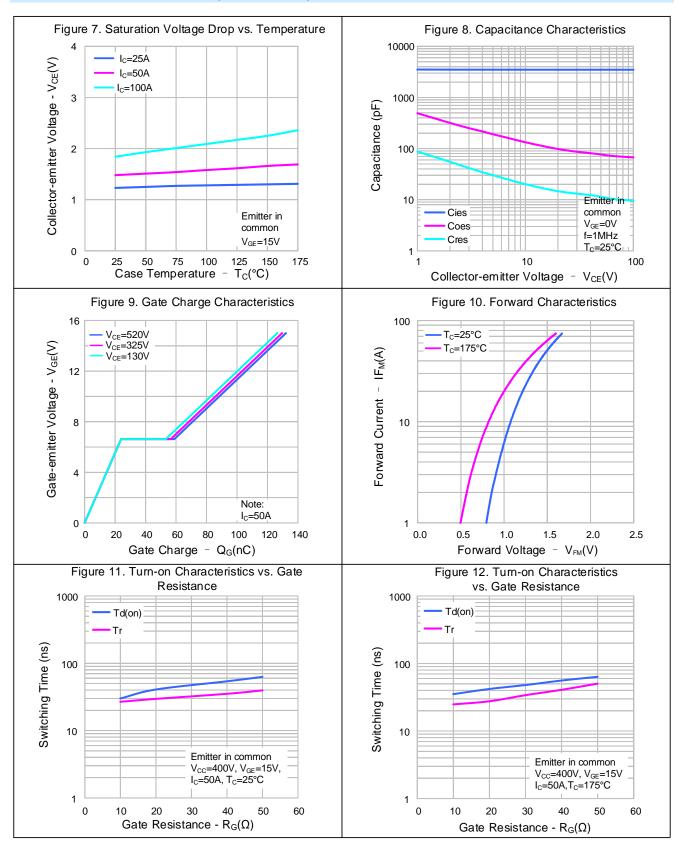
### **TYPICAL CHARACTERISTICS**



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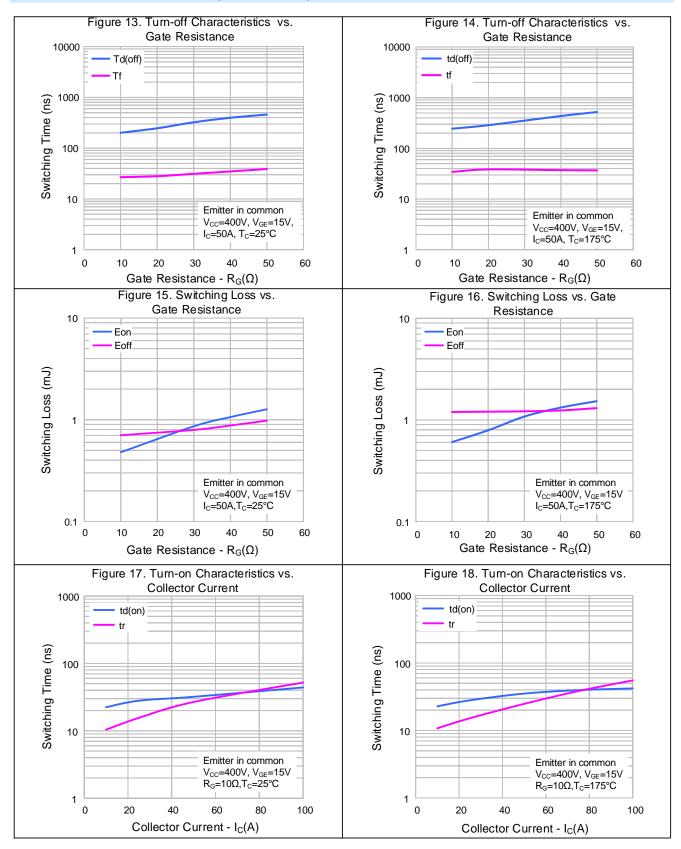
### **TYPICAL CHARACTERISTICS (CONTINUED)**



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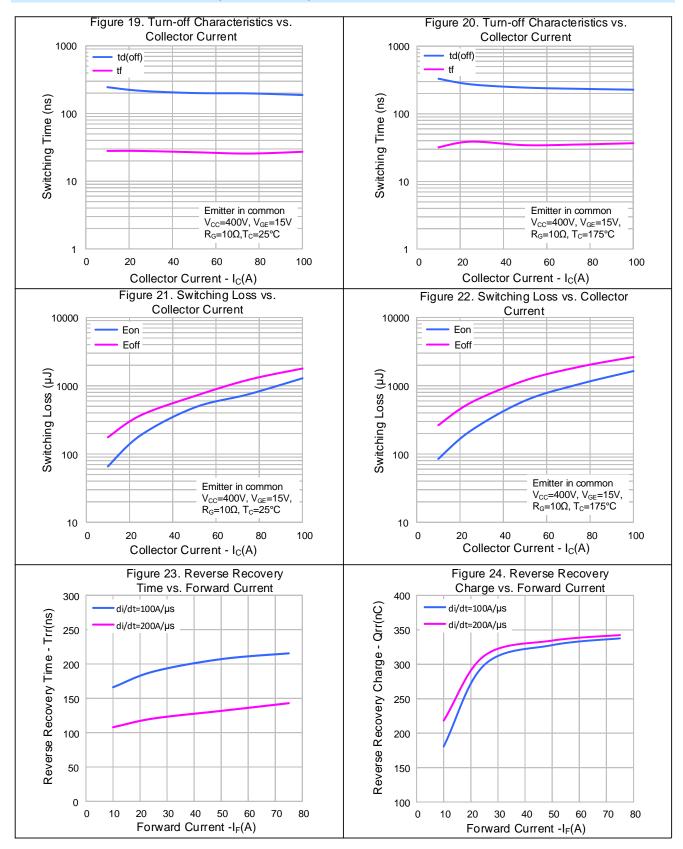
### **TYPICAL CHARACTERISTICS (CONTINUED)**



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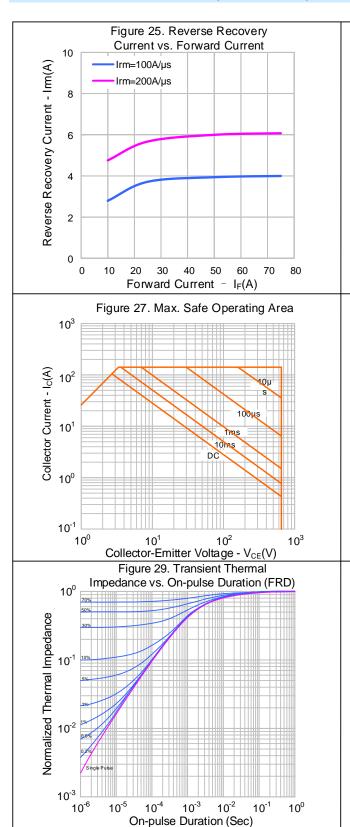
### **TYPICAL CHARACTERISTICS (CONTINUED)**

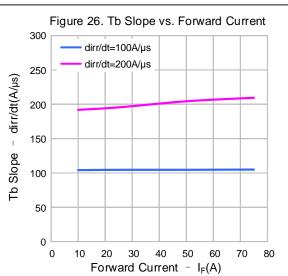


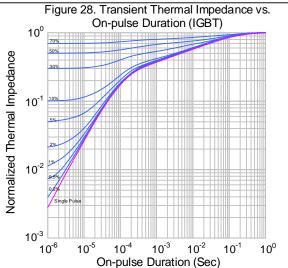
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### **TYPICAL CHARACTERISTICS (CONTINUED)**



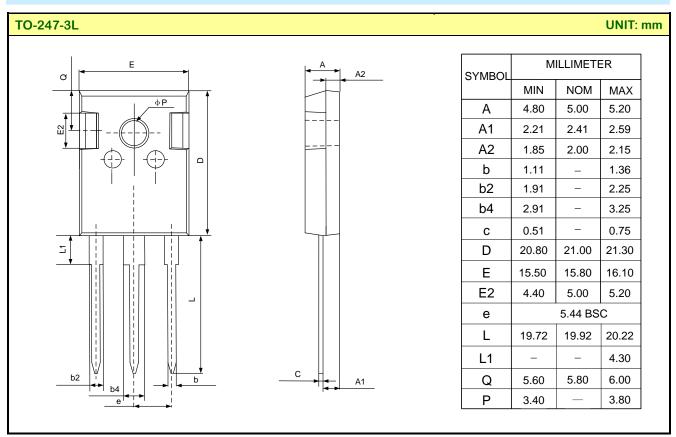




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### **PACKAGE OUTLINE**





### **MOS DEVICES OPERATE NOTES:**

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

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First release

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