

100V 15A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

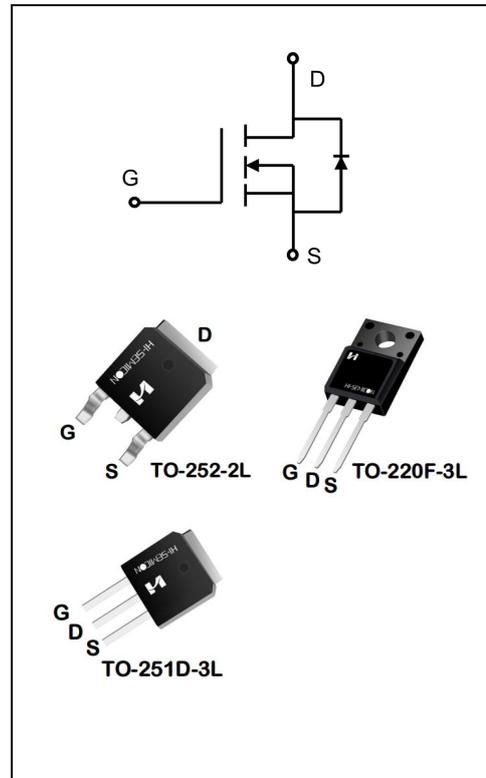
The SGX15N10 uses advanced SGT technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety applications.

Features

- ◆  $V_{DS}=100V, I_D=15A$
- ◆  $R_{DS(on)}$   
TYP:  $55m\Omega @ V_{GS}=10V, I_D=7.5A$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SGU15N10	TO-251D-3L	SGU15N10	Pb Free	Tube
SGD15N10	TO-252-2L	SGD15N10	Pb Free	Reel
SGF15N10	TO-220F-3L	SGF15N10	Pb Free	Tube

## ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C unless otherwise noted)

Characteristics	Symbol	Ratings		Unit
		SGF15N10	SGU/D15N10	
Drain-Source Voltage	V <sub>DS</sub>	100		V
Gate-Source Voltage	V <sub>GS</sub>	±20		V
Drain Current	I <sub>D</sub>	T <sub>C</sub> = 25°C		A
		T <sub>C</sub> = 100°C		
Drain Current Pulsed(Note 1)	I <sub>DM</sub>	60		A
Power Dissipation(T <sub>C</sub> =25°C) -Derate above 25°C	P <sub>D</sub>	35	68	W
		0.35	0.67	W/°C
Single Pulsed Avalanche Energy (Note 2)	E <sub>AS</sub>	49		mJ
Operation Junction Temperature Range	T <sub>J</sub>	-55~+150		°C
Storage Temperature Range	T <sub>stg</sub>	-55~+150		°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300		°C

## THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX		Unit
		SGF15N10	SGU/D15N10	
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	1.6	3.3	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	62.5	62.0	°C/W

## ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
Drain -Source Breakdown Voltage	B <sub>VDS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	--	--	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V	--	--	-100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =250μA	1.0	1.9	3.0	V
Static Drain- Source On State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =7.5A	--	55	110	mΩ
		V <sub>GS</sub> =6V, I <sub>D</sub> =7.5A	--	62	110	
<b>Dynamic Characteristics</b>						
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V; f=1.0MHZ	1	1.0	10	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1.0MHZ	--	441	--	pF
Output Capacitance	C <sub>oss</sub>		--	157	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	12.8	--	
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, V <sub>DS</sub> =10V R <sub>G</sub> =3Ω; I <sub>D</sub> =10A (Note 3.4)	--	7.6	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	21.5	--	
Turn-off Delay Time	t <sub>d(off)</sub>		--	26.3	--	

Turn-off Fall Time	$t_f$	$V_{DD}=50V, V_{DS}=10V$ $R_G=3\Omega; I_D=10A$ (Note 3.4)	--	9.5	--	ns
Total Gate Charge	$Q_g$	$V_{DS}=50V, I_D=10A$ $V_{GS}=10V$ (Note 3.4)	--	25	--	nc
Gate-Source Charge	$Q_{gs}$		--	11.4	--	
Gate-Drain Charge	$Q_{gd}$		--	3.8	--	

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	$I_S$	Integral Reverse P-N Junction Diode in the MOSFET	--	--	15	A
Pulsed Source Current	$I_{SM}$		--	--	60	
Diode Forward Voltage	$V_{SD}$	$I_S=15A, V_{GS}=0V$	--	1.0	1.4	V
Reverse Recovery Time	$T_{rr}$	$I_F=10A, V_R=10V,$ $dIF/dt=100A/\mu S$	--	95	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	0.6	--	$\mu C$

1. Pulse width limited by maximum junction temperature
2.  $L=0.5mH, V_{DD}=50V, I_D=7A, V_G=10V, R_G=25\Omega$ , starting  $T_J=25^\circ C$
3. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

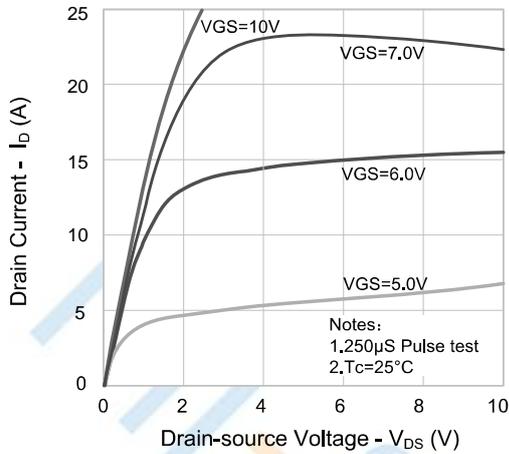


Figure 2. Transfer Characteristics

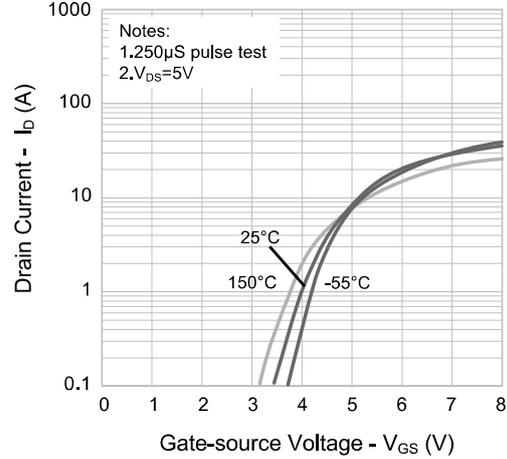


Figure 3. On-resistance vs. Drain Current

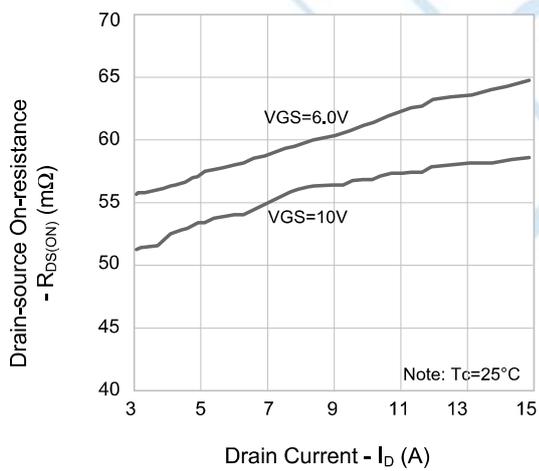


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

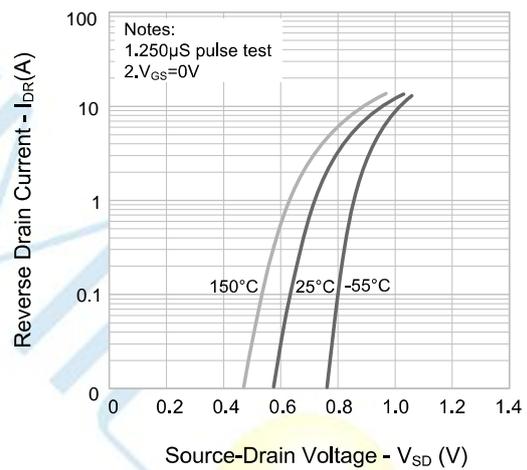


Figure 5. Capacitance Characteristics

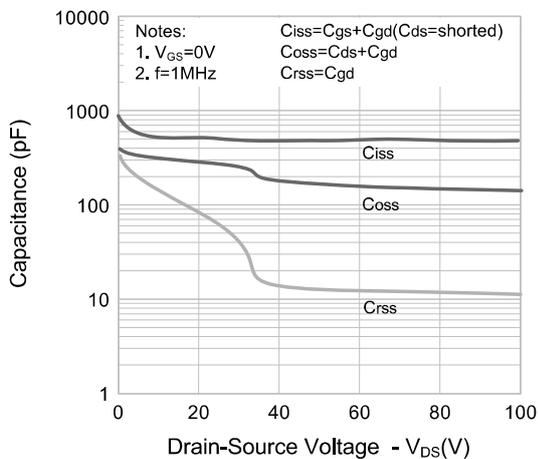
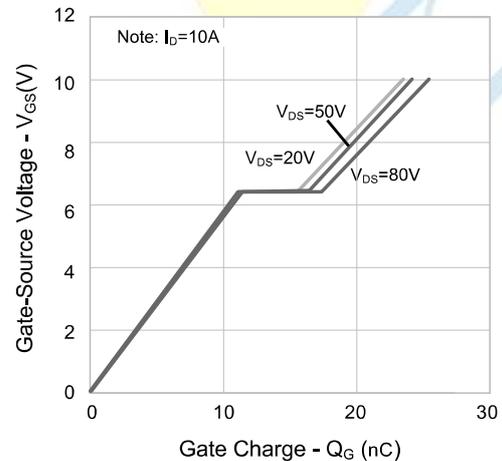
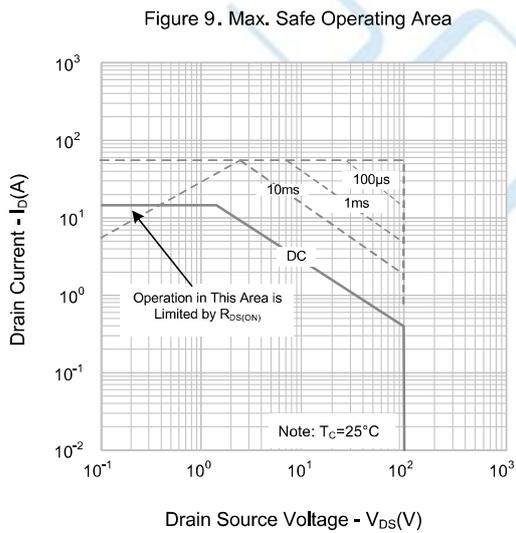
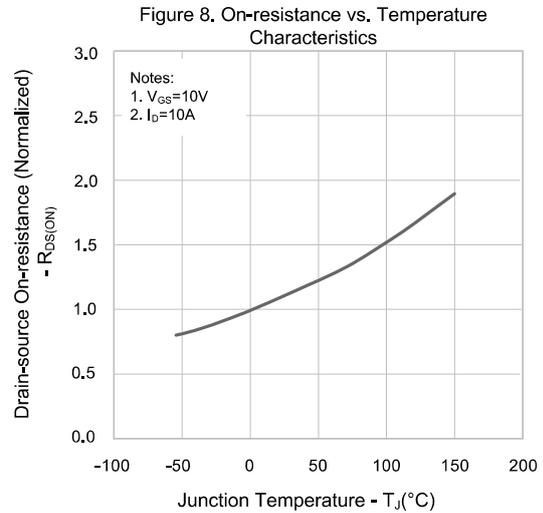
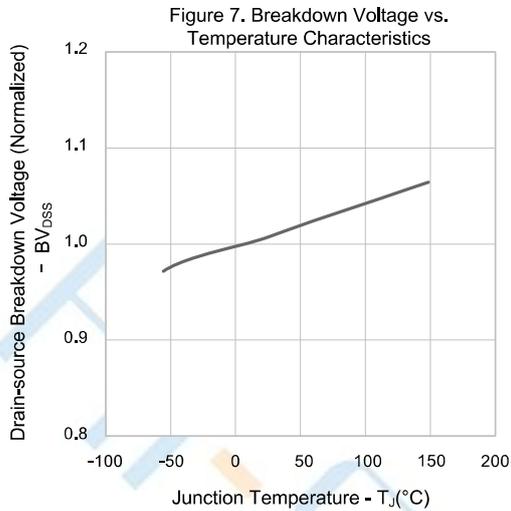


Figure 6. Gate Charge Characteristics

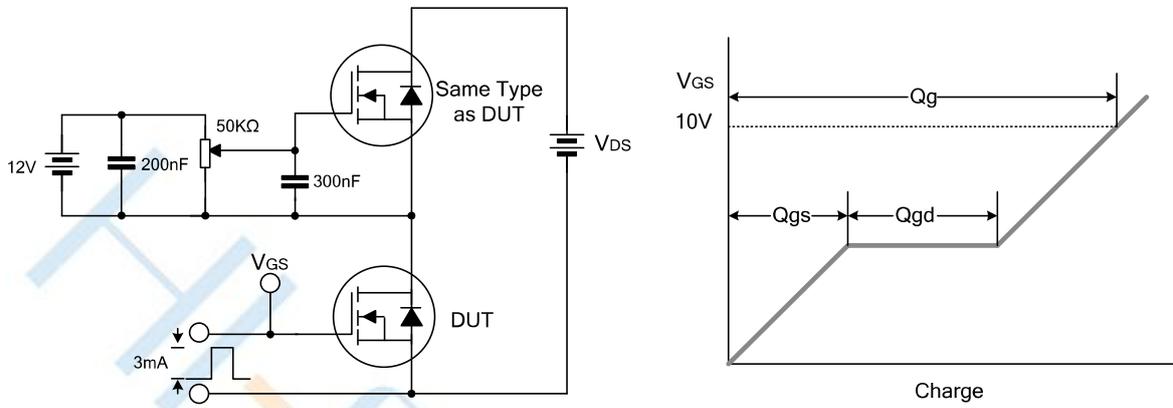


Typical Performance Characteristics

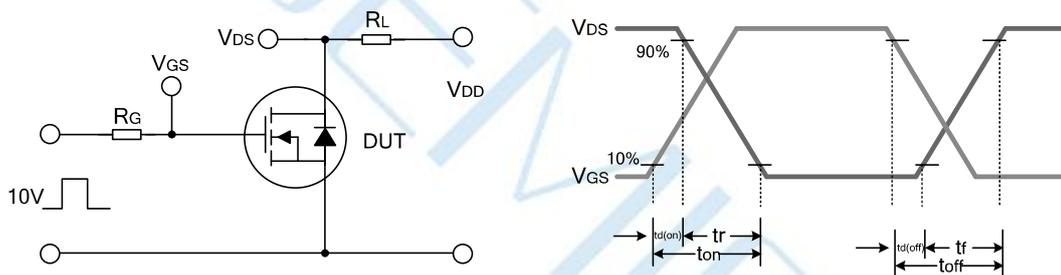


Test Circuit

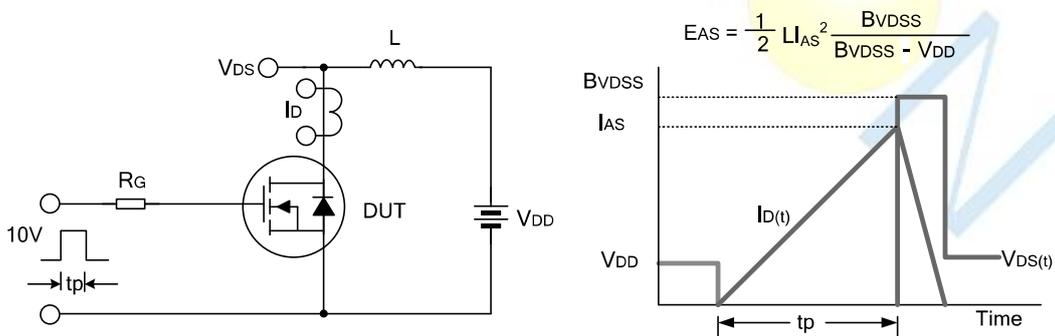
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

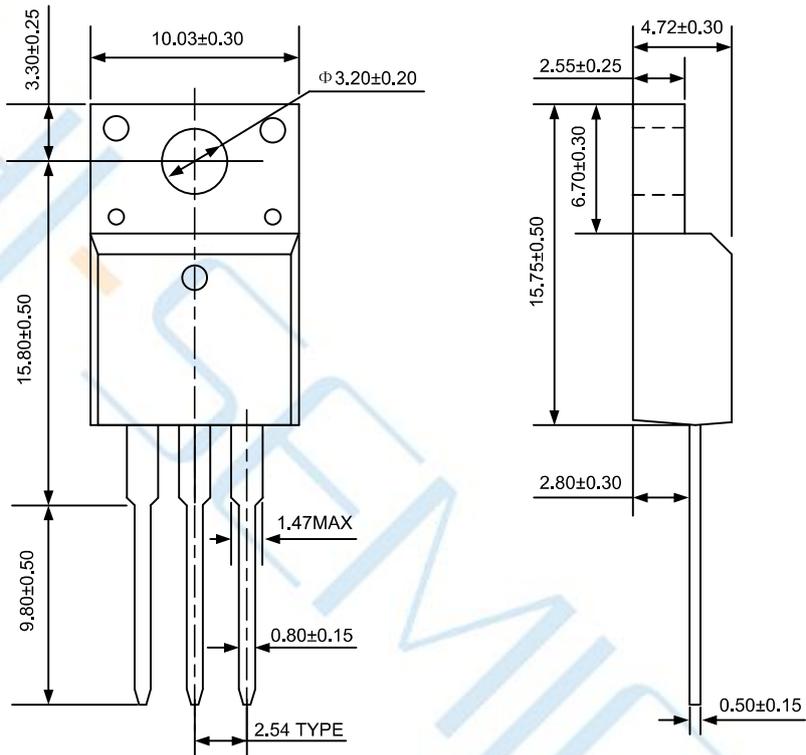


Unclamped Inductive Switching Test Circuit & Waveform



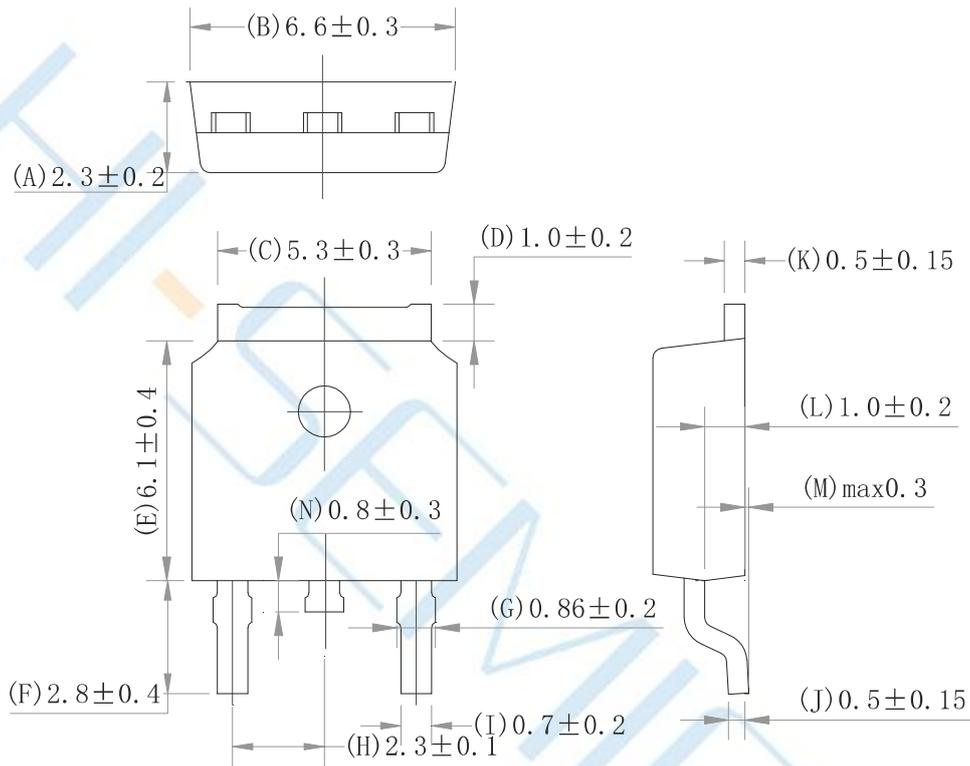
Package Dimensions of TO-220F-3L

Unit:mm



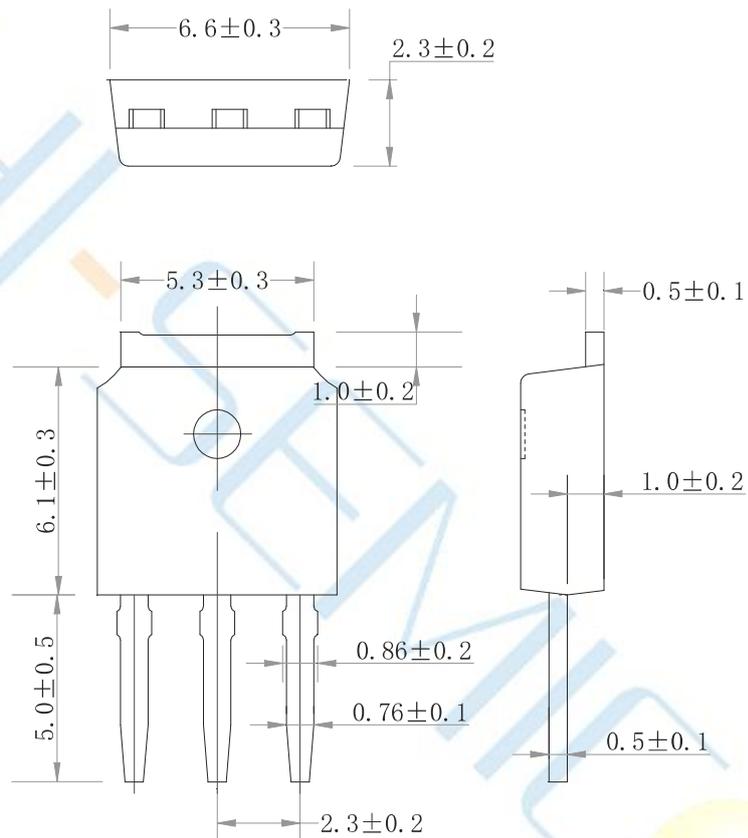
Package Dimensions of TO-252-2L

Unit:mm



Package Dimensions of TO-251D-3L

Unit:mm



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