

# SI-8000Q Series Surface Mount, Current Mode Control Step-down Switching Mode

## ■ Features

- Compact surface-mount package (HSOP8)
- Introduction of current mode control method
- Output current: 3.5A
- High efficiency: 90% ( $V_o = 5 V$ )
- Built-in reference oscillator (500 kHz)
- A ceramic capacitor can be used for output
- Built-in drooping-type over current and thermal protection circuits
- Built-in soft start circuit
- Built-in on/off function (Active Hi)
- Low current consumption during off

## ■ Applications

- DVD recorder, FPD-TV
- Onboard local power supplies
- OA equipment

## ■ Recommended Operating Conditions

Parameter	Symbol	Ratings			Unit	Conditions
		SI-8005Q				
DC Input Voltage Range	$V_{IN}$	$V_o+3V$ to 28			V	
Output Voltage Range	$V_o$	0.5 to 24			V	
Output Current Range	$I_o$	0 to 3.5			A	
Operating Junction Temperature Range	$T_{jop}$	-30 to +125			°C	
Operating Temperature Range	$T_{op}$	-30 to +85			°C	

\*1 : The minimum value of the input voltage range is 4.75 V or  $V_o + 3 V$ , whichever is higher.

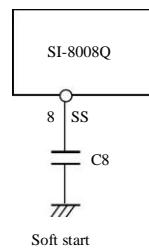
## ■ Electrical Characteristics

( $R_1=46k\Omega$ ,  $R_2=5.1k\Omega$  when  $T_a = 25^\circ C$  and  $V_o=5V$ )

Parameter	Symbol	Rating			Unit		
		SI-8005Q	min.	typ.			
Reference Voltage	$V_{ADJ}$	0.485	0.500		V		
		Conditions	$V_{IN}=12V$ , $I_o=1A$				
Temperature Coefficient of Reference Voltage	$\Delta V_{ADJ}/\Delta T$	0.05		$V_{IN}=12V$ , $I_o=1A$ , $T_a=-40$ to $+85^\circ C$		mV/°C	
		Conditions	$V_{IN}=12V$ , $I_o=1A$ , $T_a=-40$ to $+85^\circ C$				
Efficiency	$\eta$	90		$V_{IN}=12V$ , $I_o=1A$		%	
		Conditions	$V_{IN}=12V$ , $I_o=1A$				
Oscillation Frequency	$f_o$	450	500		550	kHz	
		Conditions	$V_{IN}=16V$ , $I_o=1A$				
Line Regulation	$\Delta V_{OLN}$	30		60		mV	
		Conditions	$V_{IN}=8$ to $28V$ , $I_o=1A$				
Load Regulation	$\Delta V_{OLD}$	30		60		mV	
		Conditions	$V_{IN}=12V$ , $I_o=0.1$ to $3.5A$				
Over current Protection Starting Current	$I_S$	3.6	6.0			A	
		Conditions	$V_{IN}=12V$				
Quiescent Circuit Current	$I_Q$	18		$V_{IN}=12V$ , $I_o=0A$ , $V_{EN}=open$		mA	
		Conditions	$V_{IN}=12V$ , $I_o=0A$ , $V_{EN}=open$				
		20		$V_{IN}=12V$ , $I_o=0A$ , $V_{EN}=0V$			
SS Pin	Outflow Current at Low Voltage	$I_{SSL}$	5			$\mu A$	
			$V_{IN}=1.6V$ , $V_{SSL}=0V$				
EN Pin	High Level Voltage	$V_{C/N}$	2.8			V	
			Conditions	$V_{IN}=12V$			
			$V_{IN}=12V$				
	Low Level Voltage	$V_{C/L}$	2.2			V	
			Conditions	$V_{IN}=12V$			
			$V_{IN}=12V$				
	Inflow Current at Low Low Voltage	$I_{C/E\ H}$	5			$\mu A$	
			Conditions	$V_{EN}=0V$			
			$V_{EN}=0V$				
Error Amplifier Voltage Gain		$A_{EA}$	1000			V/V	
Error Amplifier Transformer Conductance		$G_{EA}$	800			$\mu A/V$	
Current Sense Amplifier Impedance		$1/G_{CS}$	0.35			V/A	
Maximum ON Duty		$D_{MAX}$	92			%	
Minimum ON Time		$D_{MIN}$	100			nsec.	

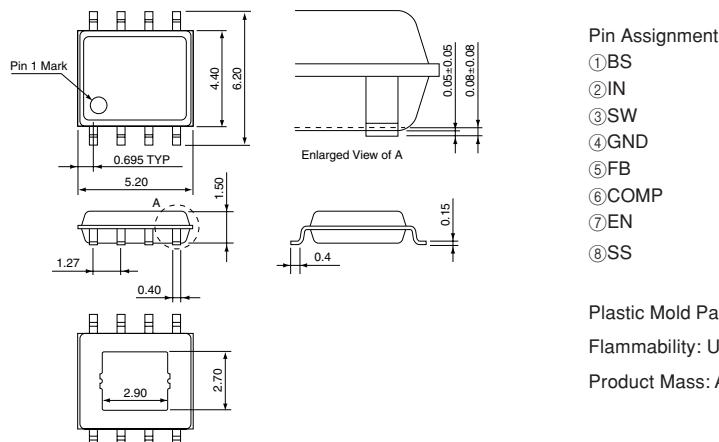
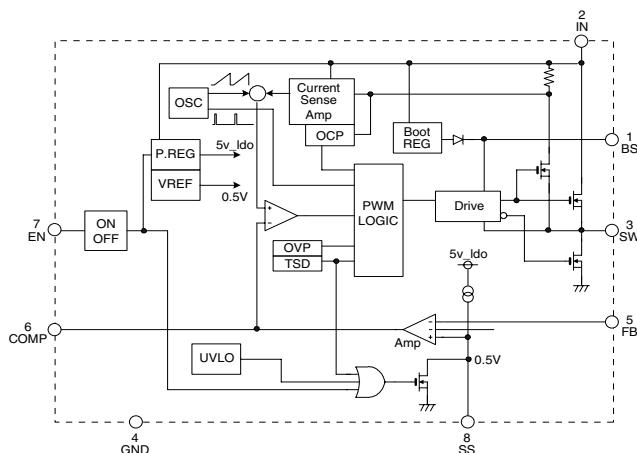
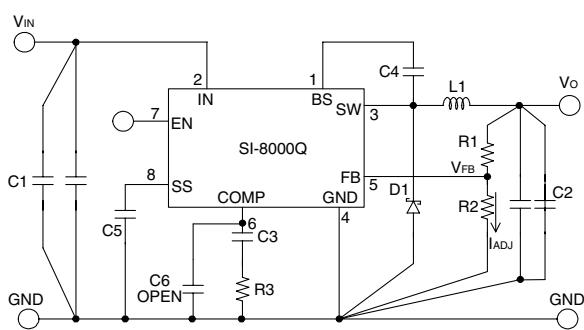
\*: Pin 8 is the SS pin. Soft start at power on can be performed with a capacitor connected to this pin.

The SS pin is pulled up to the power supply in the IC, so applying the external voltage is prohibited.



**External Dimensions (HSOP8)**

(Unit : mm)

**Block Diagram****Typical Connection Diagram**

C1:10 $\mu$ F/50V  
(Murata:GRM55DB31H106KA87)  
C2:22 $\mu$ F/16V  
(Murata:GRM32ER71A226KE20)  
C3:560pF<sup>1</sup>  
(Murata:GRM18 Type)  
C4:10nF  
(murata: GRM18 Type)  
C5:10nF  
(murata: GRM18 Type)  
L1:10 $\mu$ H  
D1:SJPW-T4 (Sanken)  
R1:46k $\Omega$  (Vo=5V)  
R2:5.1k $\Omega$   
R3:24k $\Omega$  <sup>1</sup>

<sup>1</sup>\*1: When Vo=5V