



N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
30	0.093 at V _{GS} = 10 V	1.3 ^a	5.41		
30	0.129 at V _{GS} = 4.5 V	1.2	5.41		

FEATURES

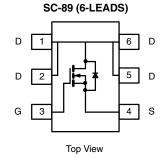
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_q and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

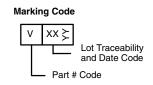


ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

· Load Switch for Portable Devices





Ordering Information: Si1072X-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	v	
Continuous Proin Current /T 150 °C\8	T _A = 25 °C	I-	1.3 ^{b, c}		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C	I _D	1.03 ^{b, c}	Α Α	
Pulsed Drain Current		I _{DM}	6		
Avalanche Current	L = 0.1 mH	I _{AS}	8		
Repetitive Avalanche Energy	L = 0.1 IIII	E _{AS}	3.2	mJ	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.2 ^{b, c}	A	
Manipular Davier Discipations	T _A = 25 °C	P _D	0.236 ^{b, c}	W	
Maximum Power Dissipation ^a	T _A = 70 °C) 'D	0.151 ^{b, c}	VV	
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	440	530	°C/W	
Maximum Junction-to-Ambient	Steady State		540	650	C/VV	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 650 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	$I_D = 250 \mu\text{A}$		30.4		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$			- 1.86		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			10	- μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	6			Α
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 1.3 A		0.077	0.093	Ω
		$V_{GS} = 4.5 \text{ V}, I_D = 1.2 \text{ A}$		0.107	0.129	
Forward Transconductance	9 _{fs}	V _{DS} = 15 V, I _D = 1.3 A		15		mS
Dynamic ^b						
Input Capacitance	C _{iss}			280		pF
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		55		
Reverse Transfer Capacitance	C _{rss}			35		
Total Gate Charge	Q _g Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 1.3 \text{ A}$		5.5	8.3	nC
		V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 1.3 A		2.7	4.1	
Gate-Source Charge				1.1		
Gate-Drain Charge	Q_{gd}			0.8		
Gate Resistance	R_g	f = 1 MHz		3.5	4.6	Ω
Turn-On Delay Time	t _{d(on)}			7	11	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 13.6 \Omega$		12	18	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 1.1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		12	18	
Fall Time	t _f			6	9	ns
Turn-On Delay Time	t _{d(on)}			13	20	115
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 15.5 \Omega$		31	47	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 0.97 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		9	14	
Fall Time	t _f			6	9	
Drain-Source Body Diode Characteristi	cs					
Pulse Diode Forward Current ^a	I _{SM}				6	Α
Body Diode Voltage	V _{SD}	I _S = 0.7 A		0.8	1.2	٧
Body Diode Reverse Recovery Time	t _{rr}			11.2	17	nC
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 1.2 A, dl/dt = 100 A/μs		4.5	6.8	ns
Reverse Recovery Fall Time	t _a	1 _F = 1.2 Δ, α/αι = 100 Δ/μδ		7.5		
Reverse Recovery Rise Time	t _b]		3.7		

Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

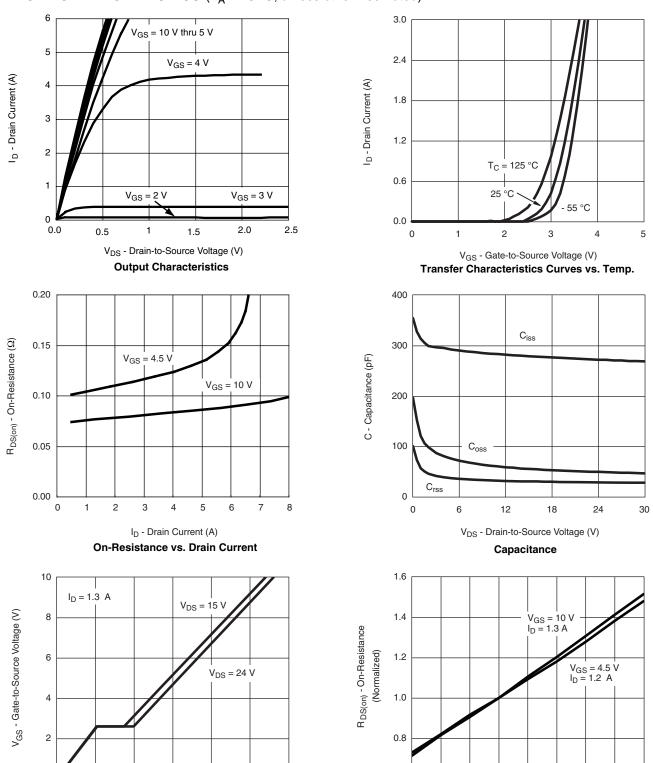
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



0.6

- 50

- 25

25

50

75

100

6

0

0

3

Q_g - Total Gate Charge (nC)

Gate Charge

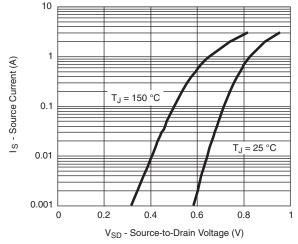
125

150

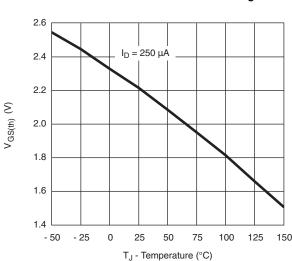
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TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



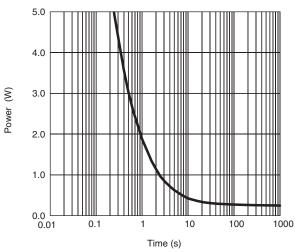
Source-Drain Diode Forward Voltage



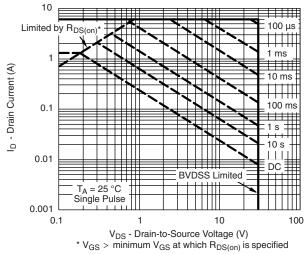
Threshold Voltage

 C_{CS} 0.16 C_{D} 0.17 C_{D} 0.18 C_{CS} 0.08 C_{CS} 0.04 C_{CS} 0.04 C_{CS} 0.04 C_{CS} 0.04 C_{CS} 0.04 C_{CS} 0.04 C_{CS} 0.06 C_{CS} 0.07 C_{CS} 0.09 $C_{\text{C$

 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



Single Pulse Power

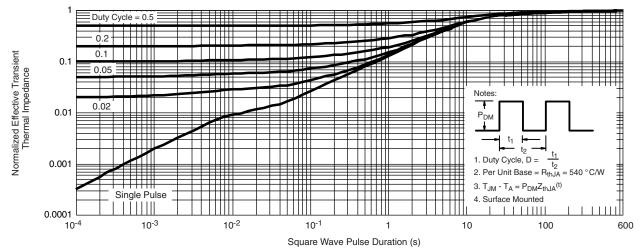


Safe Operating Area, Junction-to-Ambient





TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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Revision: 02-Oct-12 Document Number: 91000