

Vishay Siliconix

P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
	0.045 at V _{GS} = - 4.5 V	- 3.5		
- 8	0.072 at V _{GS} = - 2.5 V	- 2.8		
	0.120 at V _{GS} = - 1.8 V	- 2.0		

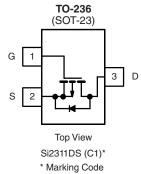
FEATURES

- Halogen-free Option Available
- TrenchFET[®] Power MOSFET

APPLICATIONS

· Load Switch





Ordering Information: Si2311DS-T1-E3 (Lead (Pb)-free) Si2311DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 8		V	
Gate-Source Voltage		V _{GS}	± 8			
Quality Design Quarter (T dec 20) ^a b	T _A = 25 °C	– I _D	- 3.5	- 3.0	٨	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		- 2.8	- 2.4		
Pulsed Drain Current		I _{DM}	- 10		A	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 0.8	- 0.6		
Maximum Power Dissipation ^{a, b}	T _A = 25 °C	– P _D	0.96	0.71	W	
	T _A = 70 °C		0.62	0.46		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marine lunction to Amhienta	t ≤ 5 s	- R _{thJA} R _{thJF}	100	130	
Maximum Junction-to-Ambient ^a	Steady State		140	175	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		60	75	

Notes:

a. Surface Mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.

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			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = - 10 µA	- 8			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.45		- 0.8		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -6.4 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	цА	
		V_{DS} = - 6.4 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	- μΑ	
On-State Drain Current ^a		$V_{DS} \leq$ - 5 V, V_{GS} = - 4.5 V	- 6			А	
	I _{D(on)}	V_{DS} \leq - 5 V, V_{GS} = - 2.5 V	- 3			A	
Drain-Source On-Resistance ^a	R _{DS(on)}	V_{GS} = - 4.5 V, I _D = - 3.5 A		0.036	0.045	Ω	
		$V_{GS} = -2.5 \text{ V}, I_{D} = -3 \text{ A}$		0.058	0.072		
		V _{GS} = - 1.8 V, I _D = - 0.7 A		0.096	0.120		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$		9.0		S	
Diode Forward Voltage	V _{SD}	$I_{S} = -0.8 \text{ A}, V_{GS} = 0 \text{ V}$			- 1.2	V	
Dynamic ^b	<u> </u>		-				
Total Gate Charge	Qg			8.5	12		
Gate-Source Charge	Q _{gs}	$V_{DS} = -4 V, V_{GS} = -4.5 V$ $I_{D} \cong -3.5 A$		1.5		nC	
Gate-Drain Charge	Q _{gd}	I <u>D</u> = − 0.0 A		2.1		1	
Input Capacitance	C _{iss}			970		pF	
Output Capacitance	C _{oss}	V_{DS} = - 4 V, V_{GS} = 0 V, f = 1 MHz		485			
Reverse Transfer Capacitance	C _{rss}			160			
Switching ^b							
Turn-On Time	t _{d(on)}			18	25		
	t _r	$V_{DD} = -4 \text{ V}, \text{ R}_{L} = 4 \Omega$ $I_{D} \cong -1.0 \text{ A}, \text{ V}_{GEN} = -4.5 \text{ V}$		45	65		
Turn-Off Time	t _{d(off)}	$R_{\rm G} = 6 \Omega$		40	60	ns	
	t _f	··· · · · · · · · · · · · · · · · · ·		45	65		

Notes:

a. For DESIGN AID ONLY, not subject to production testing.

b. Pulse test: PW \leq 300 $\mu s,$ duty cycle \leq 2 %.

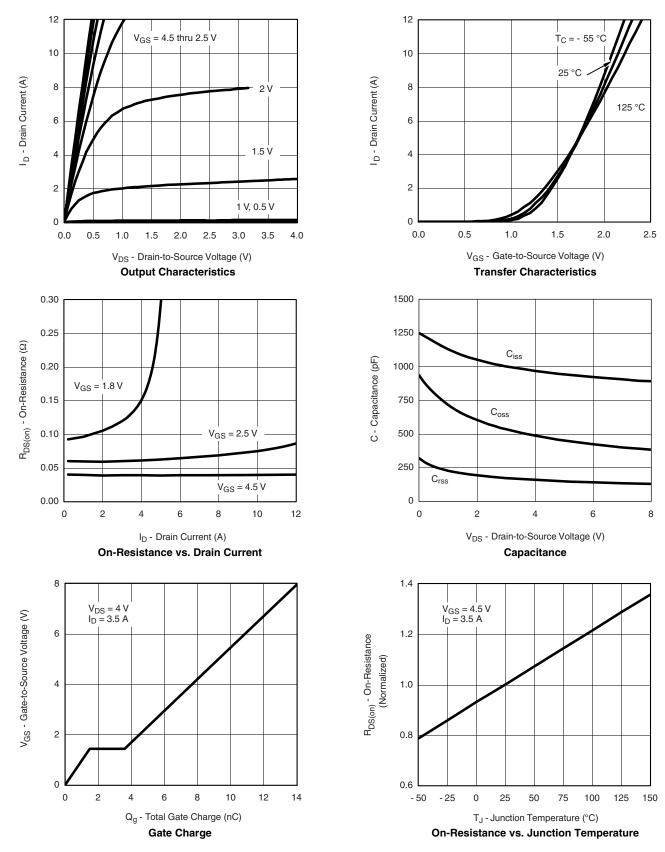
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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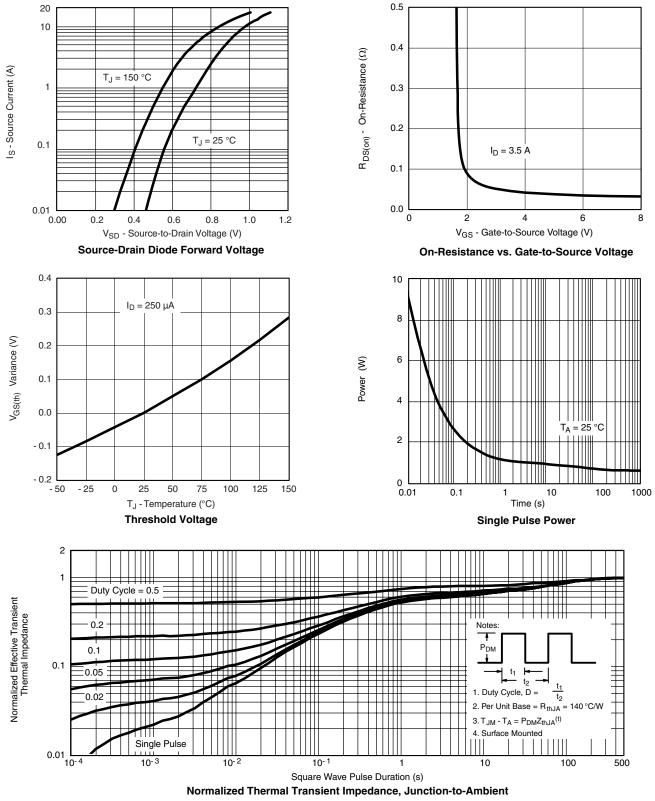
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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