

Micro Commercial Components



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SI2333

Features

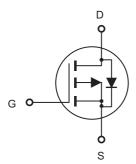
- Halogen free available upon request by adding suffix "-HF"
- TrenchFET Power Mosfet
- Excellent R_{DS(ON)}
- Marking Code: \$33
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1

Maximum Ratings @ 25 C Unless Otherwise Specified

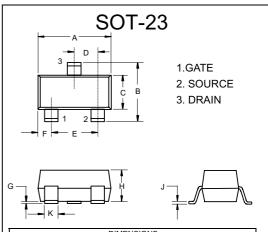
Symbol	Parameter	Rating	Unit
V_{DS}	Drain-source Voltage	-12	V
I_D	Drain Current-Continuous (1)	-6	Α
I _{DM}	Drain Current-Pulsed	-20	Α
V_{GS}	Gate-source Voltage	±8	V
P_D	Total Power Dissipation	0.35(2)	W
	Total Tower Dissipation	1.1 ⁽¹⁾	W
R _{⊕,JA}	Thermal Resistance from Junction to	357 ⁽²⁾	°C/W
K ⊕JA	Ambient	113 ⁽¹⁾	°C/W
TJ	Operating Junction Temperature	-55 to +150	$^{\circ}$ C
T _{STG}	Storage Temperature	-55 to +150	$^{\circ}\mathbb{C}$

NOTE 1. Device mounted on FR-4 substrate board, with minimum recommended pad layout, single side.

Internal Block Diagram

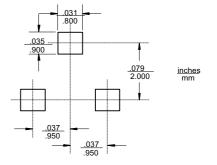


P-Channel Enhancement Mode Field Effect Transistor



DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Н	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	015	020	37	51	

Suggested Solder Pad Layout



^{2.} Device mounted on no heat sink.



SI2333

Electrical characteristics (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V(BR)DSS	V _{GS} = 0V, I _D =-250µA	-12			V
Zero gate voltage drain current	IDSS	V _{DS} =-12V,V _{GS} = 0V			-1	μΑ
Gate-body leakage current	Igss	V _{GS} =±8V, V _{DS} = 0V			±0.1	
Gate threshold voltage (note 3)	VGS(th)	V _{DS} =V _{GS} , I _D =-250μA	-0.4		-1	V
		V _{GS} =-4.5V, I _D =-5A			28	
	R _{DS(on)}	V _{GS} =-3.7V, I _D =-4.6A			32	mΩ
Drain-source on-resistance (note 4)		V _{GS} =-2.5V, I _D =-4.3A			40	
		V _{GS} =-1.8V, I _D =-1A			63	
		V _{GS} =-1.5V, I _D =-0.5A			150	
Forward tranconductance (note 3)	g FS	V _{DS} =-5V, I _D =-5A		18		S
Dynamic characteristics (note 4)						
Input Capacitance	C _{iss}			1275		pF
Output Capacitance	Coss	V _{DS} =-6V,V _{GS} =0V,f =1MHz		255		pF
Reverse Transfer Capacitance	C _{rss}			236		pF
Gate resistance	Rg	f=1MHz	1.9		19	Ω
Total Gate Charge	Qg			14	21	nC
Gate-Source Charge	Q_{gs}	V _{DS} =-6V,V _{GS} =-4.5V,I _D =-5A		2.3		nC
Gate-Drain Charge	Q_{gd}			3.6		nC
Turn-on delay time	td(on)			26	40	ns
Turn-on rise time	tr	V _{DD} =-6V,V _{GEN} =-4.5V,I _D =-4A		24	40	ns
Turn-off delay time	td(off)	$R_L=6\Omega,R_{GEN}=1\Omega$		45	70	ns
Turn-off fall time	tf			20	35	ns
Source-Drain Diode characteristics						
Diode forward current	Is	T _C =25℃			-1.4	Α
Diode pulsed forward current	I _{SM}				-20	Α
Diode Forward voltage (note 3)	V_{DS}	V _{GS} =0V, I _S =-4A			-1.2	V
Diode reverse recovery time (note 4)	t _{rr}	I _F =-4A,dI/dt=100A/μs		24	48	ns
Diode reverse recovery charge (note 4)	Q _{rr}			8	16	nC

Notes: 3. Pulse test; pulse width≤300µs, duty cycle≤2%.

4. Guaranteed by design, not subject to production testing.



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JUNCTION TEMPERATURE $T_{_{J}}$ (°C)

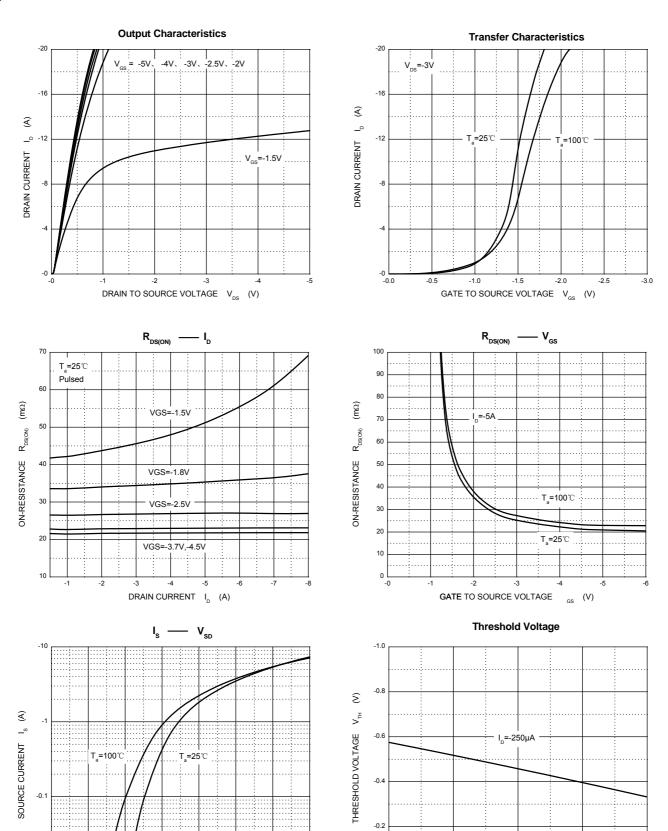
Typical Characteristics

-0.01

-0.6

SOURCE TO DRAIN VOLTAGE

-0.8



-0.0



Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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