SI2333

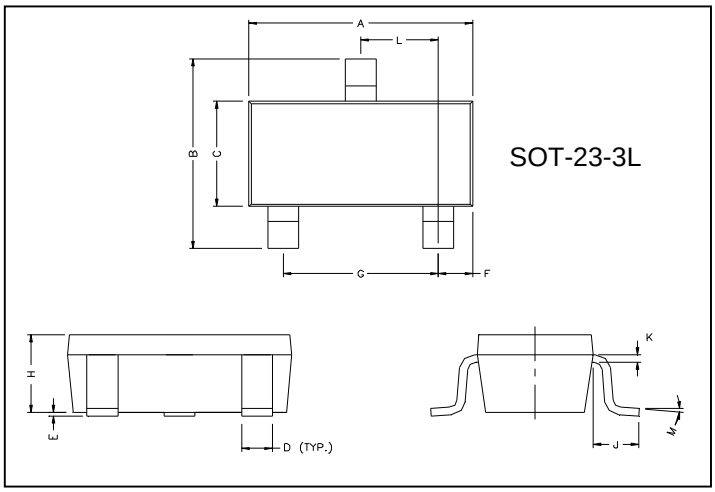
P-ChannelEnhancement Mode Power MOSFET

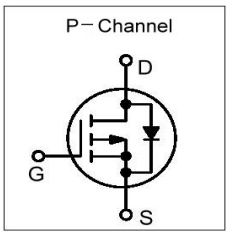
General Features

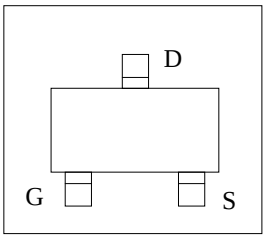
- V_{DS} = -12V,I_D = -6A
  $R_{DS(ON)}$ < 45mΩ @ V_{GS}=-2.5V
  $R_{DS(ON)}$ < 30mΩ @ V_{GS}=-4.5V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- PWM applications
- Load switch
- Power management







REF.	Millimeter		REF.	Millimete	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	1.90	REF.
B	2.65	2.95	H	1.00	1.30
C	1.50	1.70	K	0.10	0.20
D	0.35	0.50	J	0.40	-
E	0	0.10	L	0.85	1.15
F	0.45	0.55	M	0°	10°

Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-12	V
Gate-Source Voltage	V_{GS}	±12	V
Drain Current -Continuous	I_D	-6	A
Drain Current -Pulsed (Note 1)	I_{DM}	-20	A
Maximum Power Dissipation	P_D	1.25	W
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	69	℃/W

Electrical Characteristics (TA=25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=-250μA	-12	-	-	V

Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-12V,V_{GS}=0V	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±12V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=-250μA	-0.45	-0.65	-1.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=-4.5V, I_D=-6A	-	19	30	mΩ
		V_{GS}=-2.5V, I_D=-5A	-	26	45	
Forward Transconductance	g_{FS}	V_{DS}=-5V,I_D=-6A		17	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=-6V,V_{GS}=0V, F=1.0MHz	-	1100	-	PF
Output Capacitance	C_{oss}		-	390	-	PF
Reverse Transfer Capacitance	C_{rss}		-	300	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=-6V,I_D=-1A , R_L=6Ω,V_{GEN}=-4.5V,R_g=6Ω	-	25	-	nS
Turn-on Rise Time	t_r		-	45	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	72	-	nS
Turn-Off Fall Time	t_f		-	60	-	nS
Total Gate Charge	Q_g	V_{DS}=-6V,I_D=-6A,V_{GS}=-4.5V	-	11.5	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	nC
Gate-Drain Charge	Q_{gd}		-	3.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=-1.0A	-	-	-1.2	V
Diode Forward Current (Note 2)	I_S		-	-	6	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

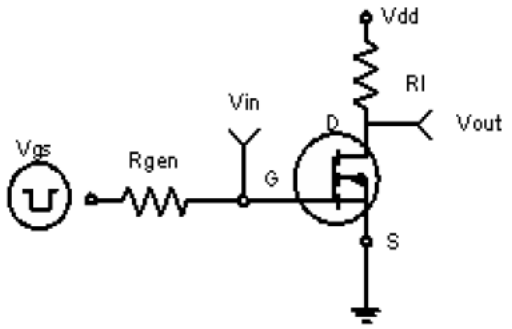


Figure 1:Switching Test Circuit

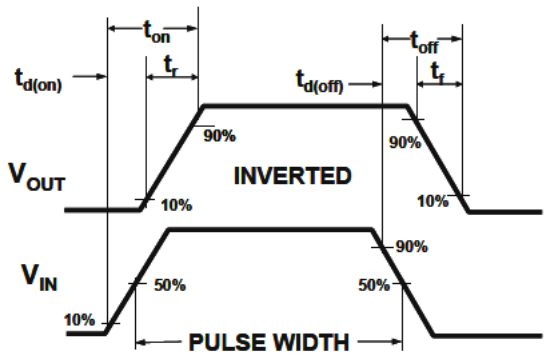
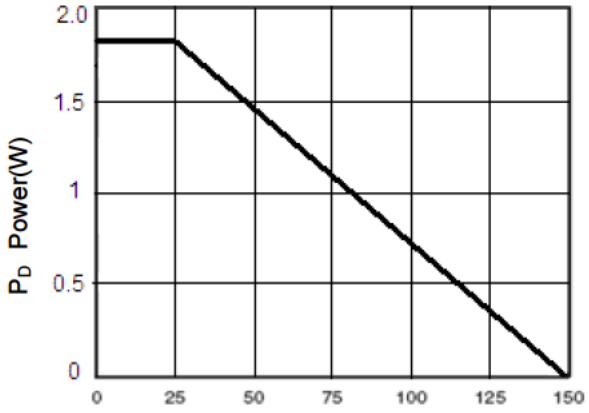
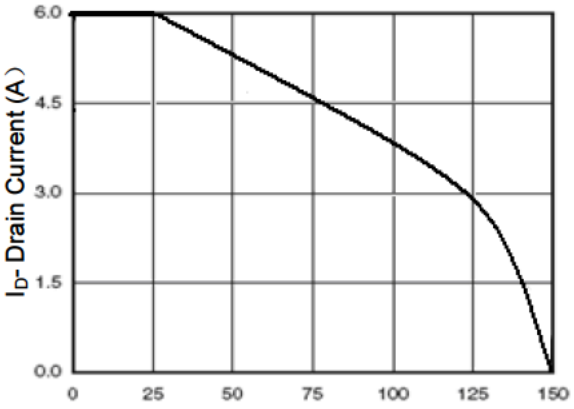


Figure 2:Switching Waveforms



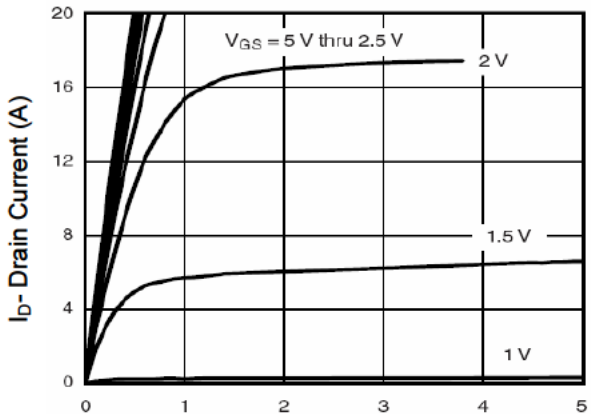
T_J-Junction Temperature(℃)

Figure 3 Power Dissipation



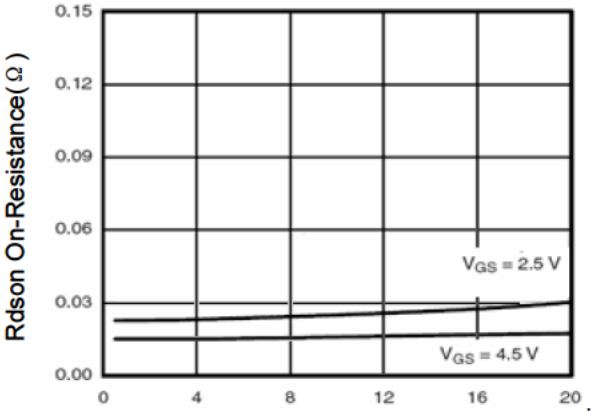
T_J-Junction Temperature(℃)

Figure 4 Drain Current



Vds Drain-Source Voltage (V)

Figure 5 Output Characteristics



I_D- Drain Current (A)

Figure 6 Drain-Source On-Resistance

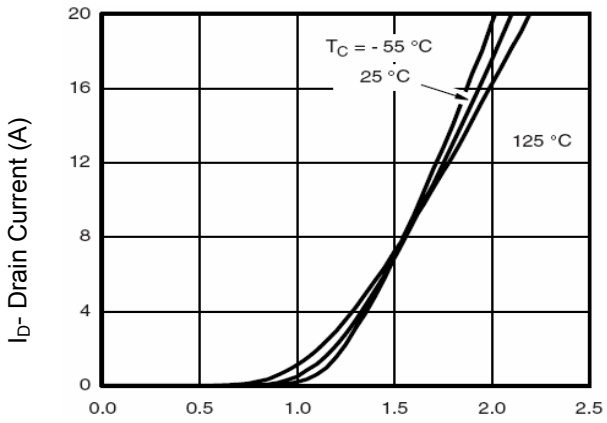


Figure 7 Transfer Characteristics

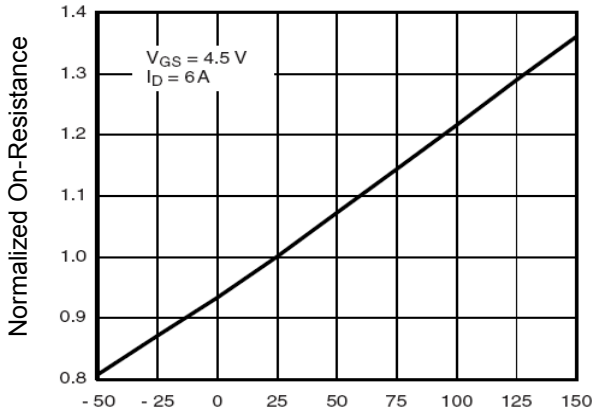


Figure 8 Drain-Source On-Resistance

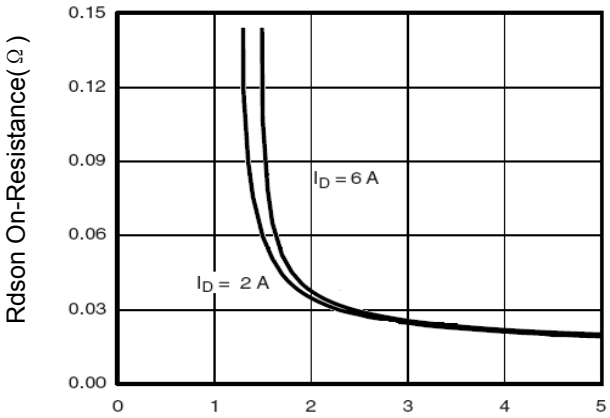


Figure 9 Rdson vs Vgs

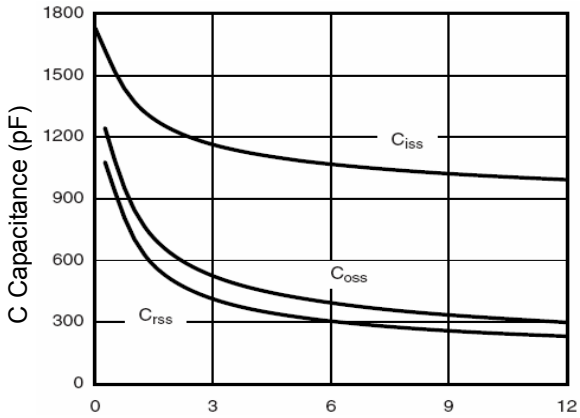


Figure 10 Capacitance vs Vds

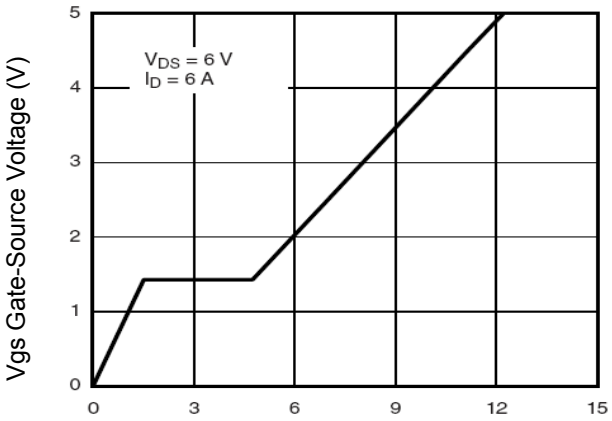


Figure 11 Gate Charge

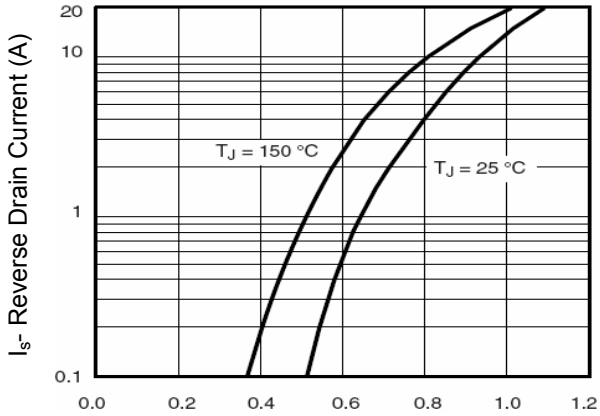
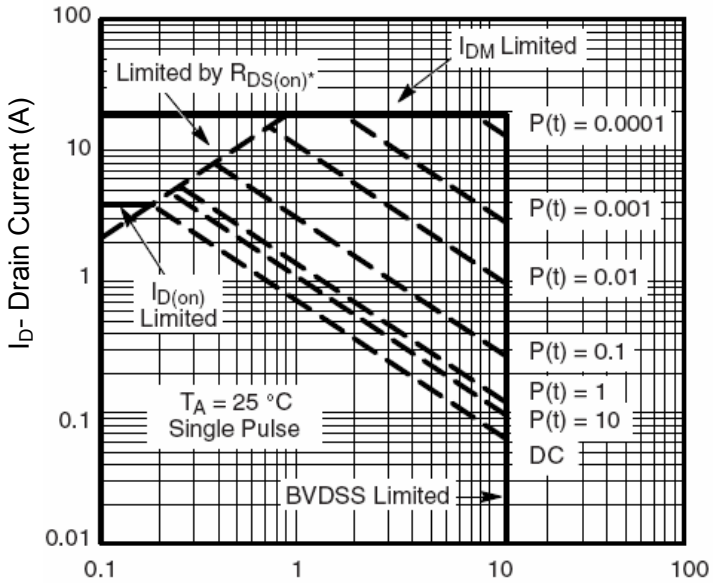
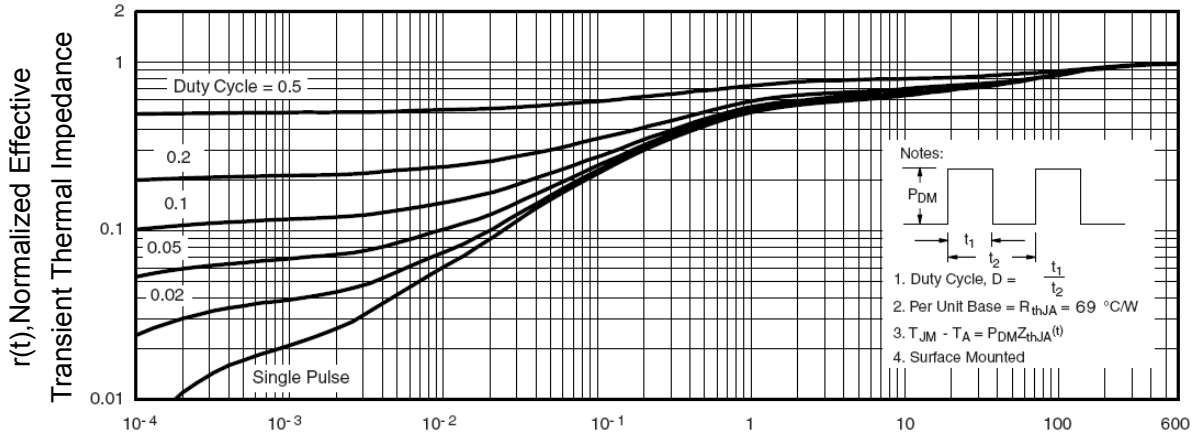


Figure 12 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area



Square Wave Pluse Duration(sec)

Figure 14 Normalized Maximum Transient Thermal Impedance