



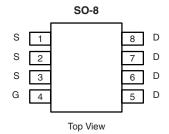
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.0065 at V _{GS} = 10 V	23		
	0.008 at V _{GS} = 4.5 V	17		

FEATURES

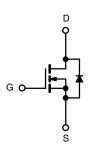
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFET
- 100 % R_g Tested





Ordering Information: Si4404DY-T1-E3 (Lead (Pb)-free)

Si4404DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Dunin Courset /T 450 00\8	T _A = 25 °C	- I _D	23	15	Δ.
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		19	12	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	60		Α
Continuous Source Current (Diode Conduction) ^a		I _S	2.9	1.3	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	3.5	1.6	W
	T _A = 70 °C		2.2	1	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian una lun ation to Analoia ata	t ≤ 10 s	R _{thJA}	29	35	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		67	80		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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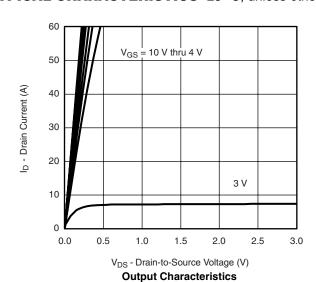
SPECIFICATIONS $T_J = 25 ^{\circ}\text{C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	V _{DS} = 30 V, V _{GS} = 0 V		1			
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
Drain-Source On-State Resistance ^a	В	V _{GS} = 10 V, I _D = 23 A		0.0045	0.0065			
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 17 \text{ A}$		0.0068	0.008	Ω		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 23 A		80		S		
Diode Forward Voltage ^a	V_{SD}	I _S = 2.9 A, V _{GS} = 0 V		0.8	1.2	V		
Dynamic ^b								
Total Gate Charge	Q_g			36	55			
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 23 \text{ A}$		15		nC		
Gate-Drain Charge	Q_{gd}			12				
Gate Resistance	R_g		1.5	2.2	3.7	Ω		
Turn-On Delay Time	t _{d(on)}			20	30			
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		15	23			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_G = 6 Ω		105	160	ns		
Fall Time	t _f			40	60			
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 2.9 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		50	80			

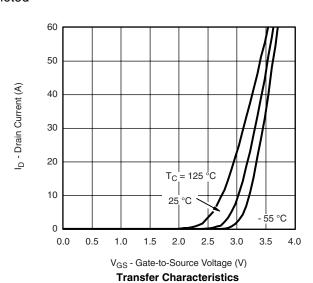
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



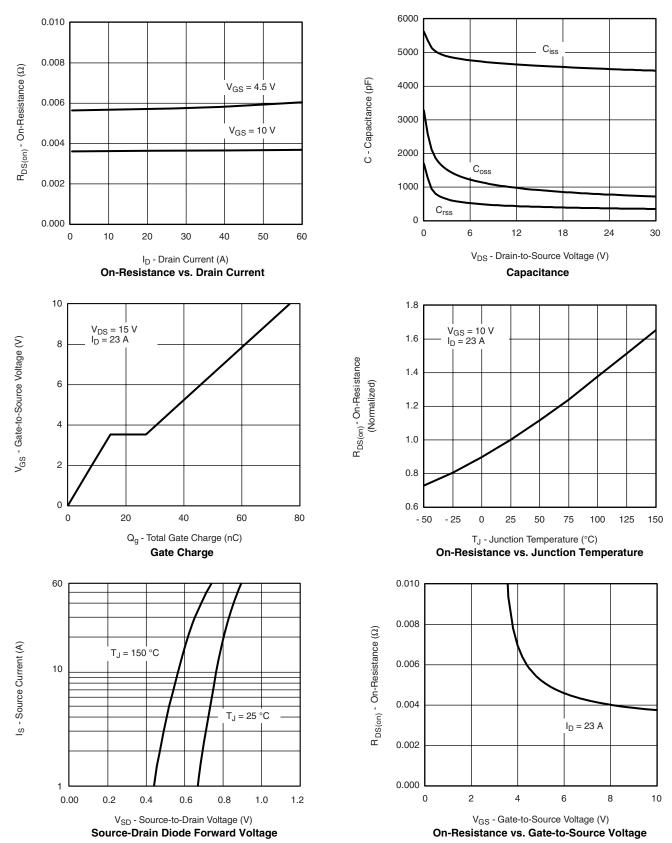








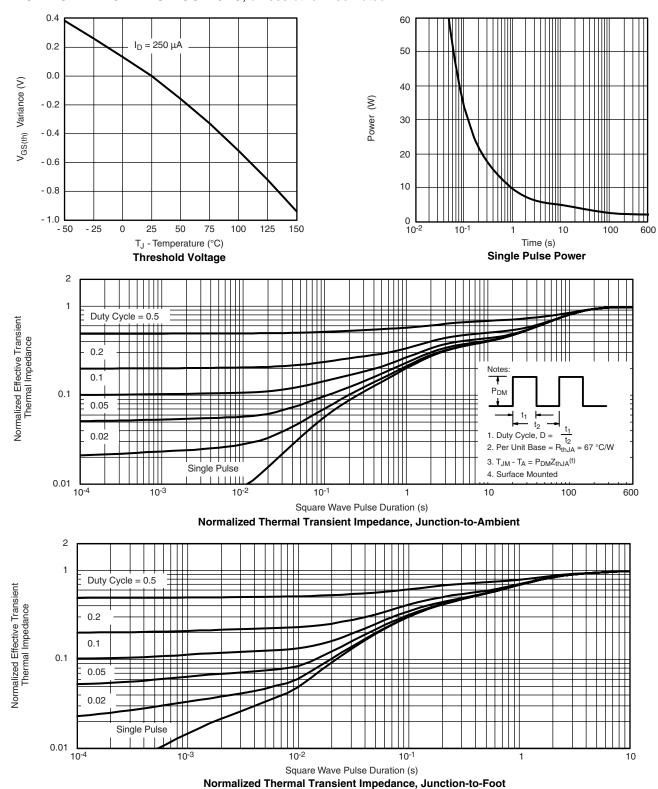
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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