



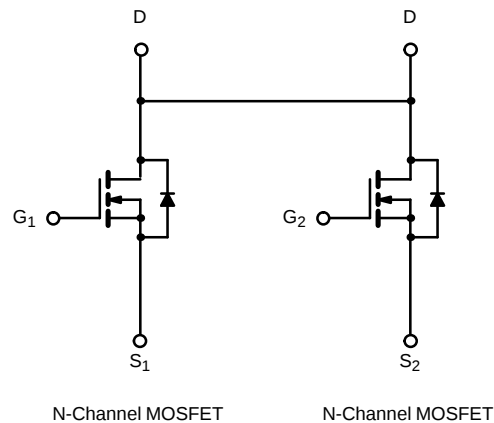
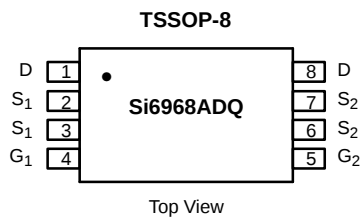
N-Channel 2.5-V (G-S) Battery Switch

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
20	0.022 @ $V_{GS} = 4.5$ V	± 6.2
	0.030 @ $V_{GS} = 2.5$ V	± 5.3

2.5-V Rated

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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	10 secs	Steady State	Unit
Drain-Source Voltage	V_{DS}	20		V
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	± 6.2	± 5.1	A
		± 5.3	± 3.4	
Pulsed Drain Current (10 μs Pulse Width)	I_{DM}	± 30		
Continuous Source Current (Diode Conduction) ^a	I_S	1.5	1.0	
Maximum Power Dissipation ^a	P_D	1.5	1.0	W
		0.96	0.64	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	72	83	$^\circ\text{C/W}$
		100	120	
Maximum Junction-to-Foot	R_{thJF}	55	70	

Notes

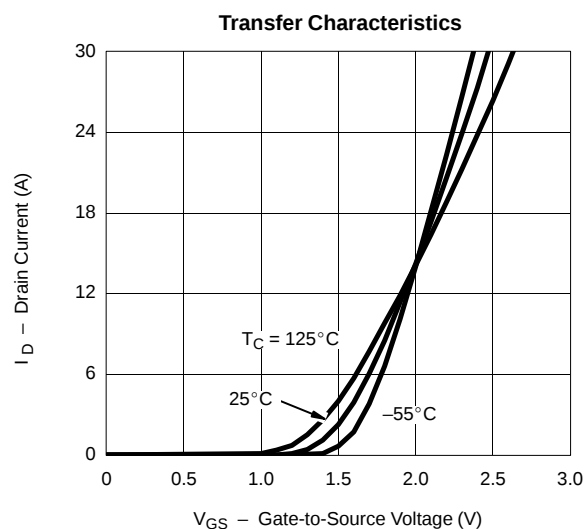
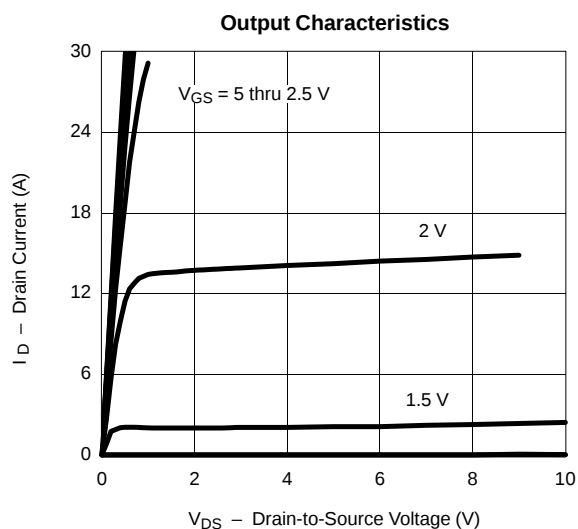
a. Surface Mounted on 1" x 1" FR4 Board.

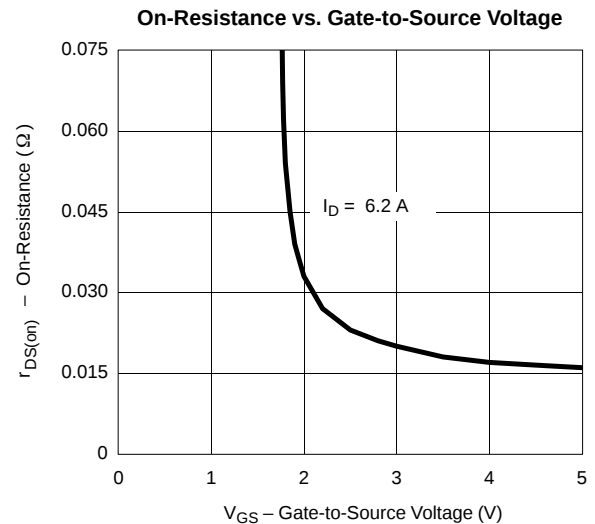
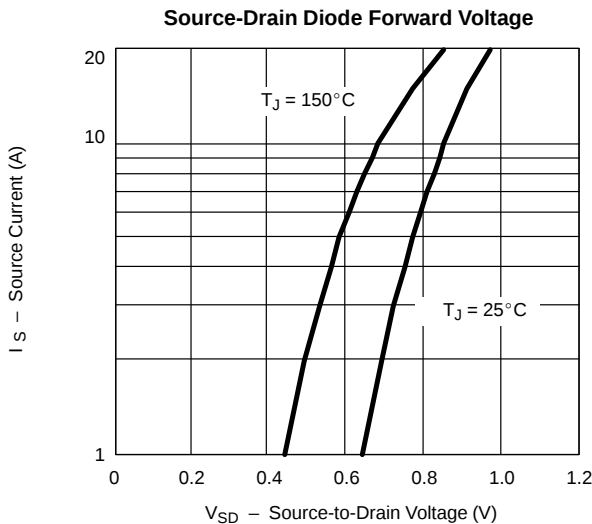
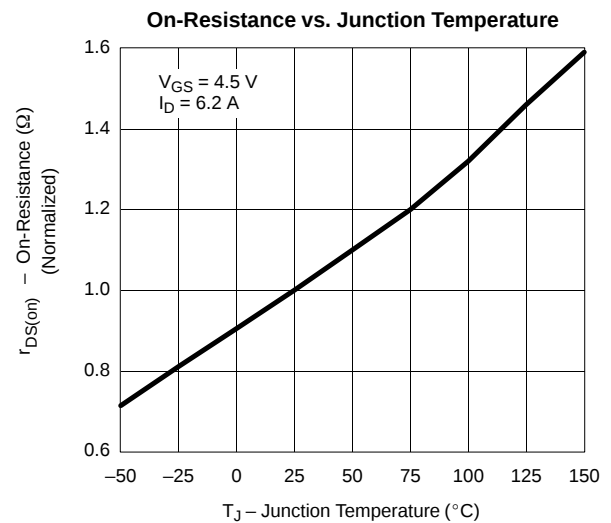
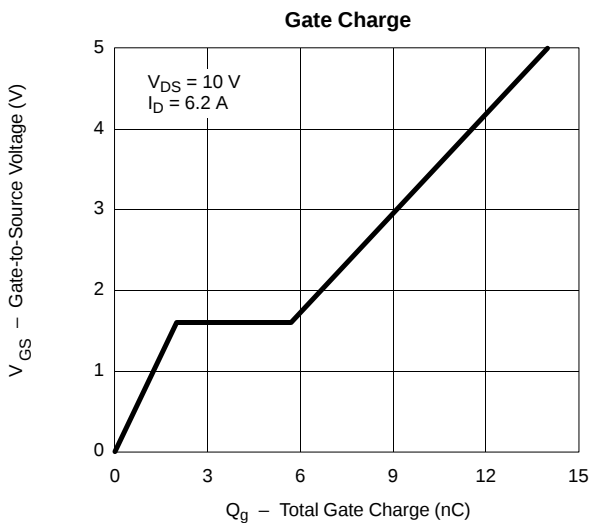
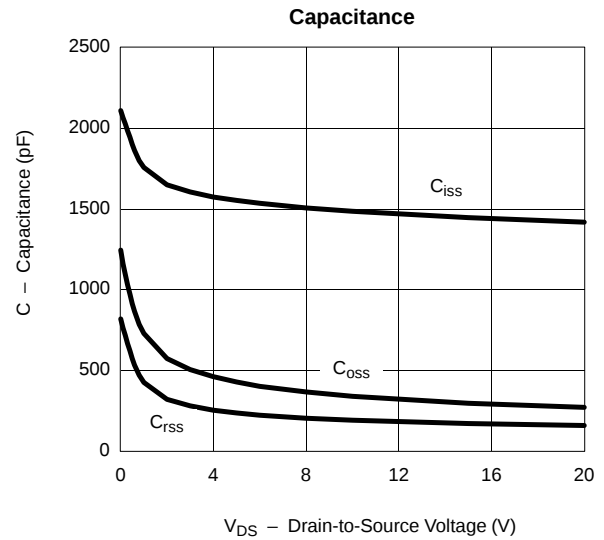
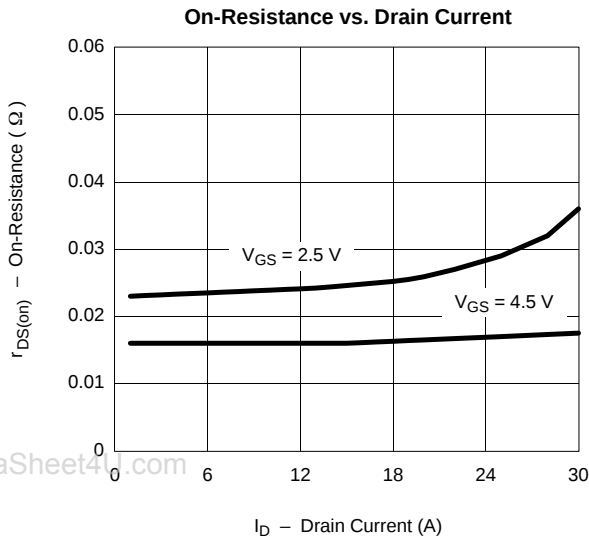
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	0.6			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 12\ \text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16\ \text{V}$, $V_{GS} = 0\ \text{V}$			1	μA
		$V_{DS} = 16\ \text{V}$, $V_{GS} = 0\ \text{V}$, $T_J = 85^\circ\text{C}$			15	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\ \text{V}$, $V_{GS} = 4.5\ \text{V}$	30			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = 4.5\ \text{V}$, $I_D = 6.2\ \text{A}$	0.014	0.018	0.022	Ω
		$V_{GS} = 2.5\ \text{V}$, $I_D = 5.3\ \text{A}$	0.018	0.024	0.030	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\ \text{V}$, $I_D = 6.2\ \text{A}$		25		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 6.2\ \text{A}$, $V_{GS} = 0\ \text{V}$		0.89	1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 10\ \text{V}$, $V_{GS} = 4.5\ \text{V}$, $I_D = 6.2\ \text{A}$		13.5	20	nC
Gate-Source Charge	Q_{gs}			2		
Gate-Drain Charge	Q_{gd}			3.7		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\ \text{V}$, $R_L = 10\ \Omega$ $I_D \cong 1\ \text{A}$, $V_{GEN} = 4.5\ \text{V}$, $R_G = 6\ \Omega$		18	30	ns
Rise Time	t_r			25	50	
Turn-Off Delay Time	$t_{d(off)}$			50	100	
Fall Time	t_f			25	50	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 6.2\ \text{A}$, $di/dt = 100\ \text{A}/\mu\text{s}$		40	70	

Notes

- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

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