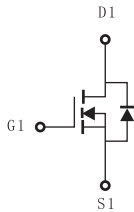


## Dual N-Channel 2.5-V (G-S) MOSFET

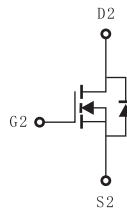
## SI9926

## ■ Features

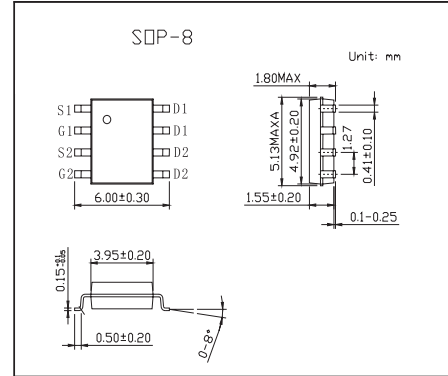
- Dual N-Channel 2.5-V (G-S) MOSFET



N-Channel MOSFET



N-Channel MOSFET

■ Absolute Maximum Ratings  $T_A=25^\circ\text{C}$ 

Parameter	Symbol	10 secs	Steady state	Unit	
Drain-source Voltage	$V_{DS}$	20		V	
Gate-source Voltage	$V_{GS}$	$\pm 12$		V	
pulsed Drain Current	$I_{DM}$	30		A	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	6	4.8	A
		$T_A = 70^\circ\text{C}$	5	3.8	A
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	2.0	1.25	W
		$T_A = 70^\circ\text{C}$	1.3	0.8	W
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	1.7	1	A	
Operating Junction and Storage temperature Range	$T_J$ $T_{stg}$	-55 to +150		$^\circ\text{C}$	

## ■ Thermal Resistance Ratings

Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	50	62.5	$^\circ\text{C}/\text{W}$
		Steady State	80	100	
Maximum Junction-to-Foot (Drain) Steady State	$R_{thJF}$	30	40		

## Notes

a. Surface Mounted on 1"x1" FR4 Board.

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## ■ Electrical Characteristics TA=25°C ±3°C

Parameter	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DC</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 uA	0.6			V
Gate-Body Leakage Current, Reverse	I <sub>GSS</sub>	V <sub>GS</sub> = ±12 V, V <sub>DS</sub> = 0			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0, V <sub>DS</sub> = 20 V V <sub>GS</sub> = 0, V <sub>DS</sub> = 20 V, T <sub>J</sub> = 55°C			1 25	uA
On-State Drain Current*1	I <sub>D(ON)</sub>	V <sub>DC</sub> ≥ 5 V, V <sub>GS</sub> = 4.5 V	20			A
Static Drain-Source On-State Resistance *1	R <sub>ds(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6A V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 5A		0.023 0.030	0.030 0.040	Ω
Forward Transconductance *1	G <sub>FS</sub>	V <sub>DS</sub> = 15V, I <sub>D</sub> = 6A		22		S
Diode Forward On-Voltage *1	V <sub>SD</sub>	I <sub>S</sub> = 1.7 mA, V <sub>GS</sub> = 0 V		0.7	1.2	V
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6A		13	20	nc
Gate-Source Charge	Q <sub>gs</sub>			3		
Gate-Drain Charge	Q <sub>gd</sub>			3.3		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15V, I <sub>D</sub> = 1A, R <sub>L</sub> = 15 Ω V <sub>GEN</sub> = 4.5 V, R <sub>G</sub> = 6Ω		22	35	ns
Rise Time	t <sub>r</sub>			40	60	
Turn-Off Delay Time	t <sub>d(off)</sub>			50	75	
Fall Time	t <sub>f</sub>			20	30	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.7A, di/dt = 100 A/us		40	80	

\*1 pulse width ≤ 300 μs, duty cycle ≤ 2%