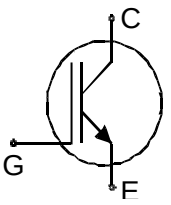
SIGC11T60NC

IGBT Chip in NPT-technology

FEATURES:
- 600V NPT technology
- 100µm chip
- positive temperature coefficient
- easy paralleling

This chip is used for:
- IGBT Modules

Applications:
- drives



Chip Type	V_{CE}	I_{Cn}	Die Size	Package	Ordering Code
SIGC11T60NC	600V	10A	3.25 x 3.25 mm^2	sawn on foil	Q67050-A4158-A001

MECHANICAL PARAMETER:

Raster size	3.25 x 3.25	mm^2
Area total / active	10.6 / 7.4	
Emitter pad size	2 x 1.6	
Gate pad size	1.08 x 0.68	
Thickness	100	µm
Wafer size	150	mm
Flat position	0	deg
Max.possible chips per wafer	1414	
Passivation frontside	Photoimide	
Emitter metallization	3200 nm Al Si 1%	
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond	Al, ≤500µm	
Reject Ink Dot Size	∅ 0.65mm ; max 1.2mm	
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C	

Edited by INFINEON Technologies AI PS DD HV3, L 7302-M, Edition 2, 28.11.2003

MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T_j=25 °C	V_{CE}	600	V
DC collector current, limited by T_{jmax}	I_C	1)	A
Pulsed collector current, t_p limited by T_{jmax}	I_{cpuls}	30	A
Gate-emitter voltage	V_{GE}	±20	V
Operating junction and storage temperature	T_j, T_{stg}	-55 ... +150	°C

1) depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_j=25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	$V_{(BR)CES}$	V_{GE}=0V, I_C=500µA	600			V
Collector-emitter saturation voltage	$V_{CE(sat)}$	V_{GE}=15V, I_C=10A	1.7	2.0	2.5	
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C=350µA, V_{GE}=V_{CE}	4.5	5.5	6.5	
Zero gate voltage collector current	I_{CES}	V_{CE}=600V, V_{GE}=0V			0.8	µA
Gate-emitter leakage current	I_{GES}	V_{CE}=0V, V_{GE}=20V			120	nA

DYNAMIC CHARACTERISTICS (tested at component):

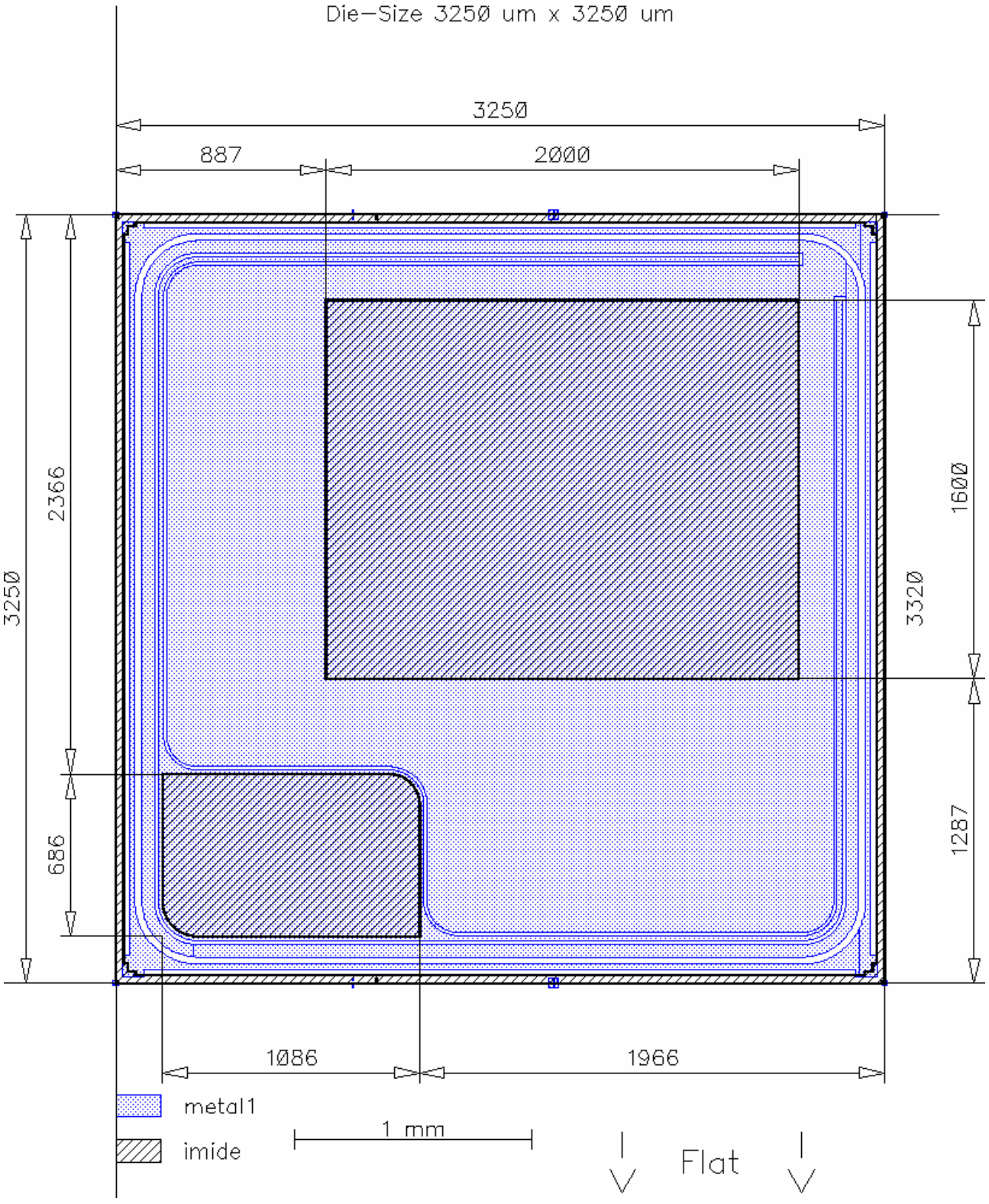
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Input capacitance	C_{iss}	V_{CE}=25V	-	550	-	pF
Output capacitance	C_{oss}	V_{GE}=0V	-	62	-	
Reverse transfer capacitance	C_{rss}	f=1MHz	-	42	-	

SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions 1)	Value			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	T_j=125°C V_{CC}=300V I_C=10A V_{GE}=±15/V R_G=27Ω	-	20	-	ns
Rise time	t_r		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	110	-	
Fall time	t_f		-	20	-	

1) values also influenced by parasitic L- and C- in measurement and package.

CHIP DRAWING:



FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet	tbd

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

Published by
Infineon Technologies AG,
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