# SIGC16T120C

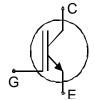
# IGBT Chip in NPT-technology

#### **FEATURES:**

- 1200V NPT technology
- 200µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

# This chip is used for:

• BUP 311D /BUP 212



# **Applications:**

• drives

Chip Type	V <sub>CE</sub>	I <sub>Cn</sub>	Die Size	Package	Ordering Code	
SIGC16T120C	1200V	8A	4.04 x 4 mm <sup>2</sup>	sawn on foil	Q67041-A4673-	
01001011200	1200 V	OΛ	4.04 X 4 111111	Sawii Oii ioii	A003	

#### MECHANICAL PARAMETER:

MECHANICAL PARAMETER:		mm <sup>2</sup>			
Raster size	4.04 x 4				
Area total / active	16.16 / 10.4				
Emitter pad size	1.88x2.18				
Gate pad size	0.71x1.08				
Thickness	200	μm			
Wafer size	150	mm			
Flat position	0	deg			
Max.possible chips per wafer	898 pcs				
Passivation frontside	Photoimide				
Emitter metalization	3200 nm Al Si 1%				
Collector metalization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	Al, ≤500μm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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### **MAXIMUM RATINGS:**

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T <sub>j</sub> =25 °C	V <sub>CE</sub>	1200	V
DC collector current, limited by T <sub>jmax</sub>	I <sub>C</sub>	1)	А
Pulsed collector current, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>cpuls</sub>	24	Α
Gate emitter voltage	V <sub>GE</sub>	±20	V
Operating junction and storage temperature	$T_j$ , $T_{stg}$	-55 <b>+</b> 150	°C

<sup>1)</sup> depending on thermal properties of assembly

# **STATIC CHARACTERISTICS** (tested on chip), $T_i$ =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
Tarameter		Conditions	min.	typ.	max.	01
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE}$ =0 $V$ , $I_{C}$ =500 $\mu$ A	1200			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =8A	2	2.5	3	V
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_C$ =350 $\mu$ A , $V_{GE}$ = $V_{CE}$	4.5	5.5	6.5	
Zero gate voltage collector current	I <sub>CES</sub>	V <sub>CE</sub> =1200V , V <sub>GE</sub> =0V			1	μA
Gate-emitter leakage current	I <sub>GES</sub>	V <sub>CE</sub> =0V , V <sub>GE</sub> =20V			120	nA

# **DYNAMIC CHARACTERISTICS** (tested at component):

Parameter	Symbol	Conditions	Value			Unit
raiailletei			min.	typ.	max.	Joint
Input capacitance	Ciss	V <sub>CE</sub> =25V,	-	600	800	pF
Output capacitance	Coss	$V_{GE}=0V$ ,	-	60	90	
Reverse transfer capacitance	Crss	f=1MHz	-	38	55	

# **SWITCHING CHARACTERISTICS** (tested at component), Inductive Load:

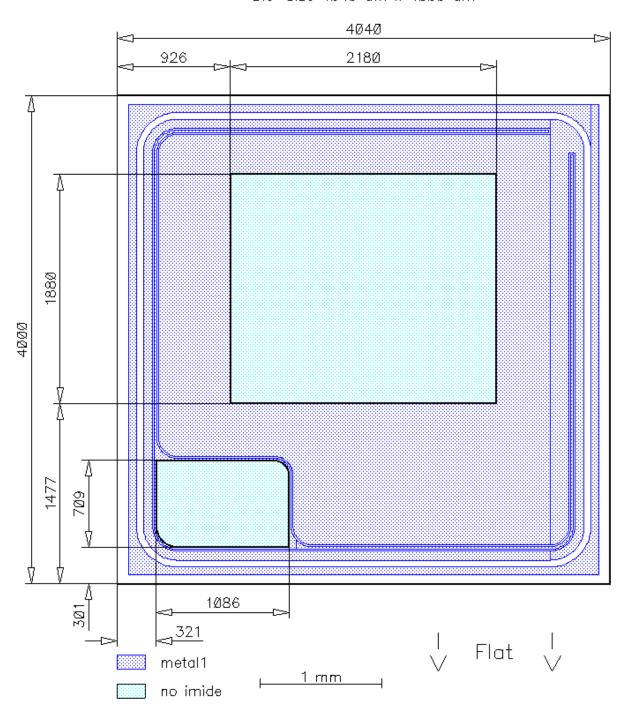
Parameter	Symbol	Conditions 1)	Value			Unit
- arameter	Syllibol		min.	typ.	max.	Oilit
Turn-on delay time	$t_{d(on)}$	T <sub>j</sub> =125°C	1	55	110	ns
Rise time	$t_{\rm r}$	V <sub>CC</sub> =600V, I <sub>C</sub> =8A	-	50	100	
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE}$ =±15V, $R_{\rm G}$ =150 $\Omega$	1	380	570	
Fall time	$t_{f}$	7.6-10022	-	80	120	

<sup>&</sup>lt;sup>1)</sup> values also influenced by parasitic L- and C- in measurement and package.



### **CHIP DRAWING:**

Die-Size 4040 um x 4000 um





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#### **FURTHER ELECTRICAL CHARACTERISTICS:**

This chip data sheet refers to the device data sheet

BUP 311D /BUP 212

Package : TO220

#### **Description:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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