

IGBT³ Chip

FEATURES:

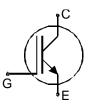
- 600V Trench & Field Stop technology
- low V_{CE(sat)}
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

This chip is used for:

• power module

Applications:

drives



Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC40T60R3	600V	75A	5.74 x 6.96 mm ²	sawn on foil	Q67050- A4347-A101

MECHANICAL PARAMETER:

Raster size	5.74 x 6.96				
Emitter pad size	(2.356 x 2.907) x 4	mm ²			
Gate pad size	1.615 x 0.817				
Area total / active	40 / 30	mm ²			
Thickness	70	μm			
Wafer size	150	mm			
Flat position	90	deg			
Max. possible chips per wafer	334 pcs				
Passivation frontside	Photoimide				
Emitter metallization	3200 nm AlSiCu				
Collector metallization	1400 nm Ni Ag -system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	Al, <500µm				
Reject ink dot size	Ø 0.65mm ; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit	
Collector-emitter voltage, T_j = 25 °C	V _{CE}	600	V	
DC collector current, limited by $T_{j\text{max}}$	I _C	1)	А	
Pulsed collector current, t_p limited by T_{jmax}	I _{cpuls}	225	А	
Gate emitter voltage	V _{GE}	±20	V	
Operating junction and storage temperatur	T _j , T _{stg}	-40 +175	°C	
SC data, $V_{GE} = 15V$, $V_{CC} = 360V$	Tvj = 150°C	tp	6	μs
$G_{C} = 10^{\circ}, V_{C} = 300^{\circ}$	Tvj = 25°C	Ϋ́	8	μ3

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_j =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0V , I _C = 4mA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =75A	1.05	1.45	1.85	V
Gate-emitter threshold voltage	V _{GE(th)}	$I_C{=}1200\mu A$, $V_{GE}{=}V_{CE}$	5.0	5.8	6.5	
Zero gate voltage collector current	I _{CES}	V_{CE} =600V , V_{GE} =0V			3.8	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V , V _{GE} =20V			600	nA
Integrated gate resistor	R _{Gint}			4		Ω

ELECTRICAL CHARACTERISTICS (verified by design/characterization):

Parameter	Symbol	I Conditions	Value			Unit
Falameter	Symbol		min.	typ.	max.	Unit
Input capacitance	C _{iss}	V _{CE} =25V,		4700		pF
Output capacitance	Coss	$V_{\rm GE}=0V$,		300		
Reverse transfer capacitance	Crss	f=1MHz		145		1

SWITCHING CHARACTERISTICS (verified by design/characterization), inductive load

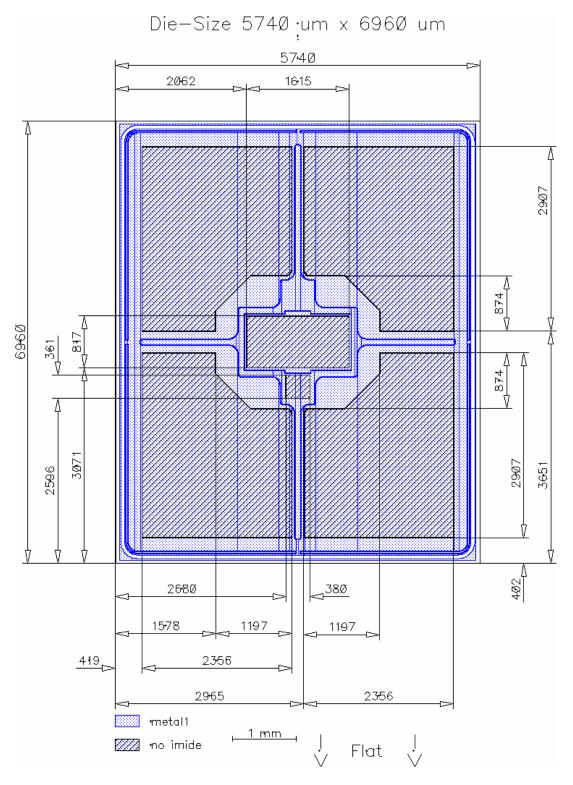
Parameter	Symbol	Conditions	Value ²⁾			Unit
			min.	typ.	max.	Unit
Turn-on delay time	t _{d(on)}	<i>T</i> _j =125°C		25		ns
Rise time	t _r	$V_{CC} = 300V,$		18		
Turn-off delay time	t _{d(off)}	I _C =75A, V _{GE} =-15/15V,		210		
Fall time	t _f	$R_{\rm G}$ = 1.2 Ω		60		

²⁾ values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the	
device data sheet	

DESCRIPTION:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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