

IGBT³ Chip

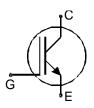
SIGC41T120R3

FEATURES:

- 1200V Trench + Field Stop technology
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

This chip is used for:

- power module
- **Applications:**
- drives



Chip Type	V _{CE}	I Cn	Die Size	Package	Ordering Code
SIGC41T120R3	1200V	35A	6.5 x 6.37 mm ²	sawn on foil	Q67050- A4105-A001

MECHANICAL PARAMETER:

Raster size	6.5 x 6.37	mm			
Emitter pad size	4.992 x 4.898				
Gate pad size	1.139 x 1.139				
Area total / active	41.4 / 30.1	mm ²			
Thickness	140	μm			
Wafer size	150	mm			
Flat position	180	grd			
Max.possible chips per wafer	350 pcs				
Passivation frontside	n frontside Photoimide				
Emitter metallization	3200 nm AlSiCu				
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	Al, <500µm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, Tj=25 °C	V _{CE}	1200	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t_p limited by T_{jmax}	I _{cpuls}	105	А
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T _j , T _{stg}	-55 +150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_j =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
	Cymbol		min.	typ.	max.	0.110
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0V , I _C = 1.5mA	1200			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =35A	1.4	1.7	2.1	V
Gate-emitter threshold voltage	V _{GE(th)}	I_C =1.5mA , V_{GE} = V_{CE}	5.0	5.8	6.5	
Zero gate voltage collector current	I _{CES}	V_{CE} =1200V , V_{GE} =0V			4.8	μA
Gate-emitter leakage current	I _{GES}	V_{CE} =0V , V_{GE} =20V			600	nA
Integrated gate resistor	R _{Gint}			6		Ω

ELECTRICAL CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
Falameter	Symbol		min.	typ.	max.	
Input capacitance	Ciss	V _{CE} =25V,		2530		pF
Output capacitance	Coss	$V_{GE}=0V$,		132		
Reverse transfer capacitance	Crss	f=1MHz		115		

SWITCHING CHARACTERISTICS (tested at component), Inductive Load

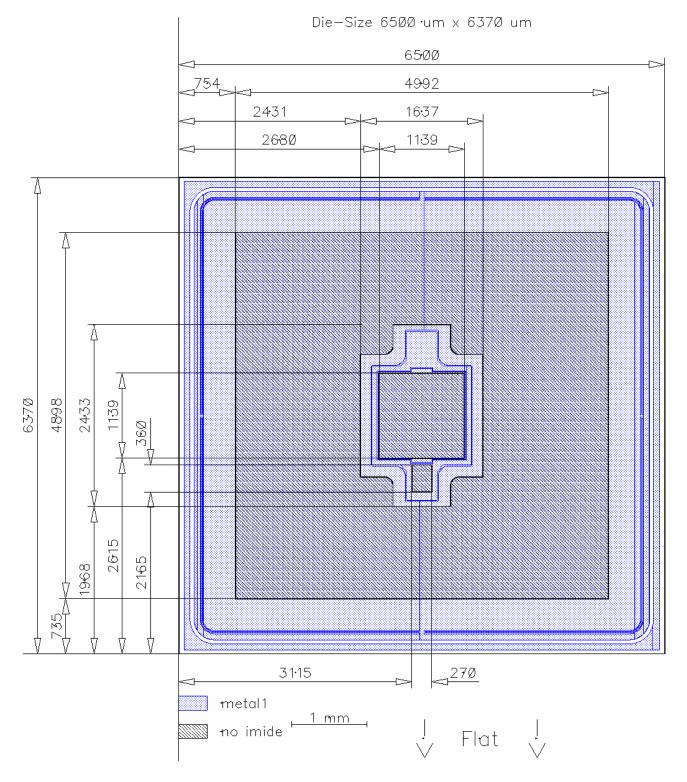
Parameter	Symbol	Conditions ¹⁾	Value			Unit
	Symbol		min.	typ.	max.	
Turn-on delay time	t _{d(on)}	$T_{\rm j}$ =125°C		90		ns
Rise time	<i>t</i> r	V _{CC} =600V, I _C =35A, V _{GE} =-15/15V,		45		
Turn-off delay time	$t_{d(off)}$	V _{GE} =-15/15V,		520		
Fall time	t _f	R _G = 27Ω		90		

¹⁾ values also influenced by parasitic L- and C- in measurement and package.





CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the	tbd	
device data sheet	20	

DESCRIPTION:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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