

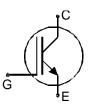
IGBT³ Chip

FEATURES:

- 600V Trench & Field Stop technology
- low V_{CE(sat)}
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

This chip is used for:

- power module
- Applications:
- drives



Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC54T60R3	600V	100A	5.97 x 8.97 mm ²	sawn on foil	Q67050- A4341-A101

MECHANICAL PARAMETER:

Raster size	5.97 x 8.97			
Emitter pad size	(2.489 x 1.767) x 4 (2.789 x 1.995) x 4	mm ²		
Gate pad size	1.615 x 0.817			
Area total / active	53.6 / 40			
Thickness	70	μm		
Wafer size	150	mm		
Flat position	90	deg		
Max. possible chips per wafer	245 pcs			
Passivation frontside	Photoimide			
Emitter metallization	3200 nm AlSiCu			
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solder			
Wire bond	Al, <500µm			
Reject ink dot size	Ø 0.65mm ; max 1.2mm			
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C			



MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit	
Collector-emitter voltage, Tj=25 °C	V _{CE}	600	V	
DC collector current, limited by T _{jmax}	I _C	1)	А	
Pulsed collector current, t_p limited by T_{jmax}	<i>I</i> _{cpuls}	300	А	
Gate emitter voltage	V _{GE}	±20	V	
Operating junction and storage temperature	T_{j} , T_{stg}	-40 +175	°C	
SC data, $V_{GE} = 15V$, $V_{CC} = 360V$, $Tvj = 150$ °C	<i>t</i> p	5	μs	

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_j =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0V , I _C = 4mA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V_{GE} =15V, I _C =100A	1.05	1.45	1.85	V
Gate-emitter threshold voltage	V _{GE(th)}	$I_C{=}1600\mu\text{A}$, $V_{GE}{=}V_{CE}$	tbd	5.8	tbd	
Zero gate voltage collector current	I _{CES}	V_{CE} =600V , V_{GE} =0V			270	μA
Gate-emitter leakage current	I _{GES}	$V_{CE}=0V$, $V_{GE}=20V$			600	nA
Integrated gate resistor	R _{Gint}			2		Ω

ELECTRICAL CHARACTERISTICS (verified by design/characterization):

Parameter	Symbol	Conditions	Value			Unit
Falameter			min.	typ.	max.	
Input capacitance	C _{iss}	V _{CE} =25V,		tbd		nF
Output capacitance	Coss	$V_{GE}=0V$,		tbd		
Reverse transfer capacitance	Crss	f=1MHz		tbd		

SWITCHING CHARACTERISTICS (verified by design/characterization), inductive load

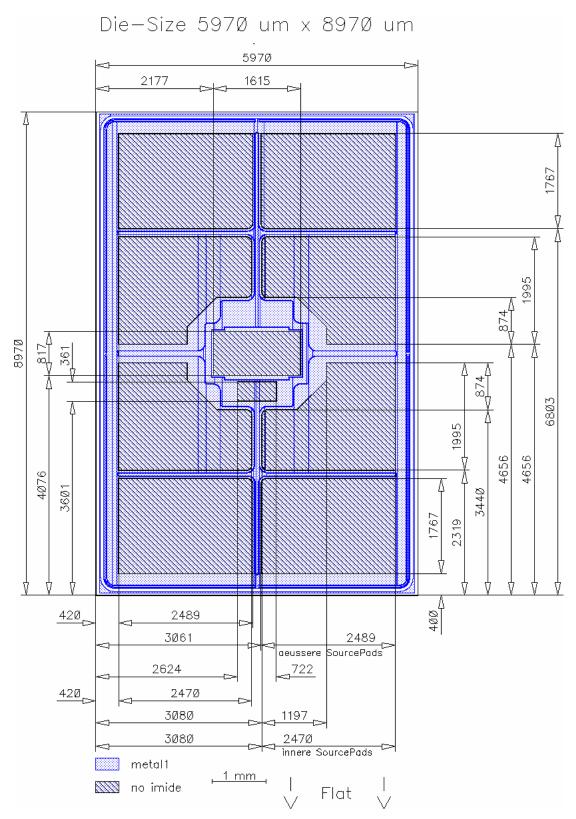
Parameter	Symbol	Conditions	Value ²⁾			Unit
Falameter			min.	typ.	max.	
Turn-on delay time	t _{d(on)}	<i>T</i> _j =125°C		tbd		ns
Rise time	t _r	$V_{\rm CC} = 300 V$,		tbd]
Turn-off delay time	$t_{d(off)}$	/ _C =100A, / _{GE} =-15/15V,		tbd		
Fall time	t _f	$R_{\rm G}$ = tbd Ω		tbd		

²⁾ values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:



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FURTHER ELECTRICAL CHARACTERISTICS:

DESCRIPTION:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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