

# SJTP20N60A

## N-Channel MOSFET

#### **Applications:**

- Adaptor
- Charger
- .SMPS

#### Features:

- RoHS Compliant
- . Low ON Resistance
- . Low Gate Charge
- Peak Current vs Pulse Width Curve
- Inductive Switching Curves

#### **Ordering Information**

PART NUMBER	KAGE BRAND							
SJTP20N60A	TO-220	IPS						

#### Absolute Maximum Ratings

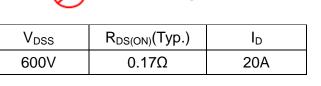
### $T_C=25^{\circ}C$ unless otherwise specified

Pb)

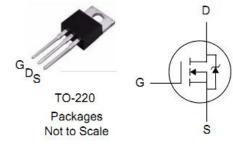
Symbol	Parameter	SJTP20N60A	Units
V <sub>DSS</sub>	Drain-to-Source Voltage	600	V
I <sub>D</sub>	Continuous Drain Current	20	А
I <sub>DM</sub>	Pulsed Drain Current, V <sub>GS</sub> @10V	60	А
D	Power Dissipation	208	W
P <sub>D</sub>	Derating Factor above 25°C	1.6	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	±30	V
E <sub>AS</sub>	Single Pulse Avalanche Energy	500	mJ
E <sub>AR</sub>	Avalanche Energy ,Repetitive	1	mJ
I <sub>AR</sub>	Avalanche Current	20	А
TL	Maximum Temperature for Soldering	300	
$T_{\rm J}$ and $T_{\rm STG}$	Operating Junction and Storage Temperature Range	-55 to150	Ĉ

#### Thermal Resistance

Symbol	Parameter	Тур.	Max.	Units	Test Conditions
$R_{ extsf{ heta}JC}$	Junction-to-Case		0.6	°C⁄W	Water cooled heatsink, $P_D$ adjusted for a peak junction temperature of +150 $^{\circ}$ C.
R <sub>0JA</sub>	Junction-to-Ambient		62		1 cubic foot chamber, free air.



Lead Free Package and Finish





Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	600			V℃	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA
I <sub>DSS</sub>	Drain-to-Source Leakage Current			1	- μΑ	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V T <sub>J</sub> =25℃
				100		V <sub>DS</sub> =600V, V <sub>GS</sub> =0V TJ=125℃
I <sub>GSS</sub>	Gate-to-Source Forward Leakage		+100	m A	V <sub>GS</sub> =+30V	
	Gate-to-Source Reverse Leakage			-100	- nA	V <sub>GS</sub> = -30V

#### **OFF Characteristics** $T_C=25^{\circ}C$ unless otherwise specified

#### **ON Characteristics** $T_J=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
R <sub>DS(ON)</sub>	Static Drain-to-Source On-Resistance		0.17	0.19	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =10A (NOTE *4)
V <sub>GS(TH)</sub>	Gate Threshold Voltage	2.5		3.5	V	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$
<b>g</b> <sub>fs</sub>	Forward Transconductance		18.8		S	V <sub>DS</sub> =10V, I <sub>D</sub> =20A (NOTE *4)

#### Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Ма	Units	Test Conditions
				х.		
C <sub>iss</sub>	Input Capacitance		2140		pF	$V_{GS}$ = 0V, $V_{DS}$ = 50V f =1.0MHz
C <sub>oss</sub>	Output Capacitance		300			
C <sub>rss</sub>	Reverse Transfer Capacitance		18			
Qg	Total Gate Charge		54		nC	
Q <sub>gs</sub>	Gate-to-Source Charge		10			$I_{D}=20A, V_{DD}=480V$ $V_{GS}=10V$
Q <sub>gd</sub>	Gate-to-Drain ("Miller") Charge		20			$V_{GS} = 10V$

#### **Resistive Switching Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
t <sub>d(ON)</sub>	Turn-on Delay Time		48	104		
t <sub>rise</sub>	Rise Time		108	220	ns	$V_{DD}$ =300V, $I_{D}$ =20A, $V_{G}$ =10V $R_{G}$ =25 $\Omega$
t <sub>d(OFF)</sub>	Turn-Off Delay Time		176	360		
t <sub>fall</sub>	Fall Time		50	108		



Oource-Dra							
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
I <sub>S</sub>	Continuous Source Current (Body Diode)			20	А	<b>ب</b> مدين	
I <sub>SM</sub>	Maximum Pulsed Current (Body Diode)			60	А	− T <sub>C</sub> =25℃	
V <sub>SD</sub>	Diode Forward Voltage		0.95	1.2	V	I <sub>SD</sub> =20A, V <sub>GS</sub> =0V	
t <sub>rr</sub>	Reverse Recovery Time		440		ns	I <sub>S</sub> =20A,	
Q <sub>rr</sub>	Reverse Recovery Charge		5		uC	di/dt=100A/us	

#### **Source-Drain Diode Characteristics** Tc=25°C unless otherwise specified

Notes:

\*1.  $T_J = +25^{\circ}C$  to  $+150^{\circ}C$ .

\*2. Repetitive rating; pulse width limited by maximum junction temperature.

\*3. di/dt < 100 A/µs,  $V_{\text{DD}}$  <  $BV_{\text{DSS}},$   $T_{\text{J}}\text{=+150}^\circ\text{C}$  .

\*4. Pulse width <  $380\mu$ s; duty cycle < 2%.



#### Disclaimers:

InPower Semiconductor Co., Ltd (IPS) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to IPS's terms and conditions supplied at the time of order acknowledgement.

InPower Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing reliability and quality control are used to the extent IPS deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

InPower Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using IPS's components. To minimize risk, customers must provide adequate design and operating safeguards.

InPower Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in IPS's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. InPower Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

Resale of IPS's products with statements different from or beyond the parameters stated by InPower Semiconductor Co., Ltd for that product or service voids all express or implied warrantees for the associated IPS's product or service and is unfair and deceptive business practice. InPower Semiconductor Co., Ltd is not responsible or liable for any such statements.

#### Life Support Policy:

InPower Semiconductor Co., Ltd's products are not authorized for use as critical components in life support devices or systems without the expressed written approval of InPower Semiconductor Co., Ltd.

As used herein:

- 1. Life support devices or systems are devices or systems which:
  - a. are intended for surgical implant into the human body,
  - b. support or sustain life,
  - c. whose failure to perform when properly used in accordance with instructions for used provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.