SK15AS

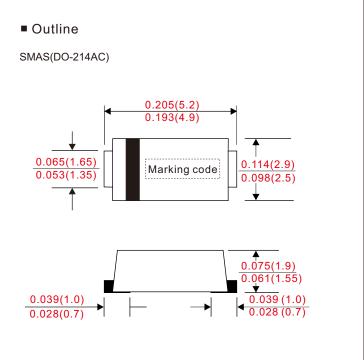
1A Surface Mount Schottky Barrier Rectifiers

Features

- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex.SK15ASG.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMAS
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : Approximated 0.08 gram



Dimensions in inches and (millimeters)

Maximum ratings and electrical characteristics

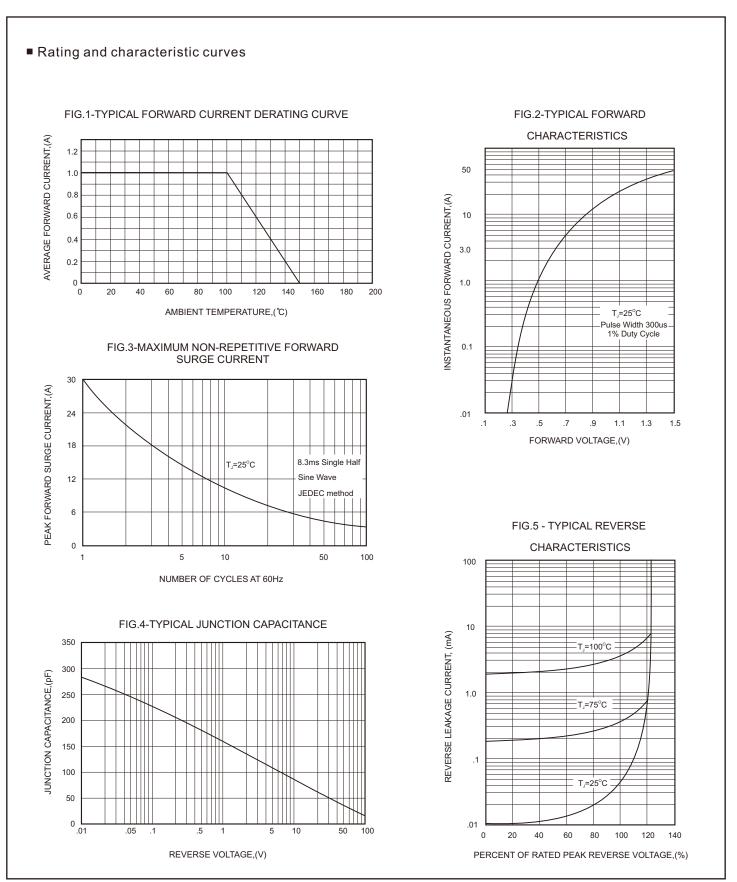
Rating at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter			Conditions			Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current			See Fig.1			I _o			1.0	А
Forward surge current			8.3ms single half sine-wave superimposed on rate load (JEDEC method)			I _{FSM}			30	А
Reverse current			$V_{R} = V_{RRM} T_{A} = 25^{\circ}C$			- I _R -			0.5	mA
			$V_{\rm R} = V_{\rm RRM} T_{\rm A} = 100^{\circ} \rm C$						10	
Diode junction capacitance			f=1MHz and applied 4V DC reverse voltage			C		120		pF
Thermal resistance			Junction to ambient			$R_{_{\theta JA}}$		88		°C/W
Storage temperature						T _{stg}	-55		+150	°C
		Max.		Mar 50		f		0.5		
Symbol	Marking code	Max. repetitive peal reverse voltag V _{RRM} (V)		Max. DC blocking voltage V _R (V)		forward v I A, T _A = 2 V _F (V)		Op	erating Ju temperat T, (°C)	ure



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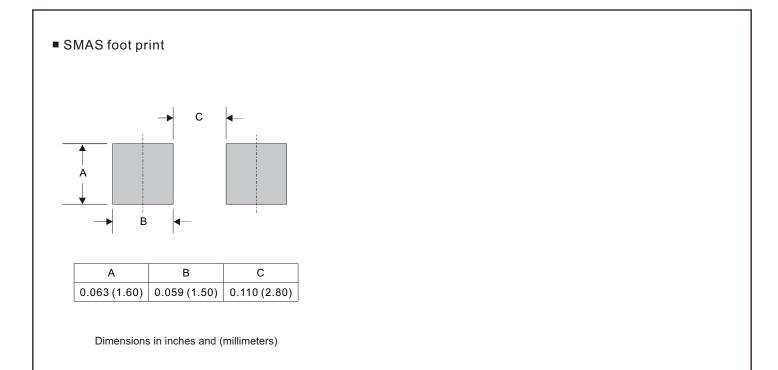


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